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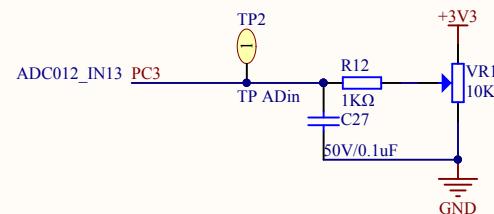
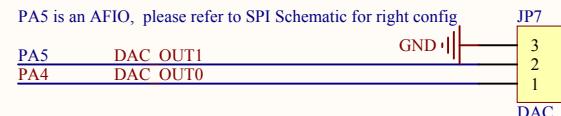
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ADC**DAC****Company Name: GigaDevice****File Name: AD_DA**

Revision: 1.0

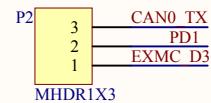
Data: 2013-8-20

Author: XuFei

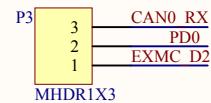
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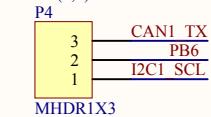
Short P2(1,2) for EXMC function
Short P2(2,3) for CAN0 function



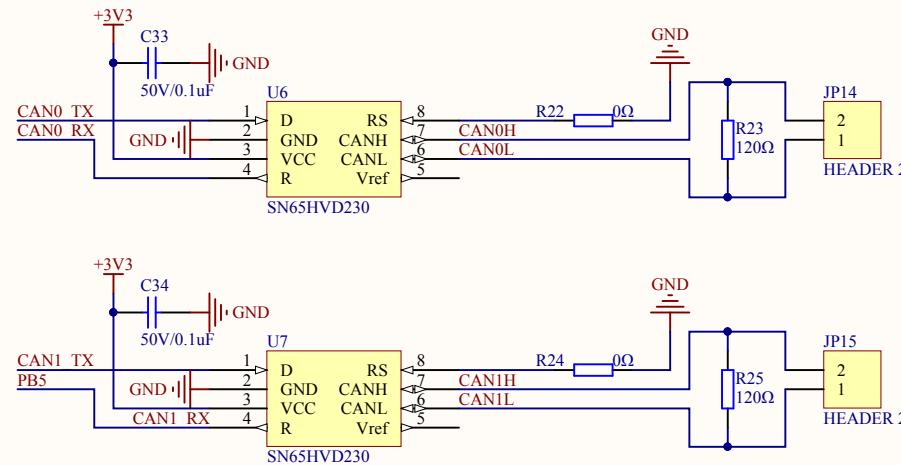
Short P3(1,2) for EXMC function
Short P3(2,3) for CAN0 function



Short P4(1,2) for I2C1 function
Short P4(2,3) for CAN1 function



CAN



Company Name: GigaDevice

File Name: CAN

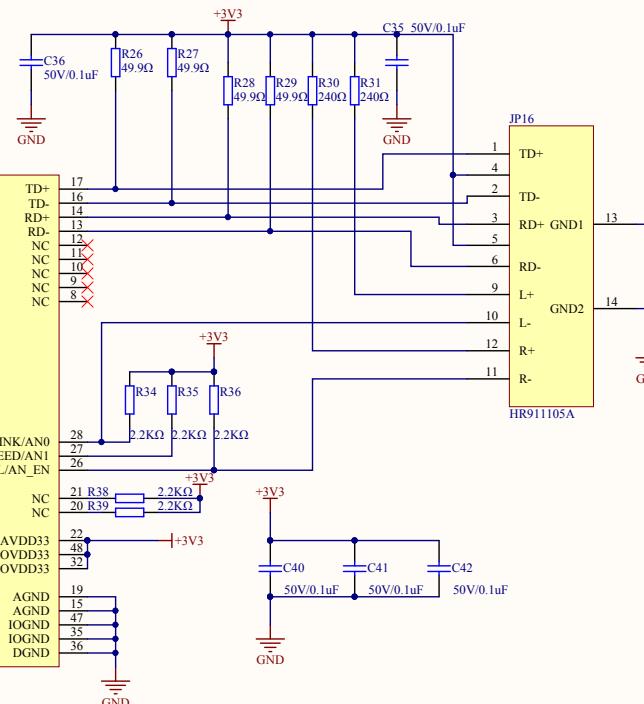
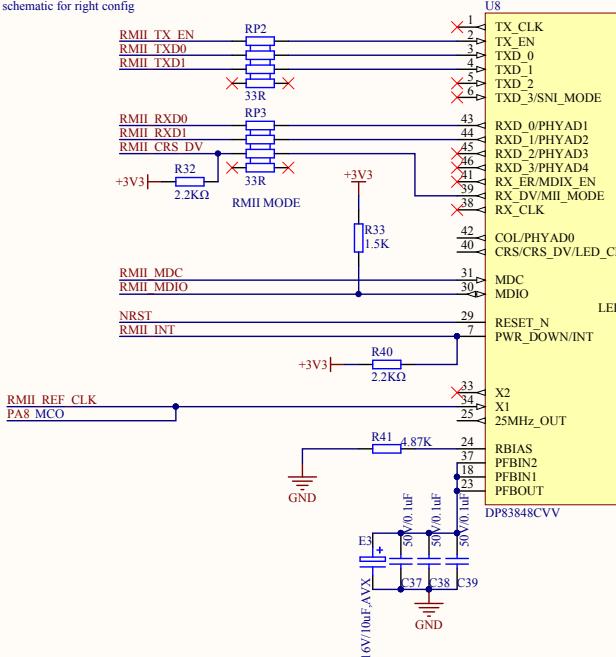
Revision: 1.0

Data: 2013-7-24

Author: XuFei

PB12 and PB13 are AFIOs, refer to I2S schematic for right config
 PA2 is an AFIO, refer to USART schematic for right config
 PA7 is an AFIO, refer to SPI schematic for right config

PB11 RMII TX_EN
 PB12 RMII TXD0
 PB13 RMII TXD1
 PC4 RMII RXD0
 PC5 RMII RXD1
 PA7 RMII CRS_DV
 PC1 RMII MDC
 PA2 RMII MDIO
 PB0 RMII INT
 PA1 RMII REF_CLK



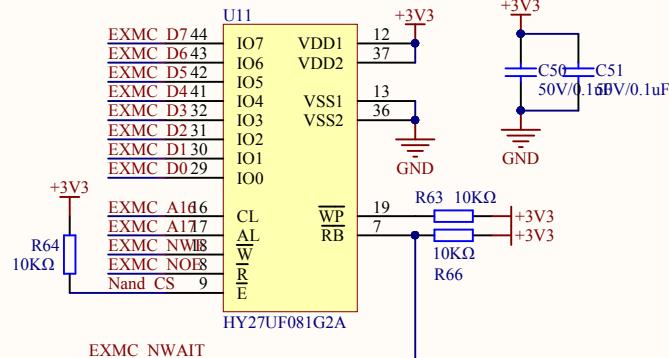
Company Name: GigaDevice

File Name: Ethernet

Revision: 1.0 | Data: 2013-8-21 | Author: Xufei

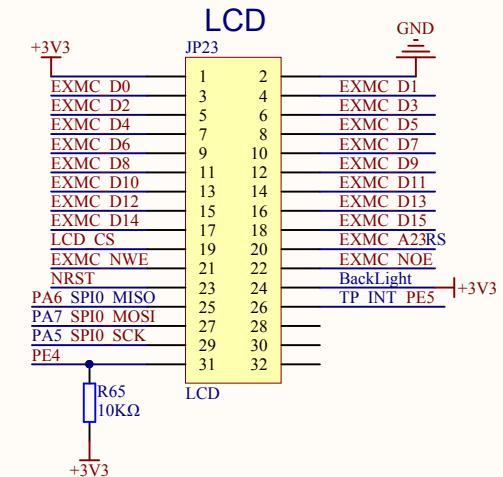
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Nand Flash

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PD0,PD1 are AFIOs, please refer to CAN schematic for right config

PD14	EXMC_D0	PD11	EXMC_A16
PD15	EXMC_D1	PD12	EXMC_A17
PD0	EXMC_D2	PE2	EXMC_A23
PD1	EXMC_D3	PD4	EXMC_NOE
PE7	EXMC_D4	PD5	EXMC_NWE
PE8	EXMC_D5	PD6	EXMC_NWAIT
PE9	EXMC_D6		
PE10	EXMC_D7		
PE11	EXMC_D8		
PE12	EXMC_D9		
PE13	EXMC_D10		
PE14	EXMC_D11		
PE15	EXMC_D12		
PD8	EXMC_D13		
PD9	EXMC_D14		
PD10	EXMC_D15		

D

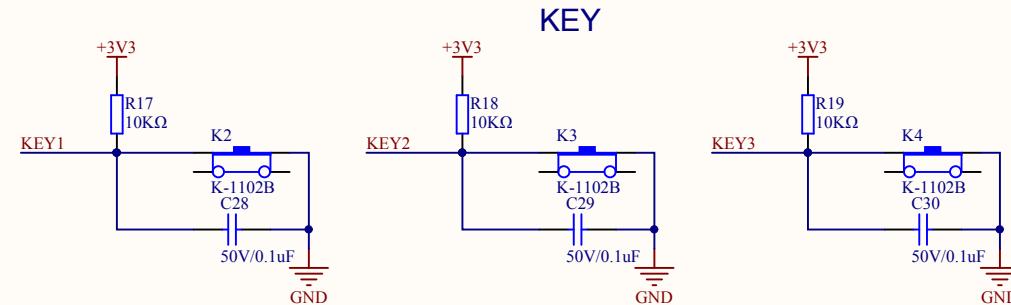
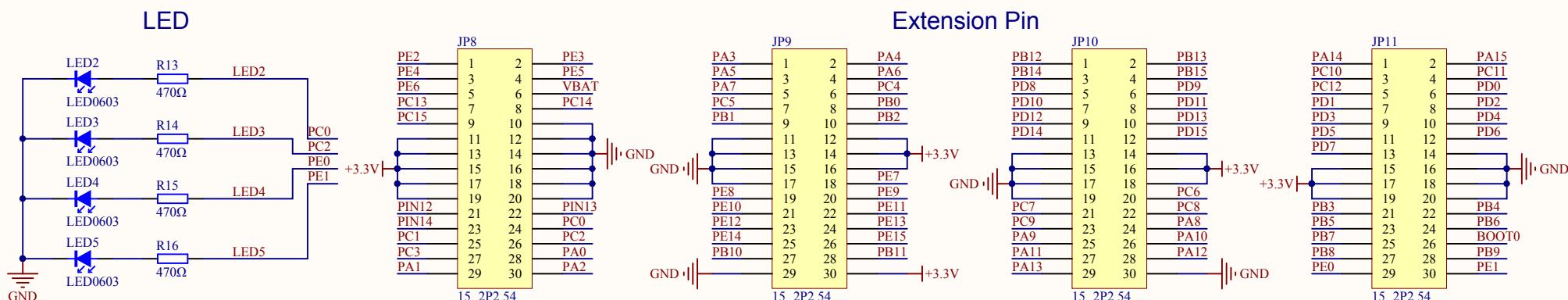
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Company Name: GigaDevice**File Name: EXMC**

Revision: 1.0

Data: 2013-8-20

Author: XuFei

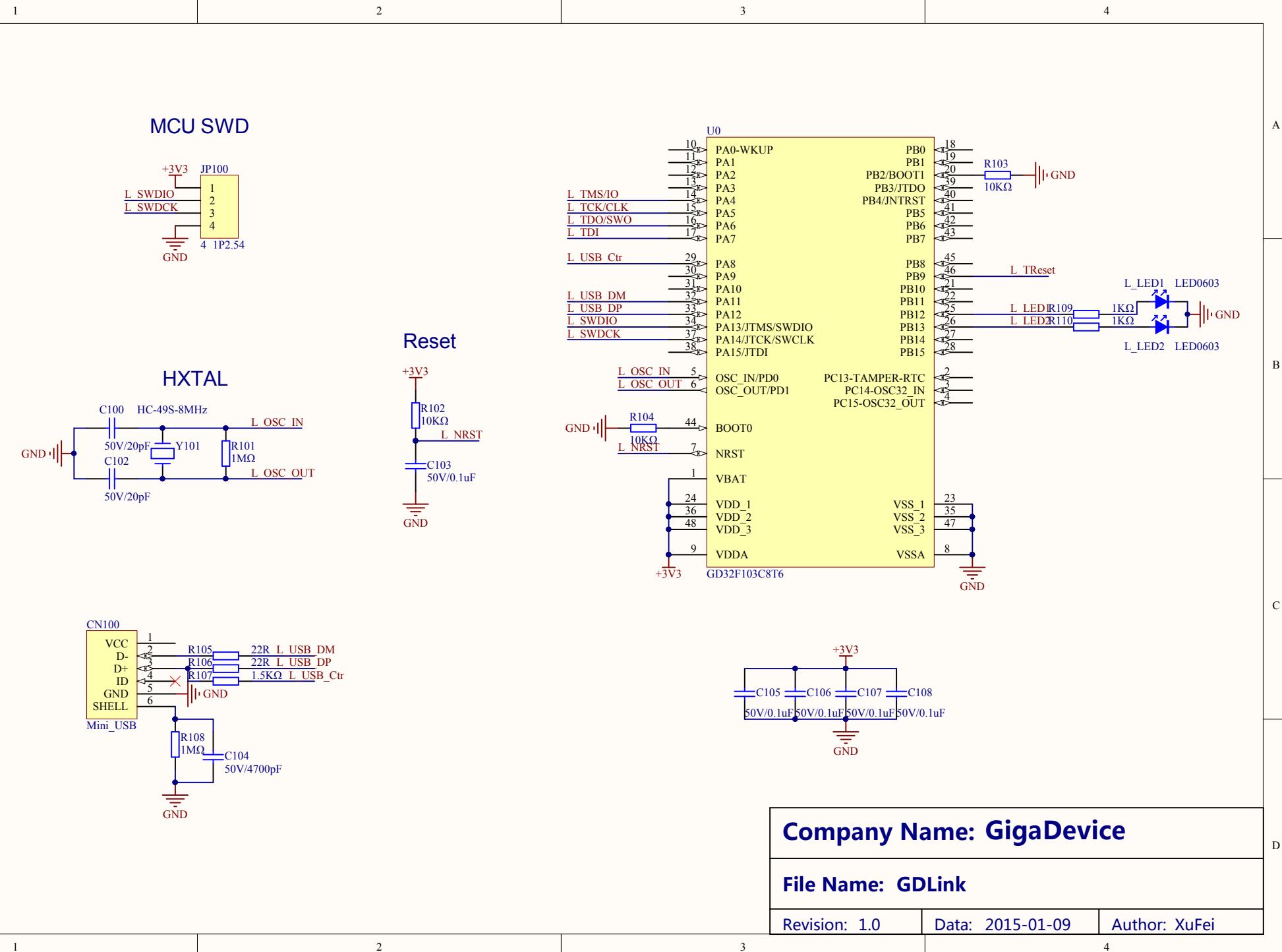


PA0 KEY1
 PC13 KEY2
 PB14 KEY3

Company Name: GigaDevice

File Name: Extension

Revision: 1.0 Data: 2013-8-20 Author: XuFei



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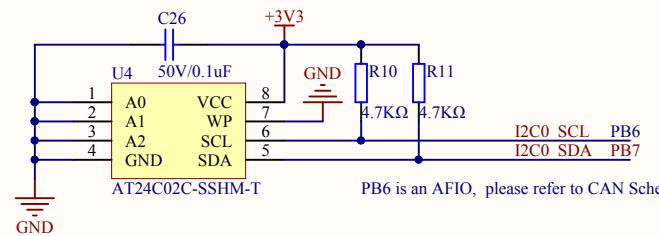
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I2C



Company Name: GigaDevice

File Name: I2C_SMbus

Revision: 1.0

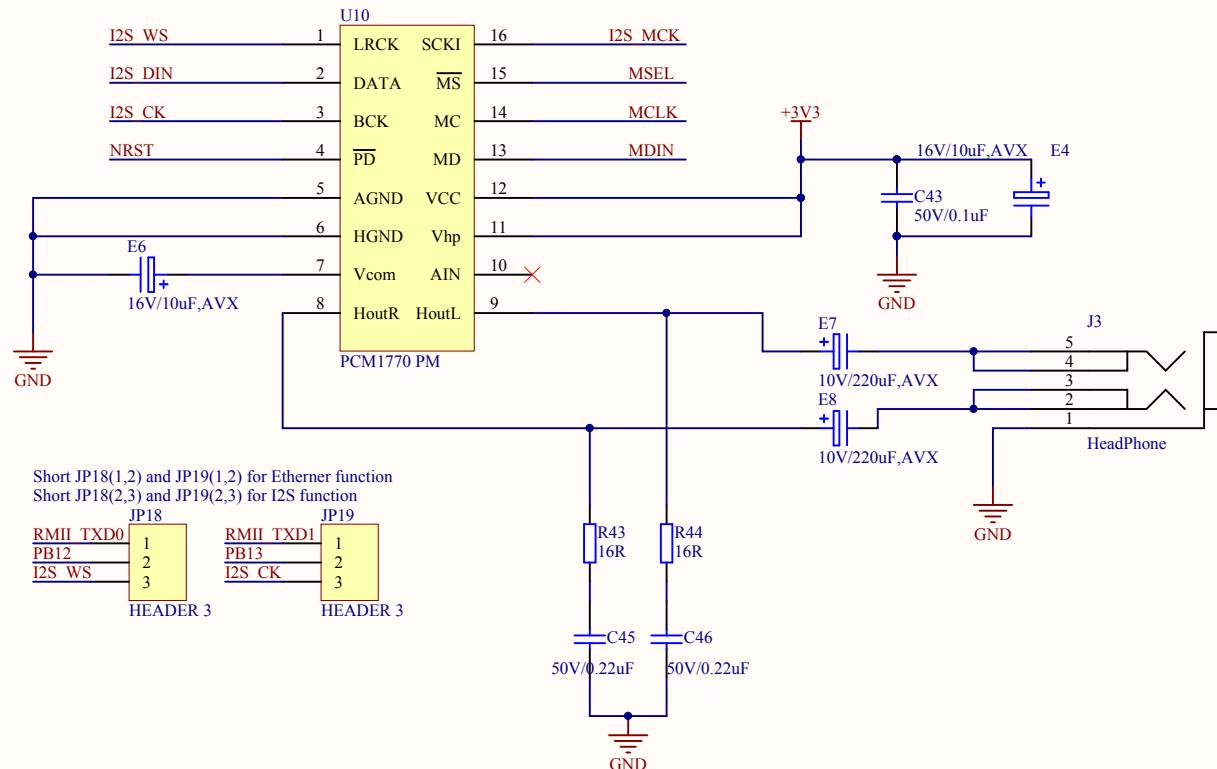
Data: 2013-8-20

Author: XuFei

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PC6 I2S MCK
 PB15 I2S DIN
 PA7 SPI0 MOSI MDIN
 SPI0 SCK MCLK
 PA4 MSEL



Company Name: GigaDevice

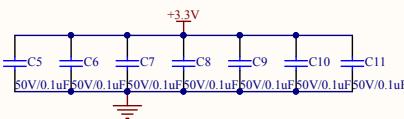
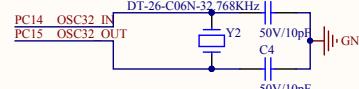
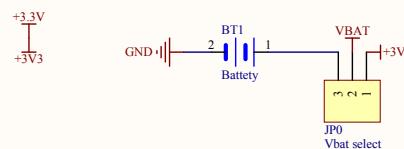
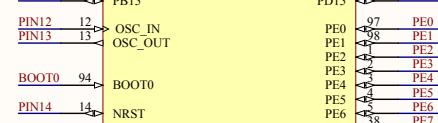
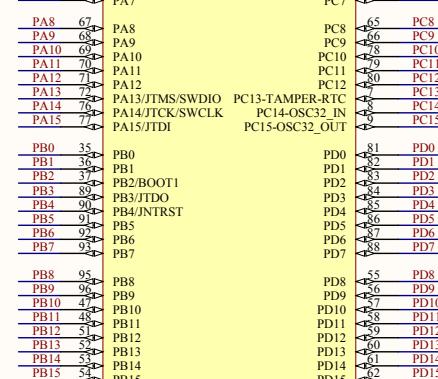
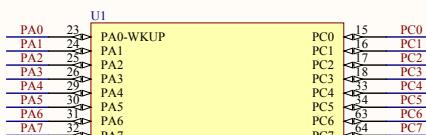
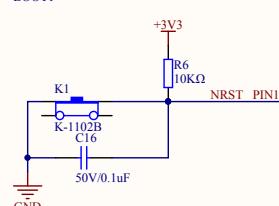
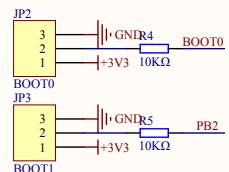
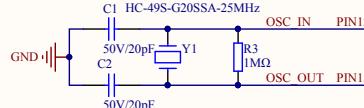
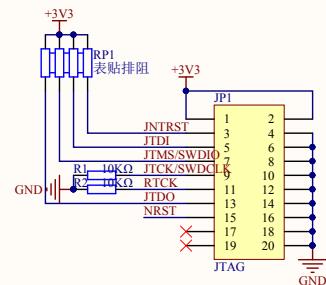
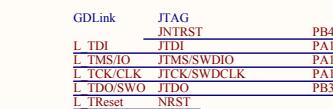
File Name: I2S

Revision: 1.0

Data: 2013-7-23

Author: Lixin

A



Company Name: GigaDevice

File Name: MCU

Revision: 1.0

Data: 2013-8-20

Author: XuFei

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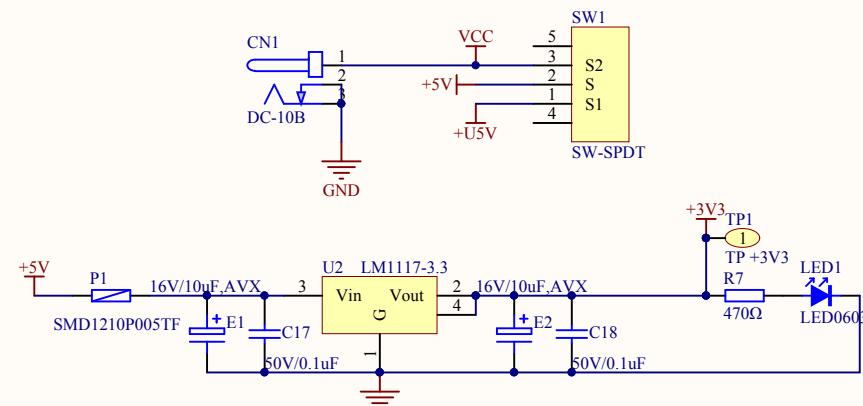
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Company Name: GigaDevice

File Name: Power

Revision: 1.0

Data: 2013-8-20

Author: XuFei

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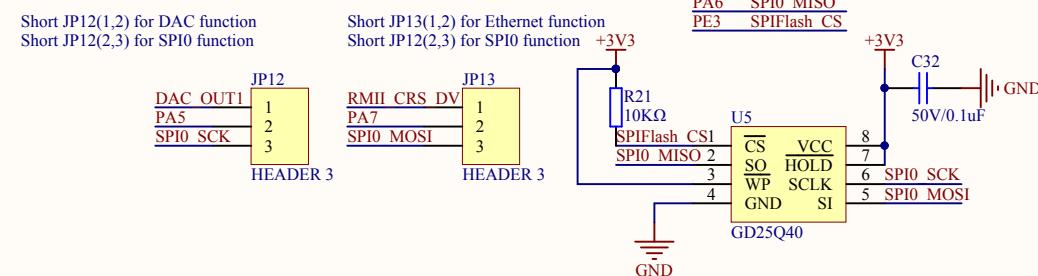
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SPI Flash



Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2013-8-20

Author: XuFei

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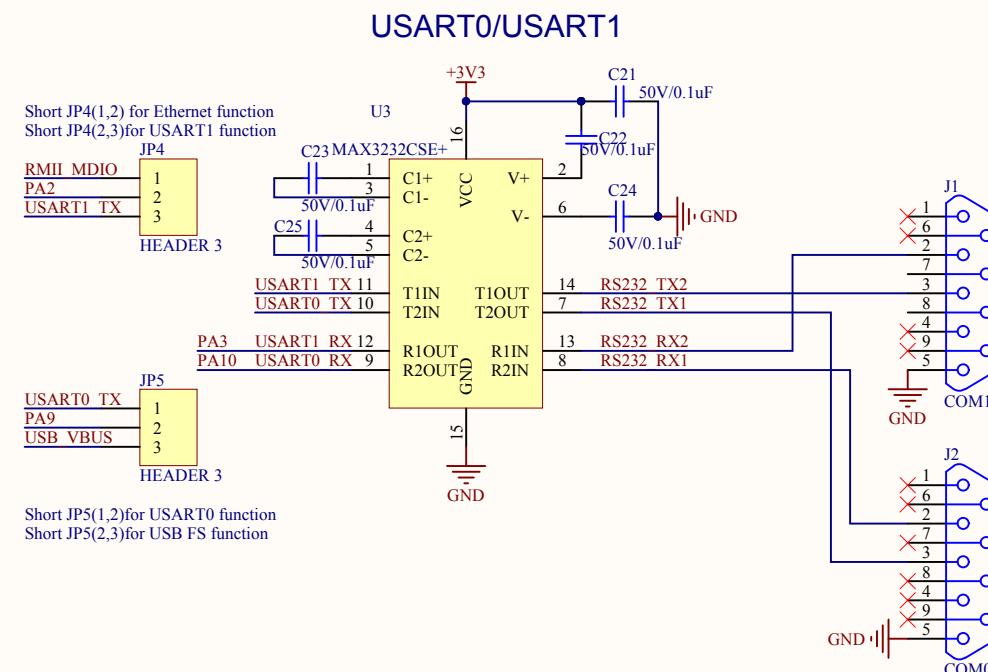
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Company Name: GigaDevice

File Name: USART

Revision: 1.0

Data: 2013-8-20

Author: XuFei

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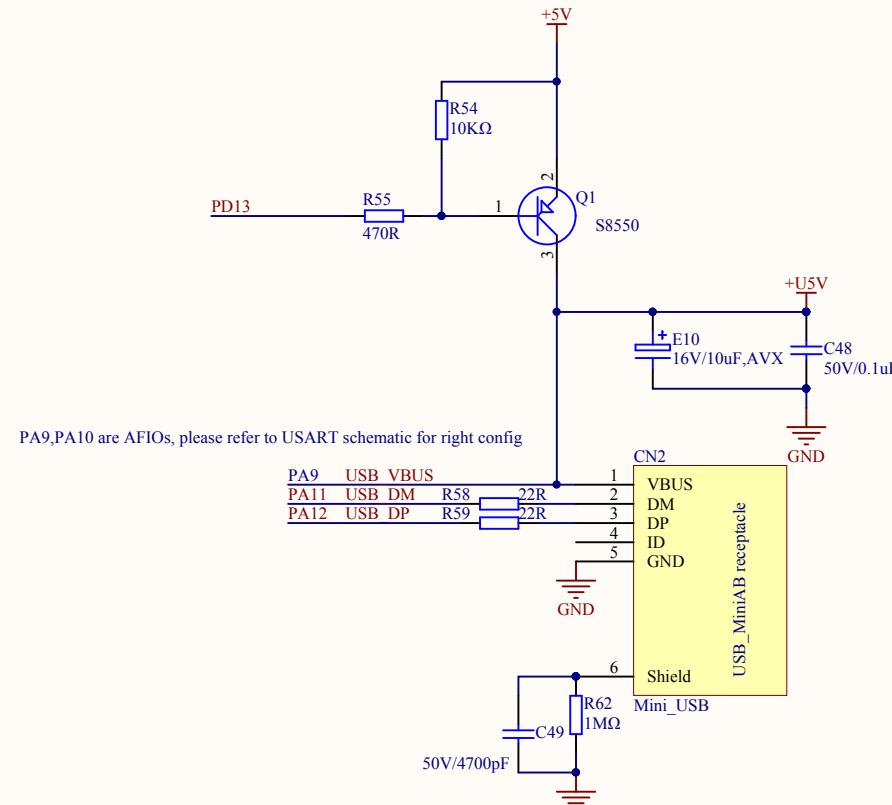
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Company Name: GigaDevice			
File Name: USBFS			
Revision: 1.1	Data: 2013.9.10	Author: Xufei	

