

A

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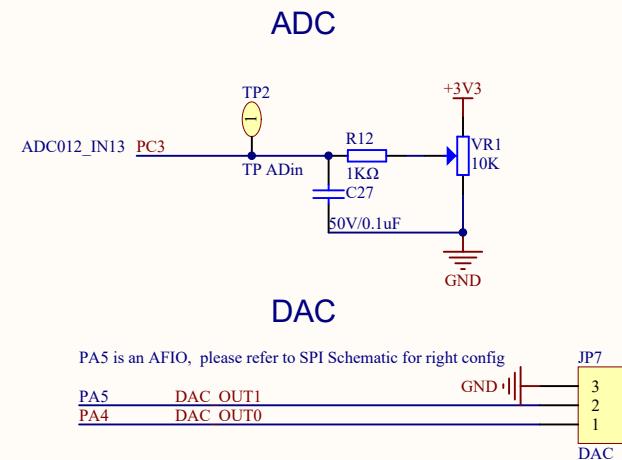
B

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D



Company Name: GigaDevice		
File Name: AD_DA		
Revision: 1.0	Data: 2018-1	Author: XuFei

A

A

B

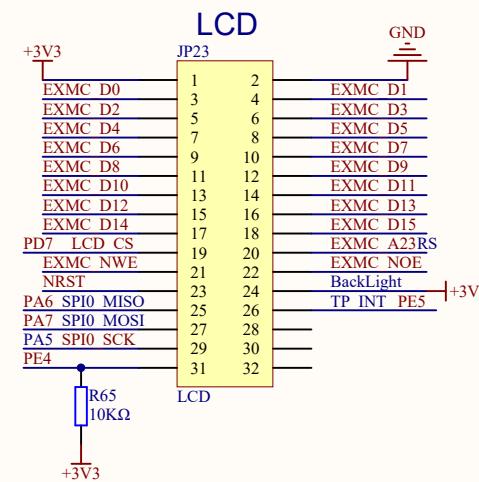
B

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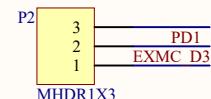
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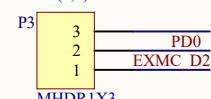
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PD15	EXMC D1
PD0	EXMC D2
PD1	EXMC D3
PE7	EXMC D4
PE8	EXMC D5
PE9	EXMC D6
PE10	EXMC D7
PE11	EXMC D8
PE12	EXMC D9
PE13	EXMC D10
PE14	EXMC D11
PE15	EXMC D12
PD8	EXMC D13
PD9	EXMC D14
PD10	EXMC D15

PE2	EXMC A23
PD4	EXMC NOE
PD5	EXMC NWE

Short P2(1,2) for EXMC function



Short P3(1,2) for EXMC function



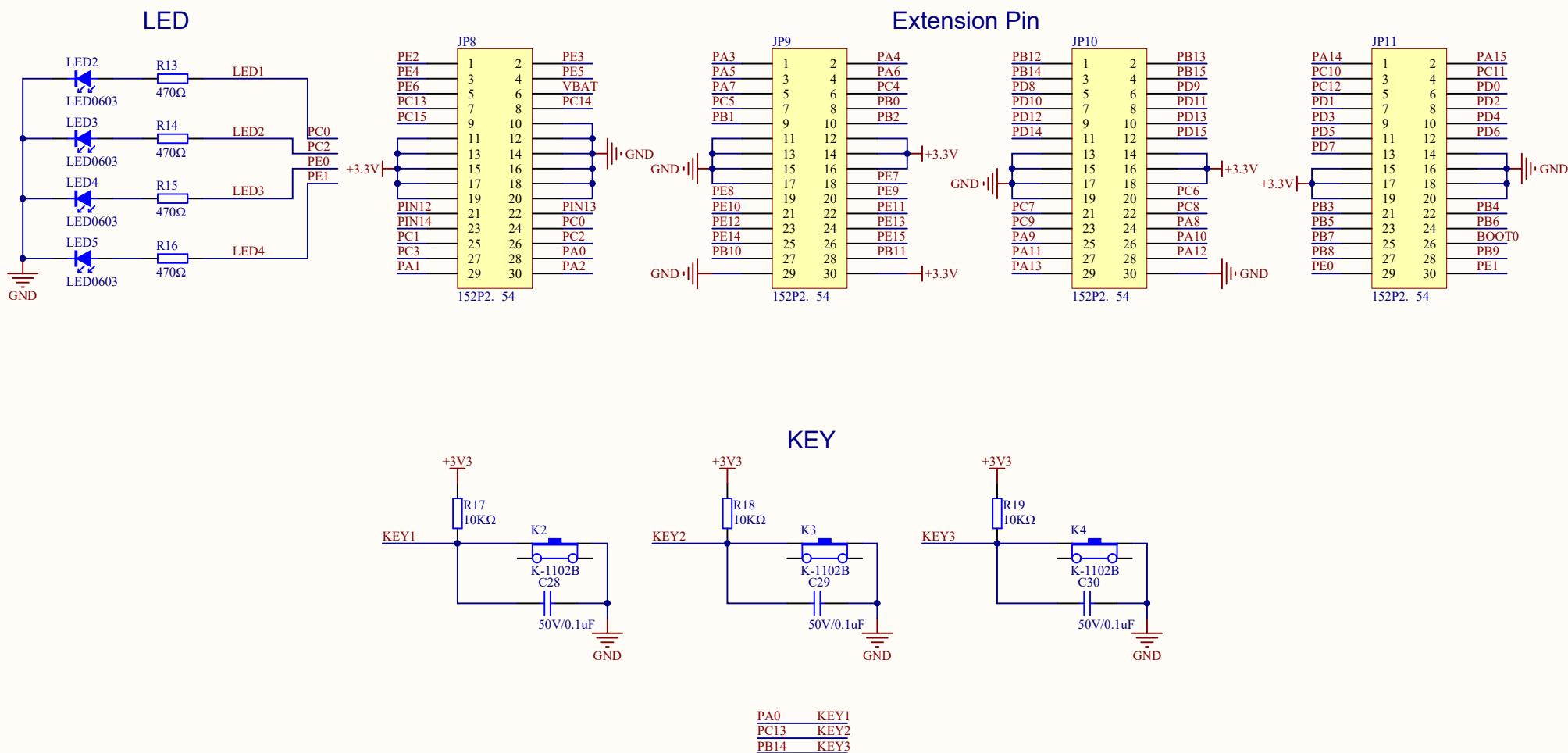
Company Name: GigaDevice

File Name: EXMC

Revision: 1.0

Data: 2018-1

Author: XuFei



Company Name: **GigaDevice**

File Name: **Extension**

Revision: 1.0

Data: 2018-1

Author: XuFei

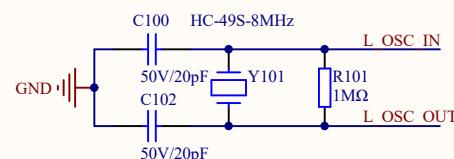
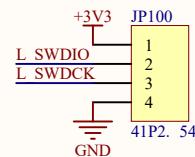
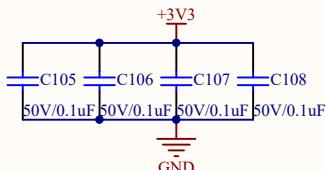
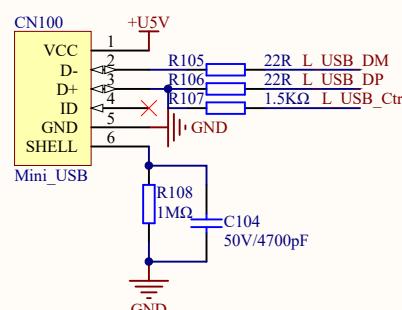
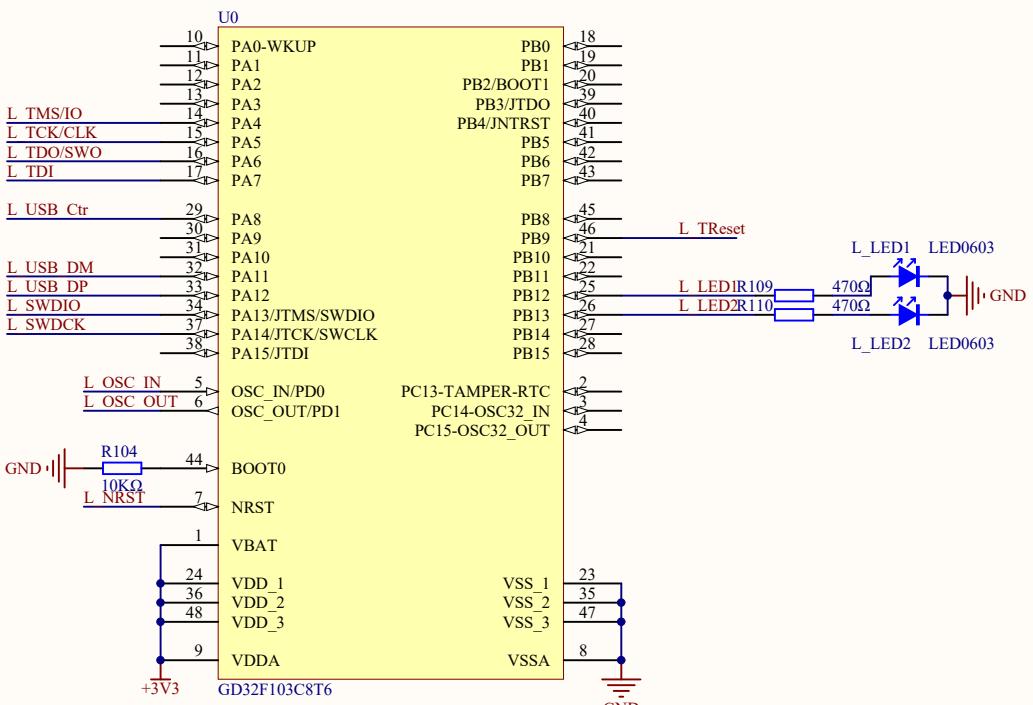
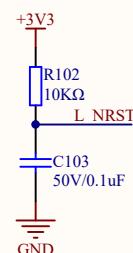
1

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A

MCU SWD**Reset****Company Name: GigaDevice****File Name: GDLink**

Revision: 1.0

Data: 2016-03

Author: XuFei

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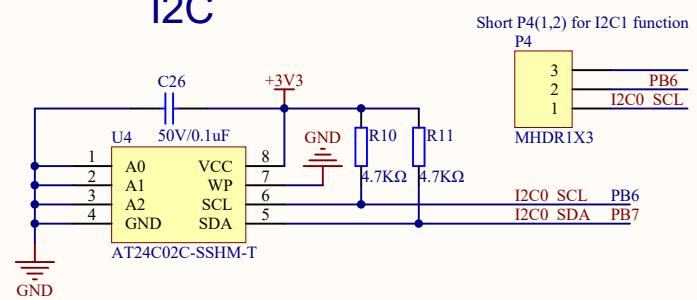
B

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I2C**Company Name: GigaDevice****File Name: I2C**

Revision: 1.0

Data: 2018-1

Author: XuFei

A

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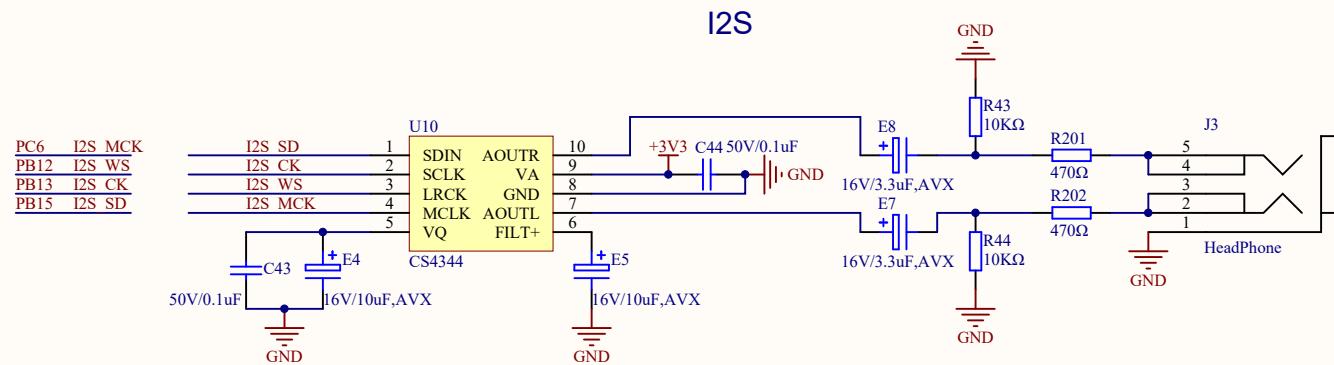
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Company Name: GigaDevice

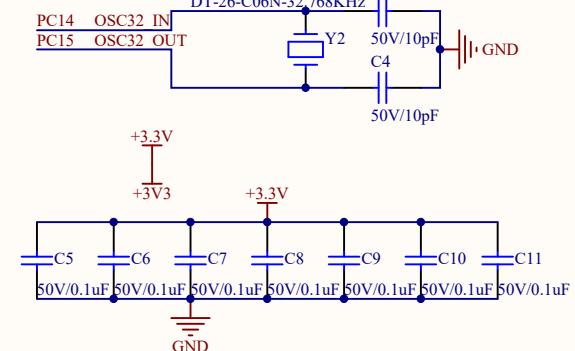
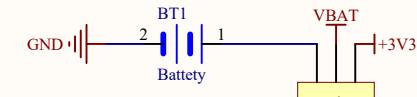
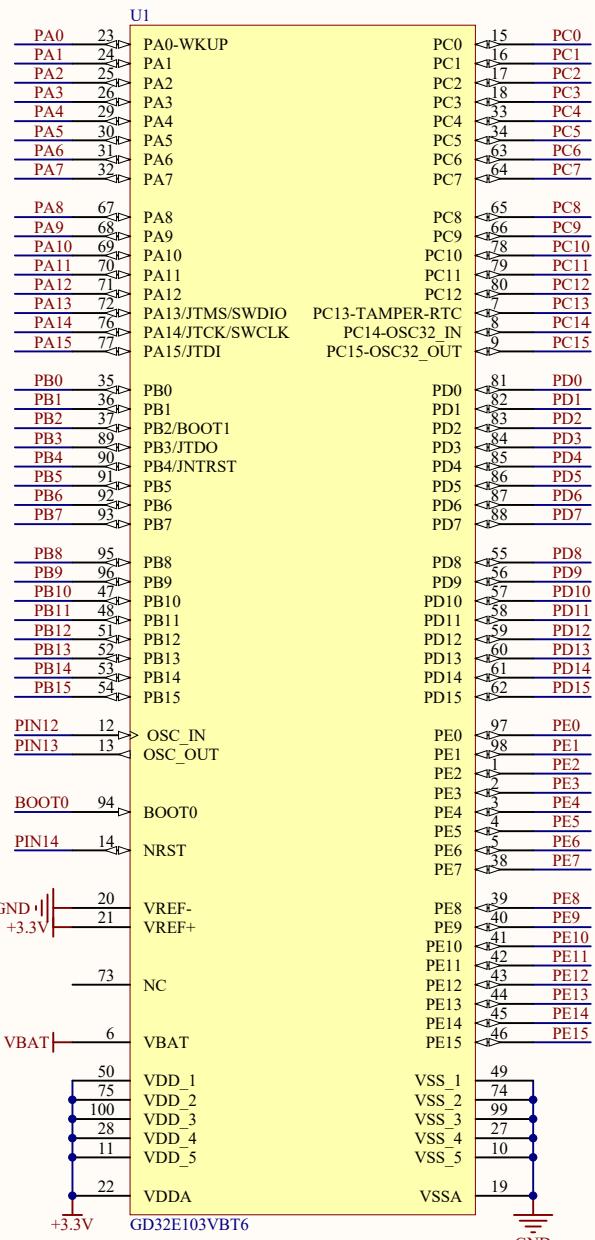
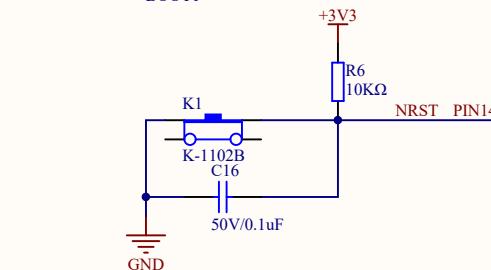
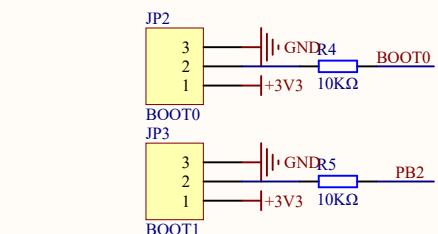
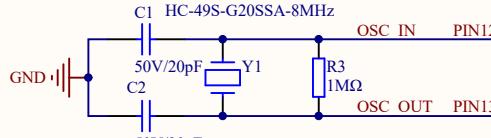
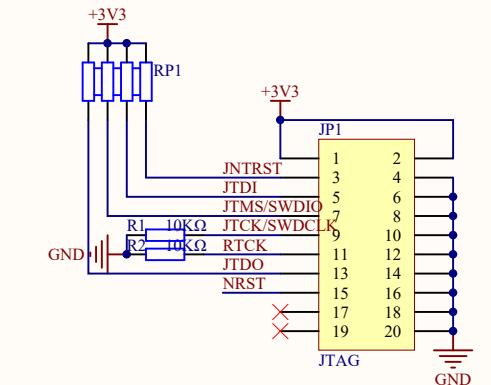
File Name: I2S

Revision: 1.0

Data: 2018-1

Author: Lixin

GDLINK	JTAG	PB4
L TDI	JNTRST	PA15
L TMS/IO	JTDI	PA13
L TCK/CLK	JTCK/SWDCLK	PA14
L TDO/SWO	JTDO	PB3
L TRst	NRST	



Company Name: GigaDevice

File Name: MCU

Revision: 1.0

Data: 2018-1

Author: XuFei

A

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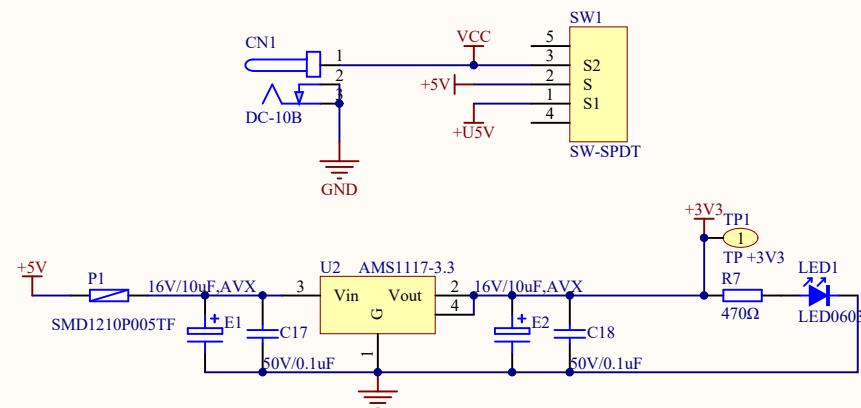
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Company Name: GigaDevice

File Name: Power

Revision: 1.0

Data: 2018-1

Author: XuFei

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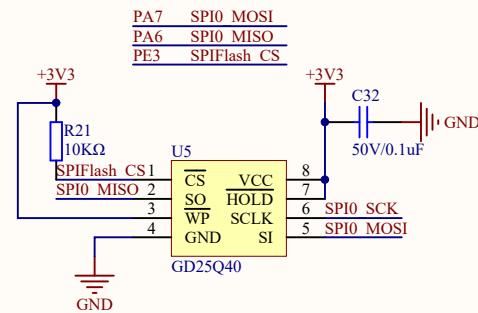
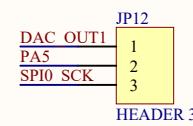
C

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SPI Flash

Short JP12(1,2) for DAC function
Short JP12(2,3) for SPI0 function



Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2018-1

Author: XuFei

A

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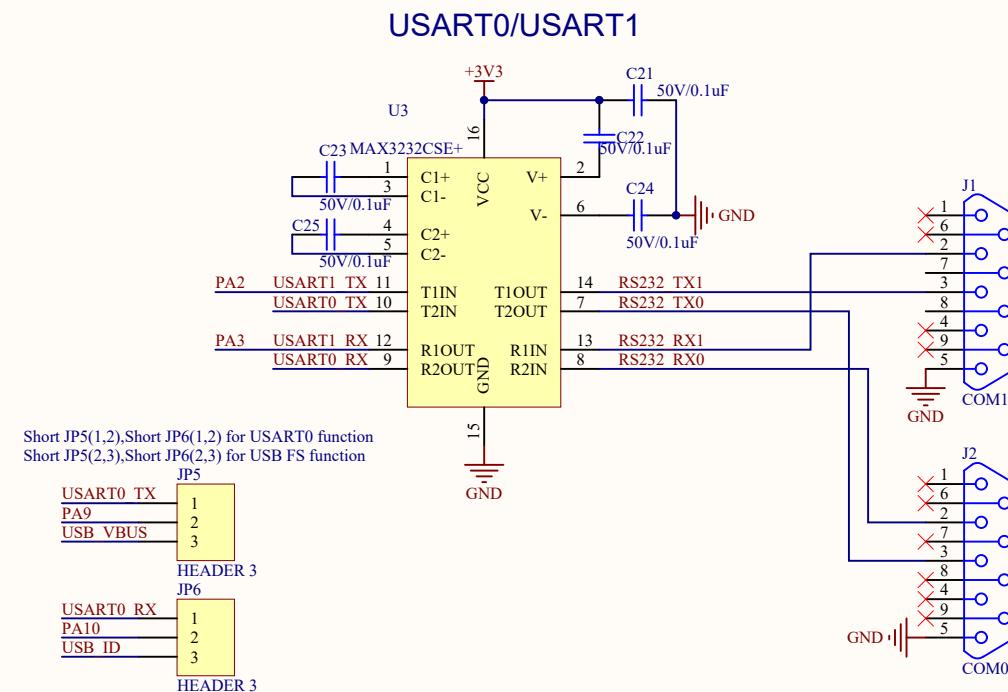
B

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Company Name: GigaDevice

File Name: USART

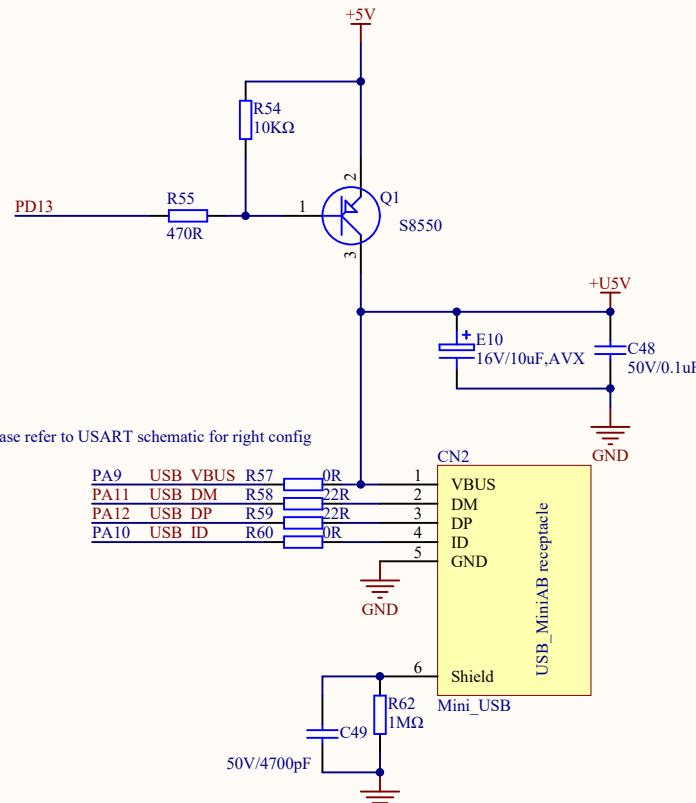
Revision: 1.0

Data: 2018-1

Author: XuFei

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Company Name: GigaDevice

File Name: USBFS

Revision: 1.1	Data: 2018-1	Author: Xufei
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