

A

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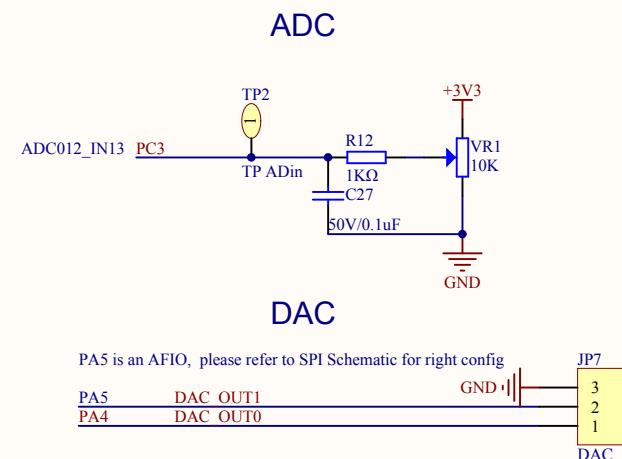
B

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Company Name: GigaDevice

File Name: AD_DA

Revision: 1.0

Data: 2017-03

Author: XuFei

A

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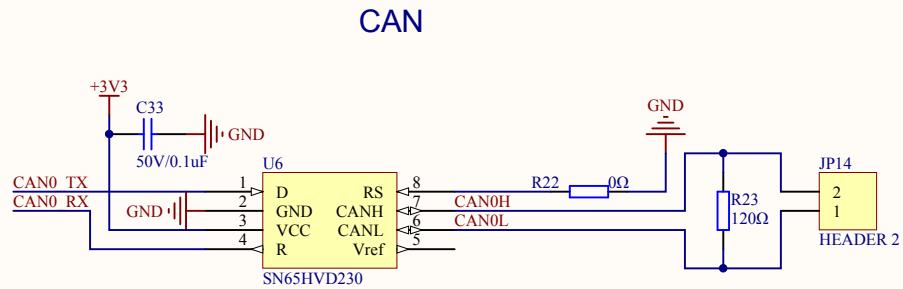
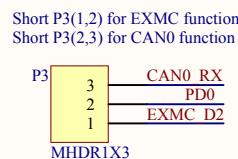
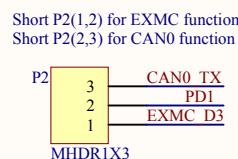
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Company Name: GigaDevice

File Name: CAN

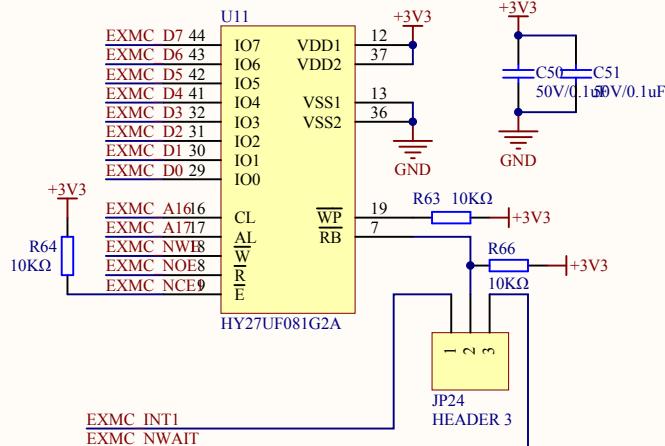
Revision: 1.0

Data: 2017-03

Author: XuFei

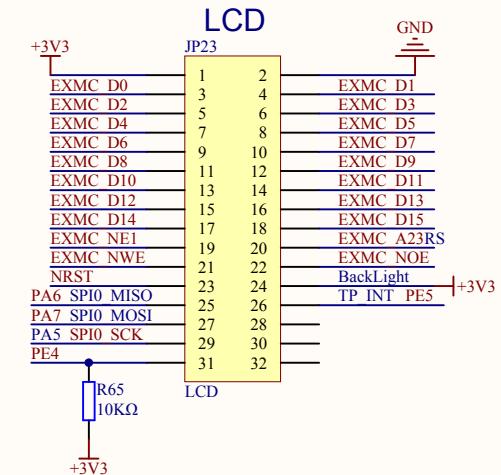
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Nand Flash

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PD0,PD1 are AFIOs, please refer to CAN schematic for right config

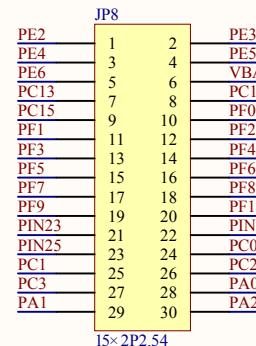
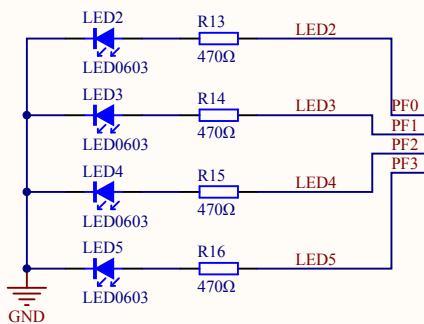
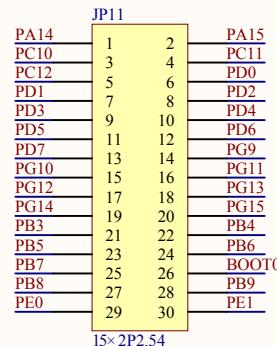
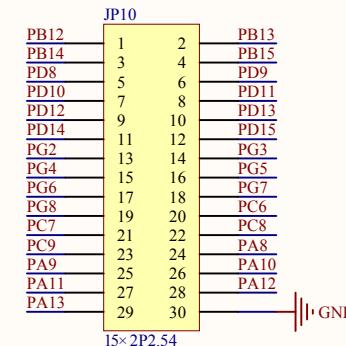
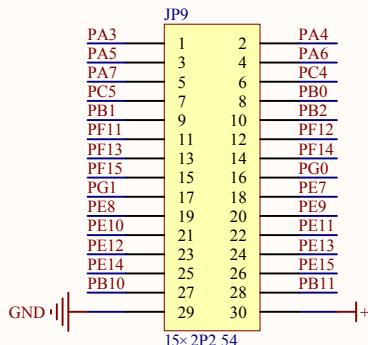
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PD15	EXMC_D1	PD12	EXMC_A17
PD0	EXMC_D2	PE2	EXMC_A23
PD1	EXMC_D3	PD4	EXMC_NOE
PE7	EXMC_D4	PD5	EXMC_NWE
PE8	EXMC_D5	PD6	EXMC_NWAIT
PE9	EXMC_D6	PD7	EXMC_NCE1
PE10	EXMC_D7	PG6	EXMC_INT1
PE11	EXMC_D8	PG9	EXMC_NE1
PE12	EXMC_D9		
PE13	EXMC_D10		
PE14	EXMC_D11		
PE15	EXMC_D12		
PD8	EXMC_D13		
PD9	EXMC_D14		
PD10	EXMC_D15		

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Company Name: GigaDevice**File Name: EXMC**

Revision: 1.0 Data: 2017-03 Author: XuFei

LED**Extension Pin**

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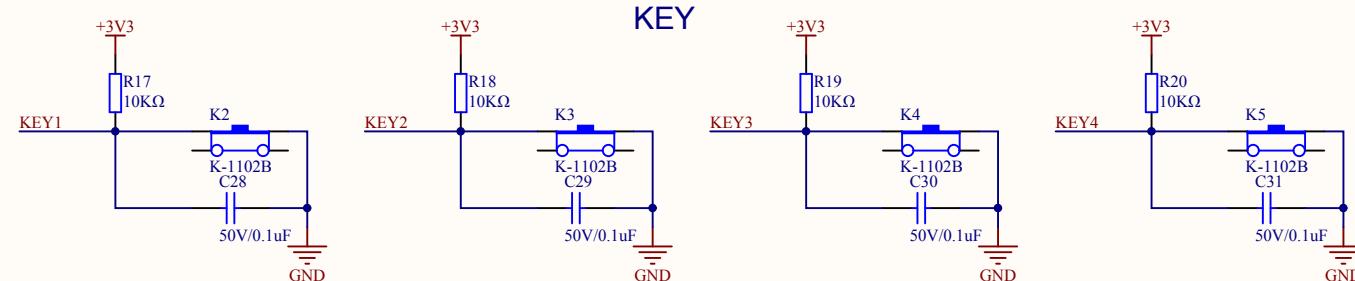
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PA0	KEY1
PC13	KEY2
PF5	KEY3
PF4	KEY4

Company Name: GigaDevice**File Name: Extension**

Revision: 1.0

Data: 2017-03

Author: XuFei

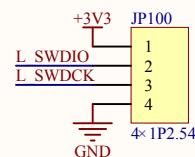
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2

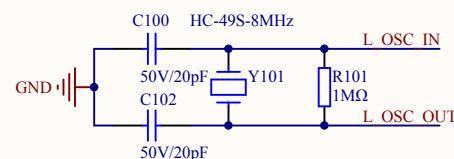
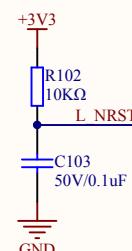
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4

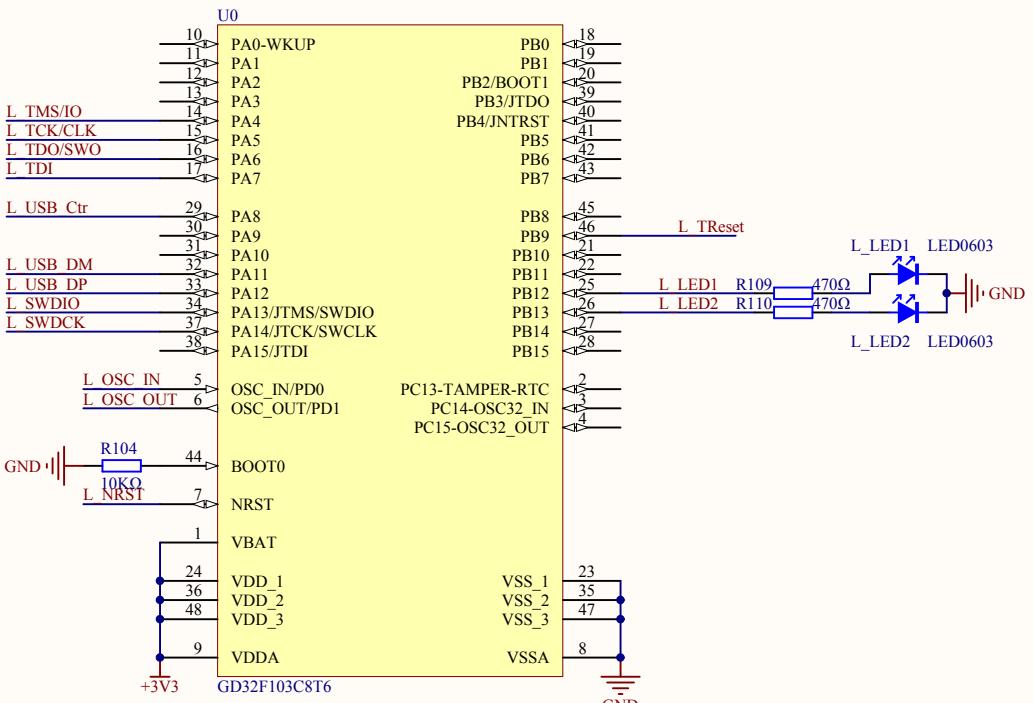
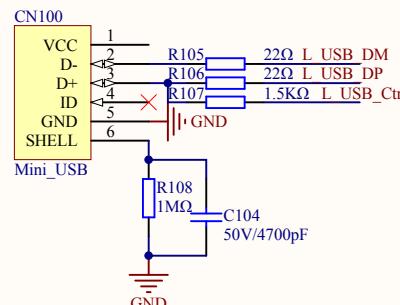
A

MCU SWD

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**Reset**

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3

Company Name: GigaDevice**File Name: GDLink**

Revision: 1.0 | Data: 2015-01-09 | Author: XuFei

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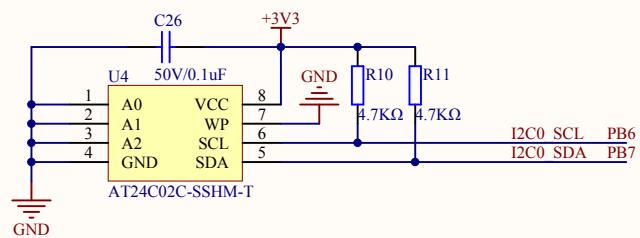
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I2C



Company Name: GigaDevice

File Name: I2C

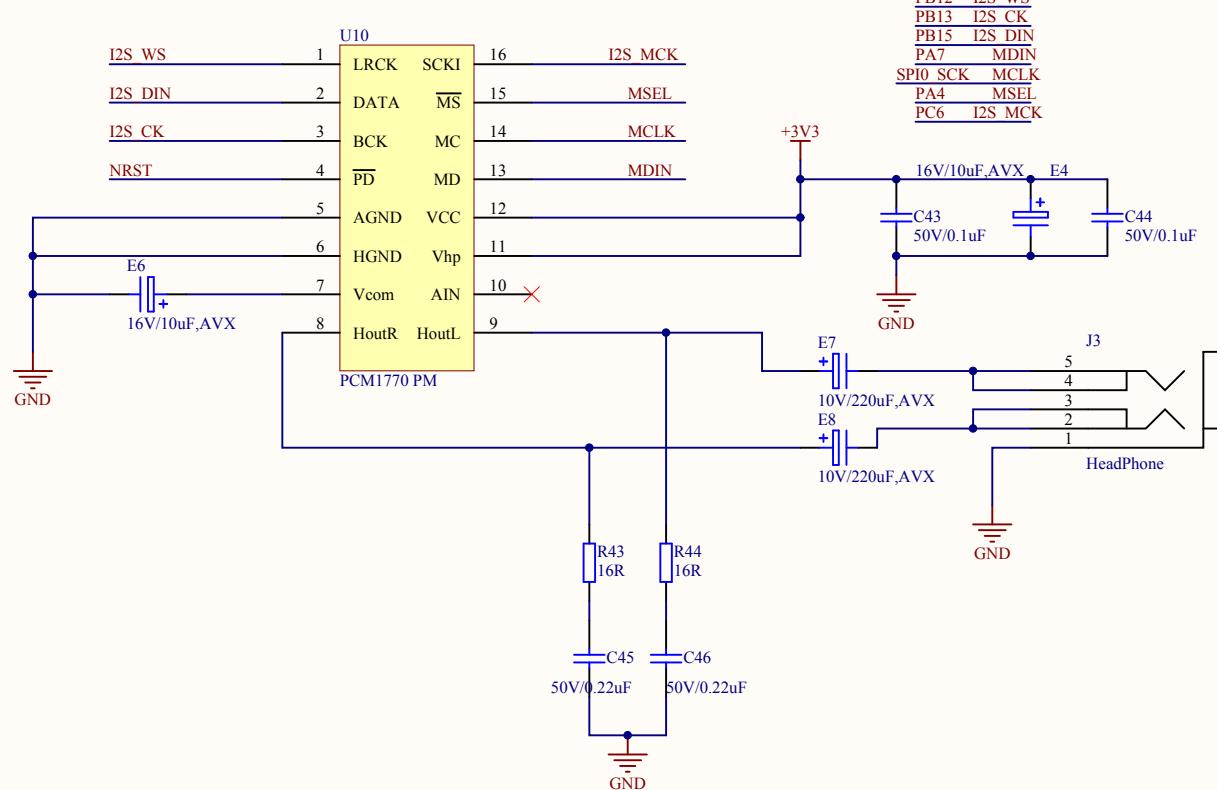
Revision: 1.0

Data: 2017-03

Author: XuFei

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I2S**Company Name: GigaDevice****File Name: I2S**

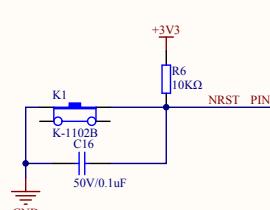
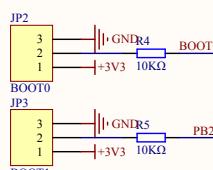
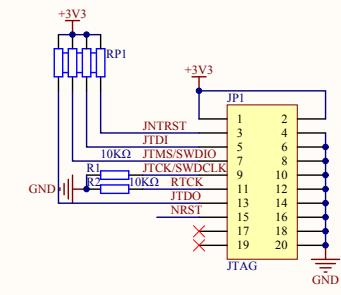
Revision: 1.0

Data: 2017-03

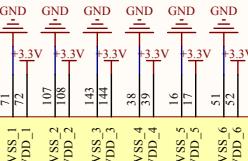
Author: Xufei

D

GDLINK JTAG
JTDI JTDI PB4
L_TDI JTMS/SWdio PA15
L_TMS/IO JTMS/SWdio PA13
L_TCK/SCLK JTCK/SWDCLK PA14
L_TDO/SWO JTDO PB3
L_Reset NRST



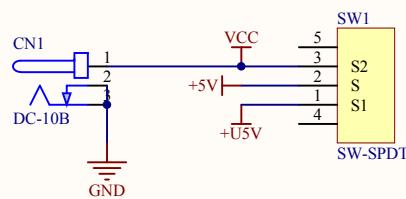
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PA1	35
PA2	36
PA3	37
PA4	40
PA5	41
PA6	42
PA7	43
PA8	100
PA9	101
PA10	102
PA11	103
PA12	104
PA13	105
PA14	109
PA15	110
PB0	46
PB1	47
PB2	48
PB3	133
PB4	134
PB5	135
PB6	136
PB7	137
PB8	139
PB9	140
PB10	69
PB11	70
PB12	73
PB13	74
PB14	75
PB15	76
PC0	26
PC1	27
PC2	28
PC3	29
PC4	44
PC5	45
PC6	96
PC7	97
PC8	98
PC9	99
PC10	111
PC11	112
PC12	113
PC13	7
PC14	8
PC15	9
PD0	114
PD1	115
PD2	116
PD3	117
PD4	118
PD5	119
PD6	122
PD7	123
PD8	77
PD9	78
PD10	79
PD11	80
PD12	81
PD13	82
PD14	85
PD15	86



VSS_1	72	GND	72	+3.3V
VDD_2	107	GND	108	+3.3V
VSS_3	143	GND	144	+3.3V
VDD_4	38	GND	39	+3.3V
VSS_5	16	GND	17	+3.3V
VDD_6	51	GND	52	+3.3V
PG0	56	PG0	57	PG1
PG1	87	PG1	88	PG3
PG2	90	PG5	91	PG6
PG3	92	PG7	93	PG8
PG4	94	PG9	124	PG9
PG5	125	PG10	126	PG11
PG6	127	PG12	128	PG13
PG7	129	PG14	132	PG15
PF0	10	PF0	11	PF1
PF1	12	PF2	13	PF3
PF2	14	PF4	15	PF5
PF3	18	PF6	19	PF7
PF4	20	PF8	21	PF9
PF5	22	PF10	49	PF11
PF6	50	PF12	53	PF13
PF7	54	PF14	55	PF15
PF8	141	PE0	142	PE1
PF9	1	PE2	2	PE3
PE2	3	PE4	4	PE5
PE3	5	PE6	58	PE7
PE4	59	PE8	60	PE9
PE5	63	PE10	64	PE11
PE6	65	PE12	66	PE13
PE7	67	PE14	68	PE15
PE8	69	PE16	70	PE17
PE9	71	PE18	72	PE19
PE10	73	PE20	74	PE21
PE11	75	PE22	76	PE23
PE12	77	PE24	78	PE25
PE13	79	PE26	80	PE27
PE14	81	PE28	82	PE29
PE15	83	PE30	84	PE31
PE16	85	PE32	86	PE33
PE17	87	PE34	88	PE35
PE18	89	PE36	90	PE37
PE19	91	PE38	92	PE39
PE20	93	PE40	94	PE41
PE21	95	PE42	96	PE43
PE22	97	PE44	98	PE45
PE23	99	PE46	100	PE47
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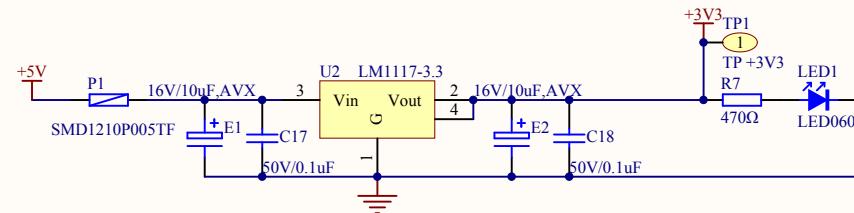
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Company Name: GigaDevice

File Name: Power

Revision: 1.0

Data: 2017-03

Author: XuFei

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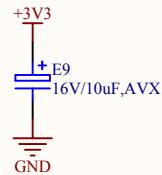
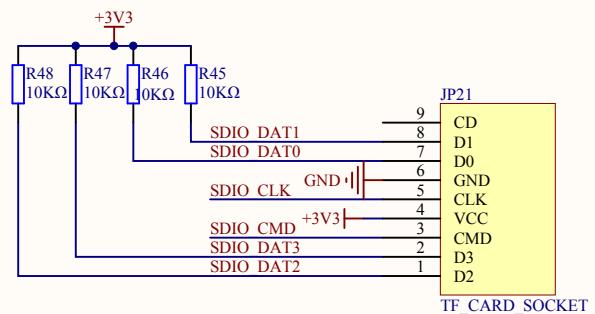
C

D

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SDIO

PD2	SDIO_CMD
PC12	SDIO_CLK
PC8	SDIO_DAT0
PC9	SDIO_DAT1
PC10	SDIO_DAT2
PC11	SDIO_DAT3



Company Name: GigaDevice

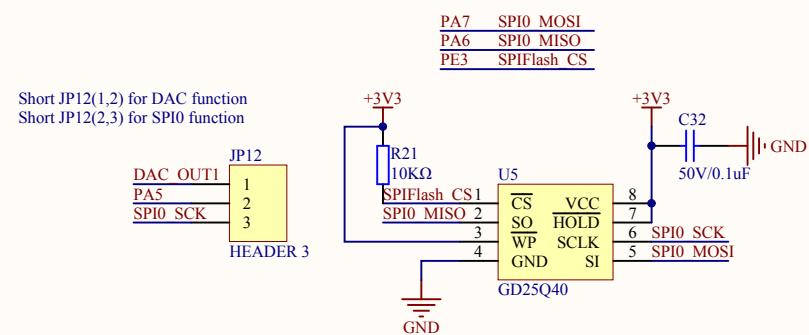
File Name: SDIO

Revision: 1.0

Data: 2017-03

Author: Xufei

SPI Flash



Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2017-03

Author: XuFei

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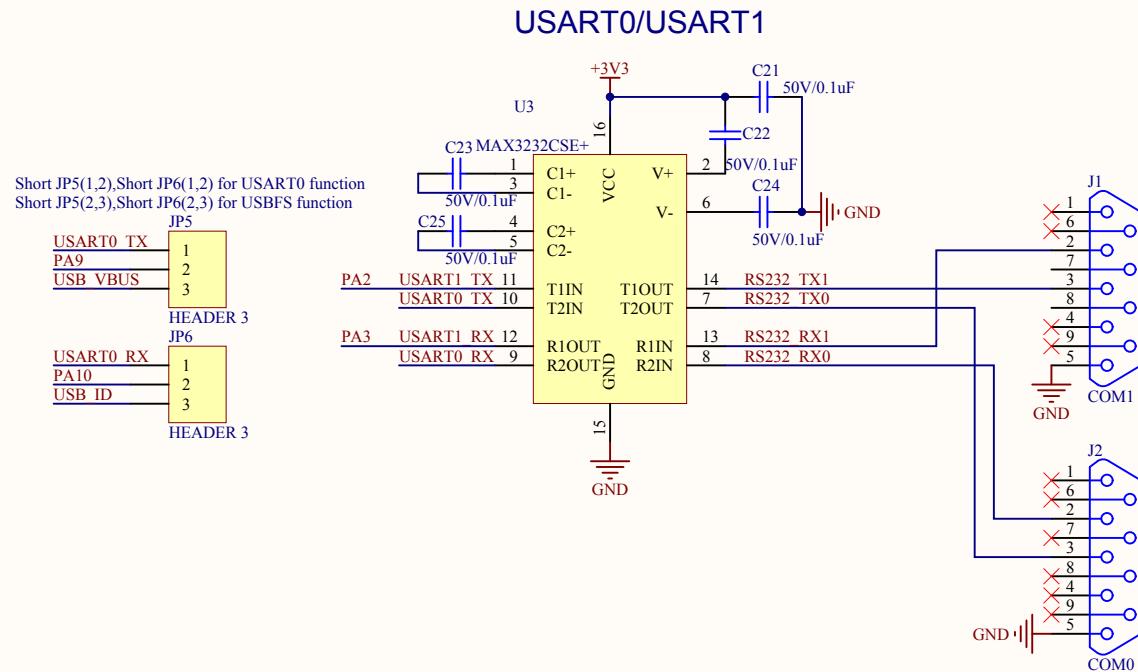
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Company Name: GigaDevice

File Name: USART

Revision: 1.0	Data: 2017-03	Author: XuFei
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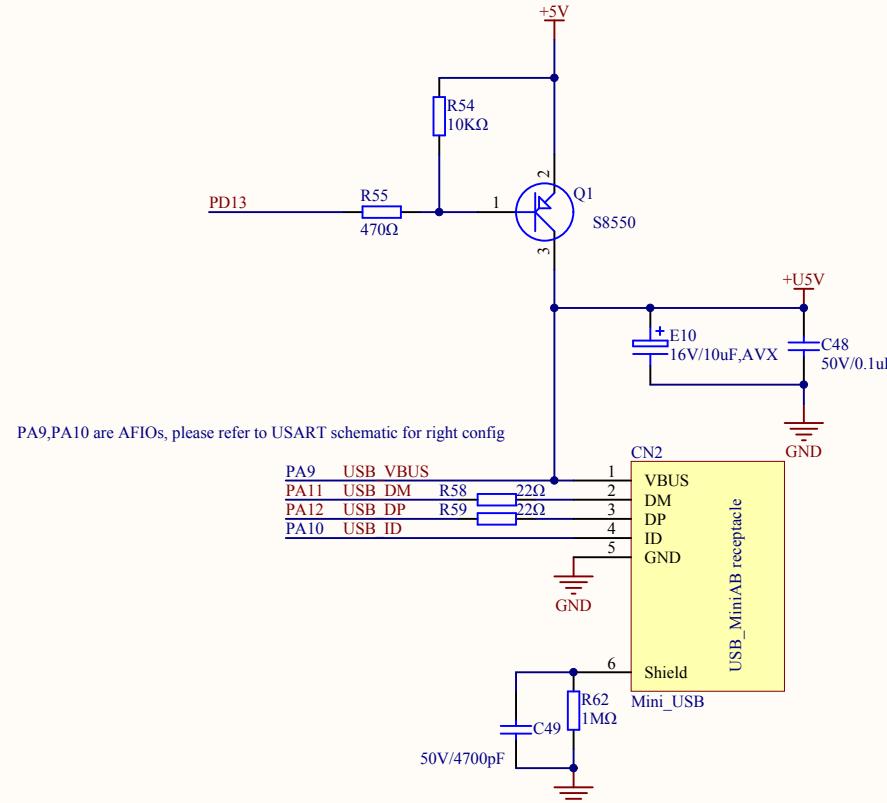
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Company Name: GigaDevice

File Name: USB

Revision: 1.0	Data: 2017-03	Author: Xufei
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