

**GigaDevice Semiconductor Inc.**

**使用 TIMER 对内部低速时钟的校准方法**

**应用笔记**

**AN064**

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## 1. 简介

GD32 MCU 内部的低速时钟（IRC32K 或 IRC40K）可以担当一个低功耗时钟源的角色，其优点是不需要外部器件从而可以降低硬件成本。其频率大约为 40kHz 或 32kHz，可以为独立看门狗（FWDGT）和实时时钟（RTC）等外设提供时钟。其缺点是精确度相对较低，适用的场景不多，但是可以利用 TIMER 对其捕获从而进行校准。本文以 GD32F30x 系列 MCU 为例，介绍使用 TIMER 校准内部低速时钟的方法。

## 2. 校准原理

即使是同一个系列的 MCU，每一颗 MCU 的内部低速时钟都可能不同且有一定的偏差，因此当它给 RTC 或 FWDGT 提供时钟时，软件设置 RTC 或 FWDGT 的预分频时以 40K 的固定数值进行计算，会造成较大的误差（例如：若以 40K 时钟设置 RTC 预分频得到 1S 的时间基准，则将 RTC 的预分频值设为 39999，当 IRC40K 的实际频率小于 40K 时，会使 RTC 的计时效果偏快）。

GD32F30x 系列 MCU 可以通过软件配置重映射，将 TIMER4\_CH3 通道与 IRC40K 在内部进行连接，如 [图 2-1. TIMER4\\_CH3 的备用功能重映射](#) 所示。

图 2-1. TIMER4\_CH3 的备用功能重映射

表 8-6. TMER4 备用功能重映射		
备用功能	TIMER4CH3_IEMAP = 0	TIMER4CH3_IEMAP = 1
TIMER4_CH3	TIMER4_CH3 与 PA3 相连	IRC40K 内部时钟与 TIMER4_CH3 输入相连，用于校正

1. 重映射适用于高密度、超高密度和互联型的产品。

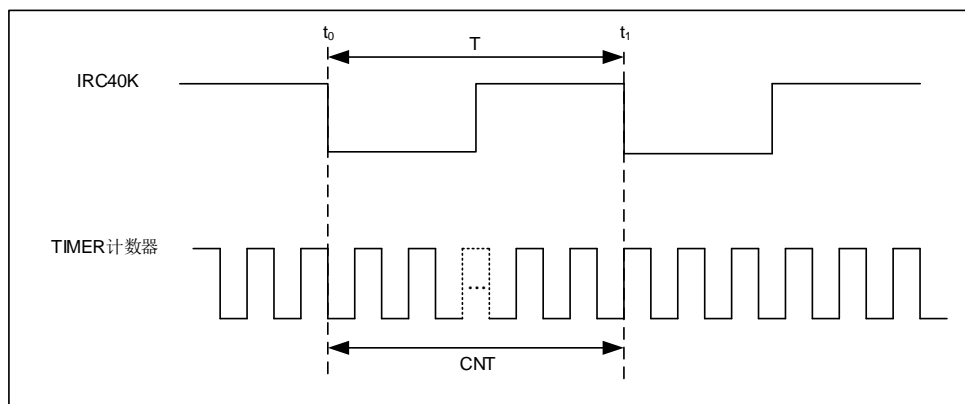
进而可以使用 TIMER4\_CH3 通道捕获 IRC40K 边沿得到准确的时钟频率，如 [图 2-2. TIMER4\\_CH3 捕获 IRC40K 边沿](#) 所示。从而修改如 RTC 或 FWDGT 等外设的时钟分频值，获得更精确的时基。则有：

$$T = t_1 - t_0 = \text{CNT} / f_{\text{ck}} \quad (2-1)$$

$$f_{\text{IRC40K}} = 1 / T = f_{\text{ck}} / \text{CNT} \quad (2-2)$$

其中， $t_0$  为 TIMER4 第一次捕获到 IRC40K 下降沿的时间， $t_1$  为 TIMER4 第二次捕获到 IRC40K 下降沿的时间， $T$  为 IRC40K 的周期， $f_{\text{ck}}$  为 TIMER4 计数器的时钟。 $f_{\text{ck}}$  是已知的，CNT 通过两次捕获分别读取 TIMER 计数器可以得到，则  $f_{\text{IRC40K}}$  通过式（2-2）计算得出。

图 2-2. TIMER4\_CH3 捕获 IRC40K 边沿



以 RTC 的应用为例，在计算出  $f_{\text{IRC40K}}$  之后，可用实际的 IRC40K 频率来配置 RTC 的预分频值，用来产生 RTC 的时间基准 SC\_CLK。通常情况下将预分频值设置为  $f_{\text{IRC40K}} - 1$ ，得到 1S 的 RTC 时间基准，从而达到校准的目的。

### 3. 实现方法

#### 3.1. IRC40K 边沿捕获

通过配置 TIMER4\_CH3 捕获 IRC40K 的下降沿，代码[表 3-1. TIMER 配置](#)如所示。

表 3-1. TIMER 配置

```
/* TIMER4 configuration: input capture mode -----
the RTC signal is connected to TIMER4 CH3
the rising edge is used as active edge
the TIMER4 CH3CV is used to compute the frequency value
----- */
timer_ic_parameter_struct timer_icinitpara;
timer_parameter_struct timer_initpara;

/* connect IRC40K clock to the TIMER4_CH3 input for calibration */
rcu_periph_clock_enable(RCU_AF);
gpio_pin_remap_config(GPIO_TIMER4CH3_IEMAP, ENABLE);

rcu_periph_clock_enable(RCU_TIMER4);
timer_deinit(TIMER4);

/* initialize TIMER init parameter struct */
timer_struct_para_init(&timer_initpara);
/* TIMER4 configuration */
timer_initpara.prescaler      = 0;
timer_initpara.alignedmode    = TIMER_COUNTER_EDGE;
timer_initpara.counterdirection = TIMER_COUNTER_UP;
timer_initpara.period         = 0xFFFF;
timer_initpara.clockdivision   = TIMER_CKDIV_DIV1;
timer_init(TIMER4, &timer_initpara);

/* TIMER4 configuration */
/* initialize TIMER channel input parameter struct */
timer_channel_input_struct_para_init(&timer_icinitpara);
/* TIMER4 CH3 input capture configuration */
timer_icinitpara.icpolarity   = TIMER_IC_POLARITY_FALLING;
timer_icinitpara.icselection  = TIMER_IC_SELECTION_DIRECTTI;
timer_icinitpara.icprescaler  = TIMER_IC_PSC_DIV1;
timer_icinitpara.icfilter     = 0x0;
timer_input_capture_config(TIMER4, TIMER_CH_3, &timer_icinitpara);
```

```

/* auto-reload preload enable */
timer_auto_reload_shadow_enable(TIMER4);
/* clear channel 3 interrupt bit */
timer_interrupt_flag_clear(TIMER4, TIMER_INT_FLAG_CH3);
/* channel 3 interrupt enable */
timer_interrupt_enable(TIMER4, TIMER_INT_CH3);

/* TIMER4 counter enable */
timer_enable(TIMER4);

```

TIMER4\_CH3 捕获到 IRC40K 的下降沿将触发捕获中断，中断处理代码如[表 3-2. 中断处理](#)所示。

**表 3-2. 中断处理**

```

void TIMER4_IRQHandler(void)
{
    if(SET == timer_interrupt_flag_get(TIMER4, TIMER_INT_CH3)){
        /* clear channel 3 interrupt bit */
        timer_interrupt_flag_clear(TIMER4, TIMER_INT_CH3);

        timer_capture_num++;
        if(1 == timer_capture_num){
            /* get the input capture value */
            timer_capture1 = timer_channel_capture_value_register_read(TIMER4,
TIMER_CH_3);
        }else if(2 == timer_capture_num){
            /* get the input capture value */
            timer_capture2 = timer_channel_capture_value_register_read(TIMER4,
TIMER_CH_3);

            /* end capture, disable TIMER4 and CH3 interrupt */
            timer_interrupt_disable(TIMER4, TIMER_INT_CH3);
            timer_disable(TIMER4);
        }
    }
}

```

## 3.2. IRC40K 频率计算

经过两次捕获，TIMER4 计数器的值分别为 timer\_capture1 和 timer\_capture2，则 IRC40K 的频率可以计算出来并配置 RTC 的预分频值，如[表 3-3. IRC40K 实际频率计算](#)所示。

**表 3-3. IRC40K 实际频率计算**

```
temp_capture1 = timer_capture1;
```



```
temp_capture2 = timer_capture2;
timer_period = temp_capture2 - temp_capture1 + 1;

/* compute the actual frequency of the IRC40K (TIMER4_CLK = 2 * CK_APB1) */
if(0 != timer_period){
    irc_freq = ((rcu_clock_freq_get(CK_APB1) * 2) / timer_period);
}

printf("timer_capture1 = %d, timer_capture2 = %d, irc_freq = %d\n", temp_capture1, temp_capture2,
irc_freq);

/* adjust the RTC prescaler value to calibrate the clock */
rtc_lwoff_wait();
rtc_prescaler_set(irc_freq - 1);
rtc_lwoff_wait();
```

## 4. 版本历史

表 4-1. 版本历史

版本号.	说明	日期
1.0	首次发布	2022 年 5 月 20 日

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