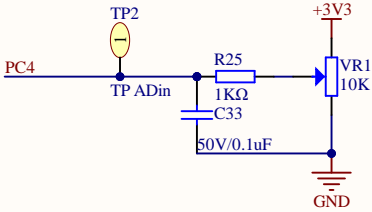
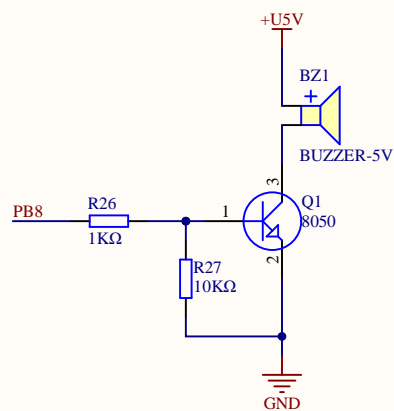


ADC



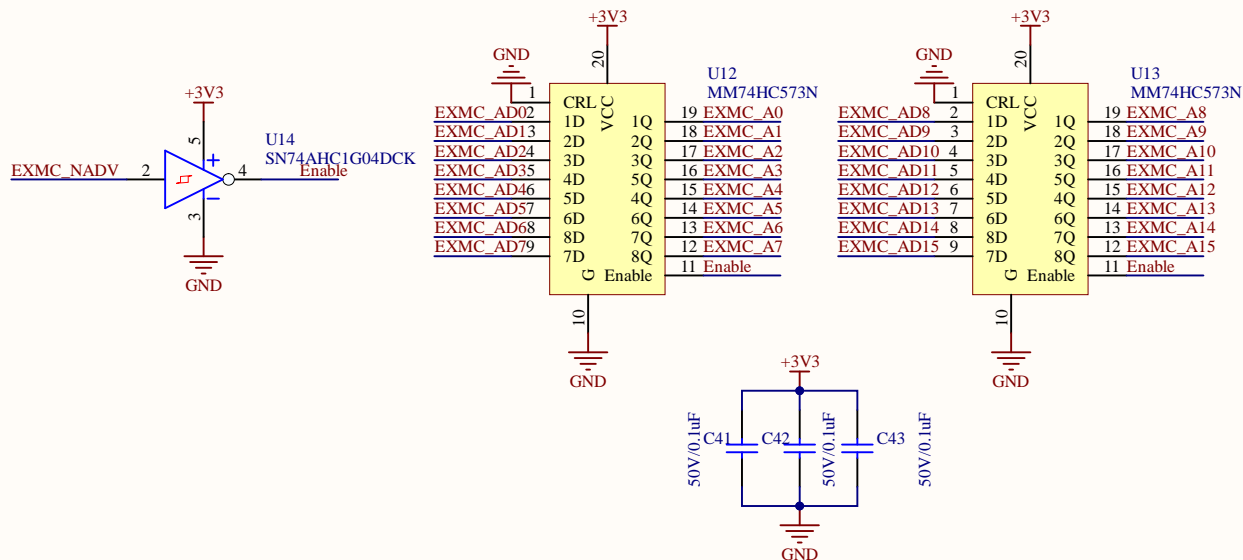
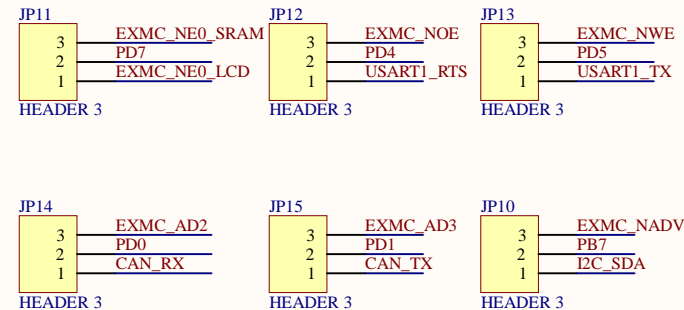
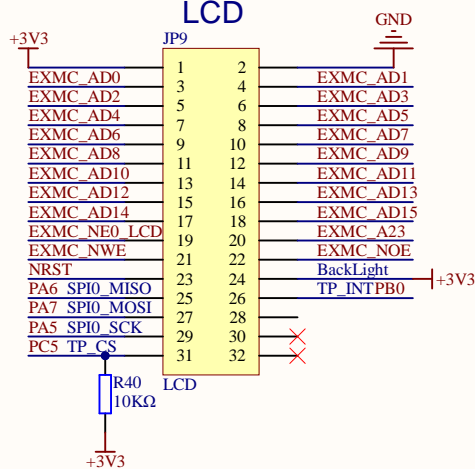
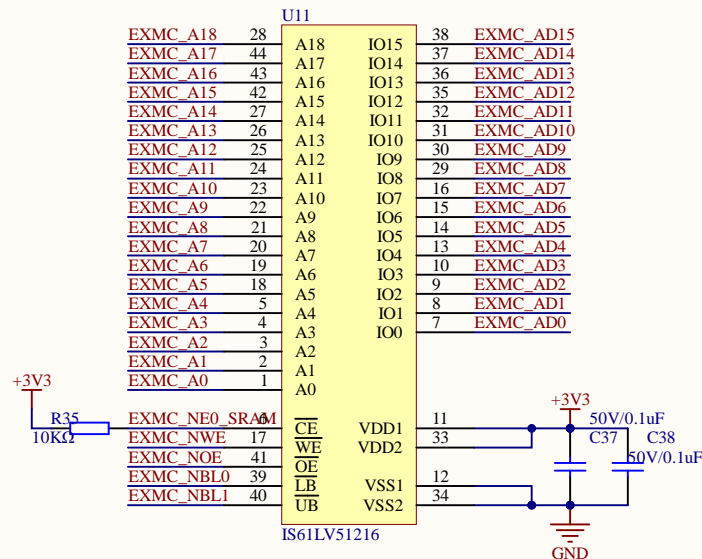
Company Name: GigaDevice		
File Name: AD		
Revision: 1.0	Data: 2015-01	Author: XuFei

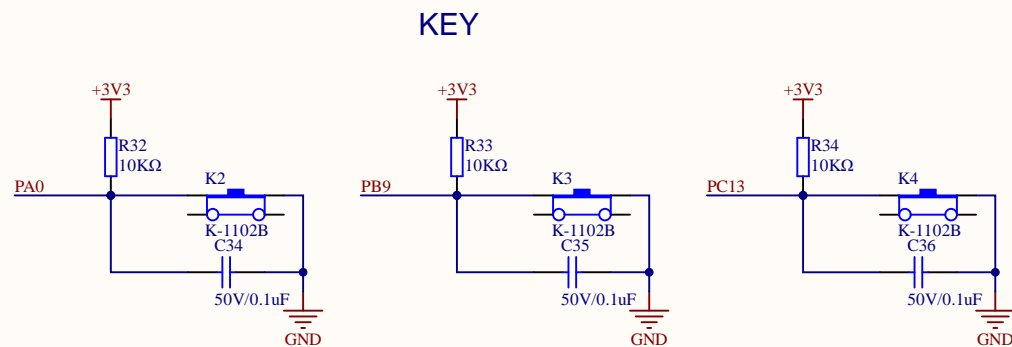
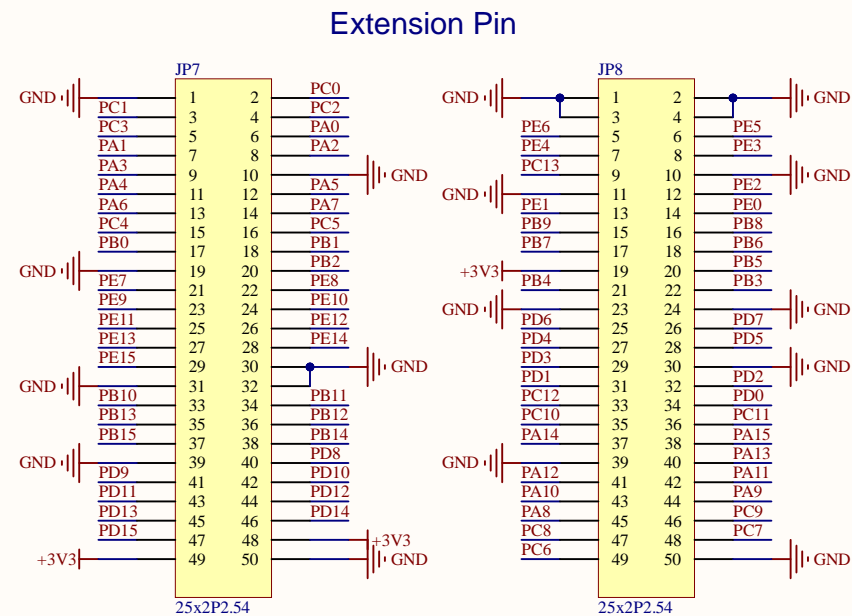
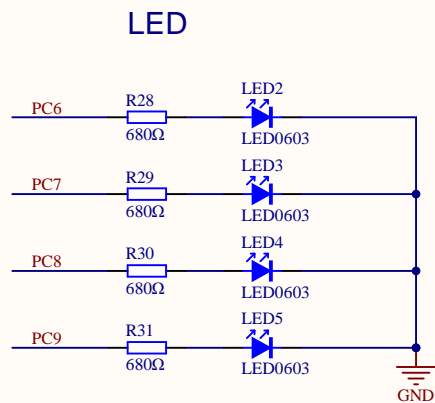


Company Name: GigaDevice		
File Name: Audio		
Revision: 1.0	Data: 2015-01	Author: XuFei

## SRAM

## LCD





Company Name: GigaDevice

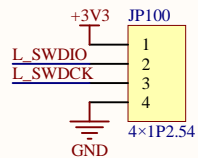
File Name: Extension

Revision: 1.0

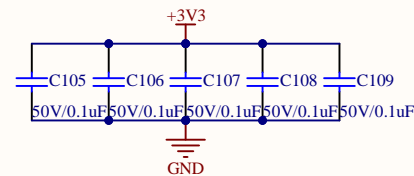
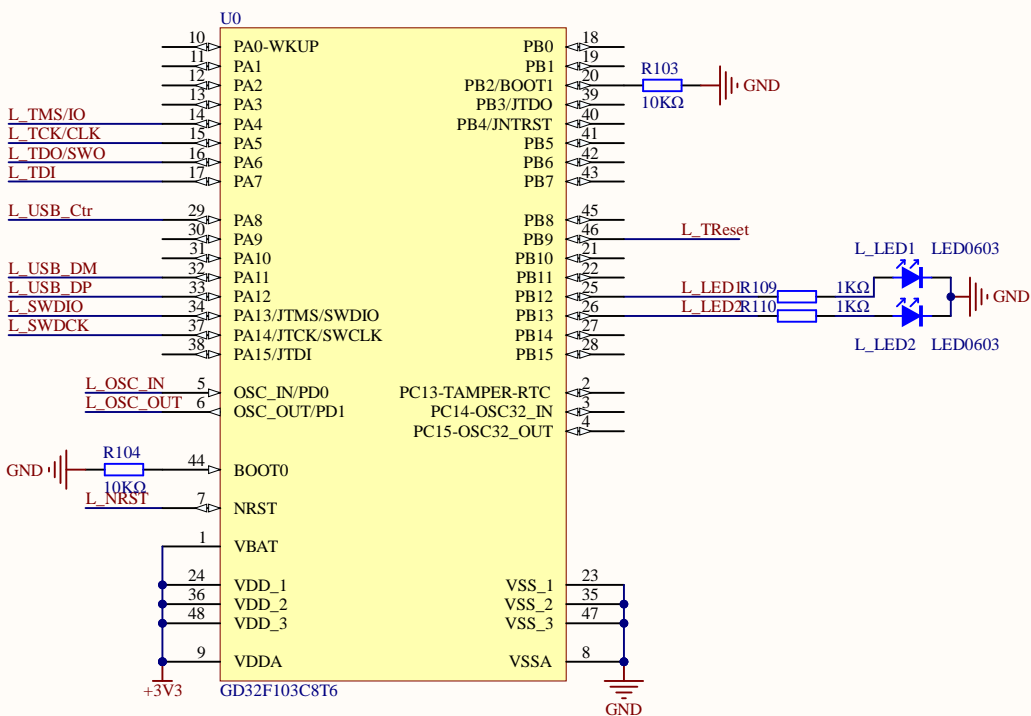
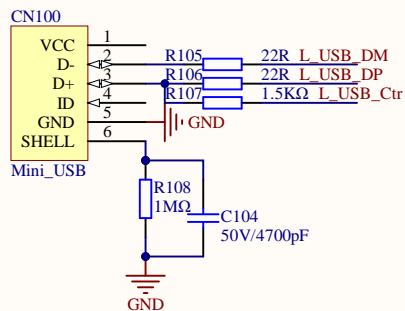
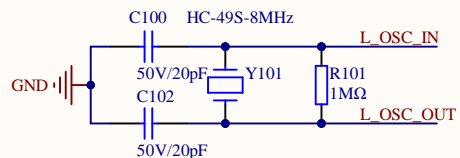
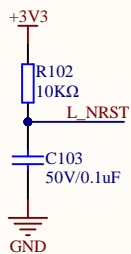
Data: 2015-01

Author: XuFei

## MCU SWD



## Reset



Company Name: GigaDevice

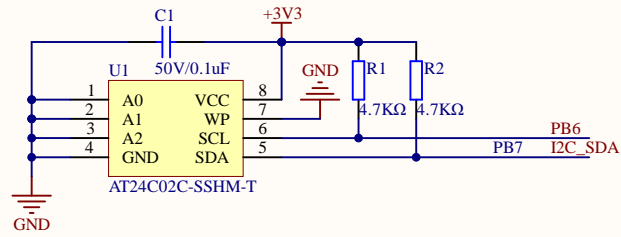
File Name: GDLink

Revision: 1.0

Data: 2015-01-09

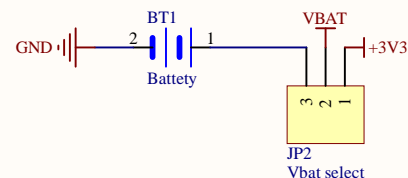
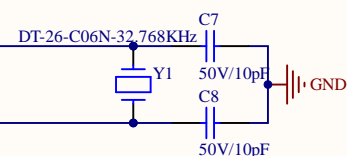
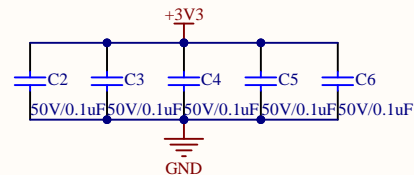
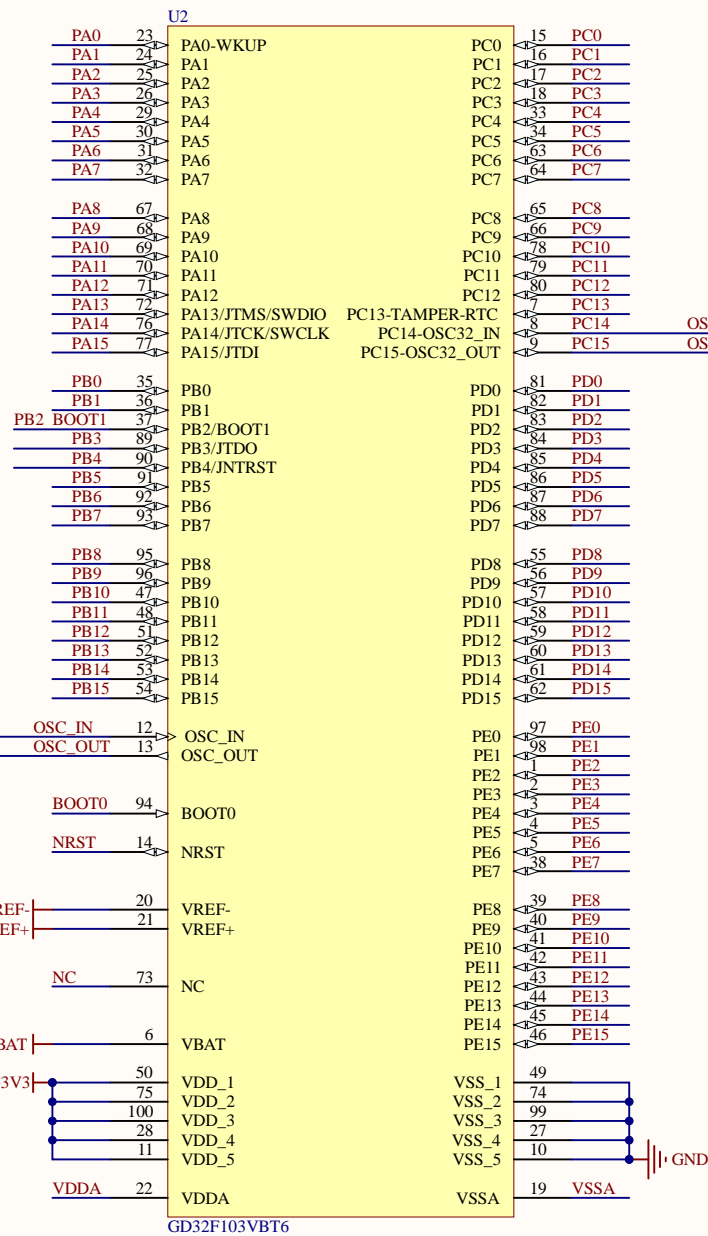
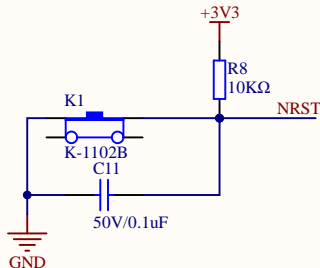
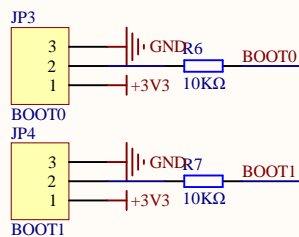
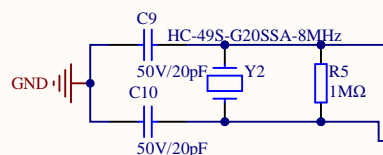
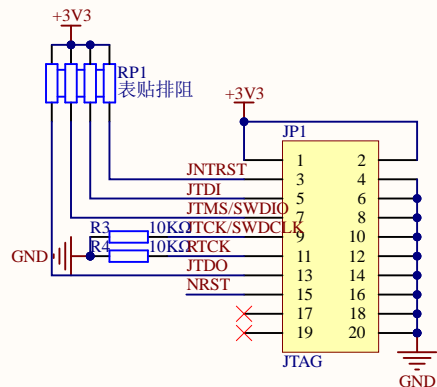
Author: XuFei

# I2C



Company Name: GigaDevice		
File Name: I2C		
Revision: 1.0	Data: 2015-01	Author: XuFei

GDLink	JTAG	
L_TDI	JNTRST	PB4
L_TMS/IO	JTDI	PA15
L_TCK/CLK	JTMS/SWDIO	PA13
L_TDO/SWO	JTCK/SWDCLK	PA14
L_TReset	JTDO	PB3
	NRST	



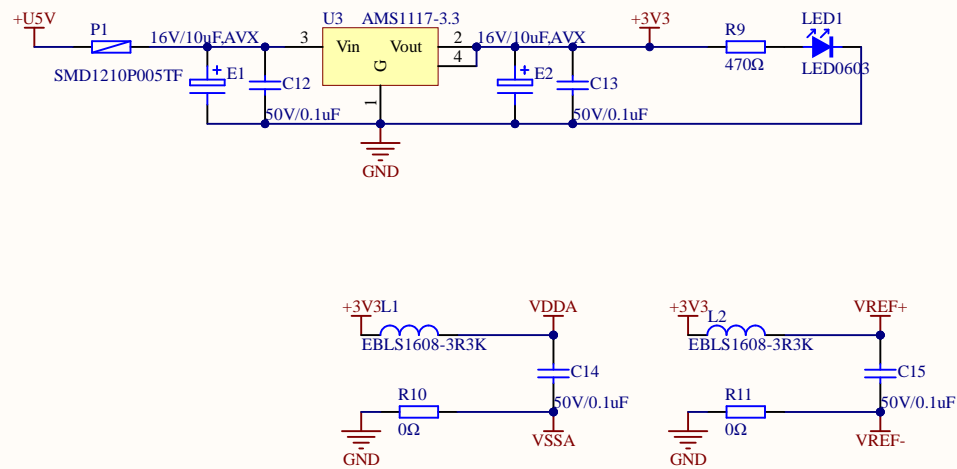
Company Name: GigaDevice

File Name: MCU

Revision: 1.0

Data: 2015-01

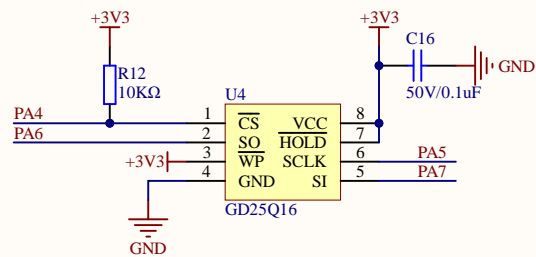
Author: XuFei



Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2015-01	Author: XuFei

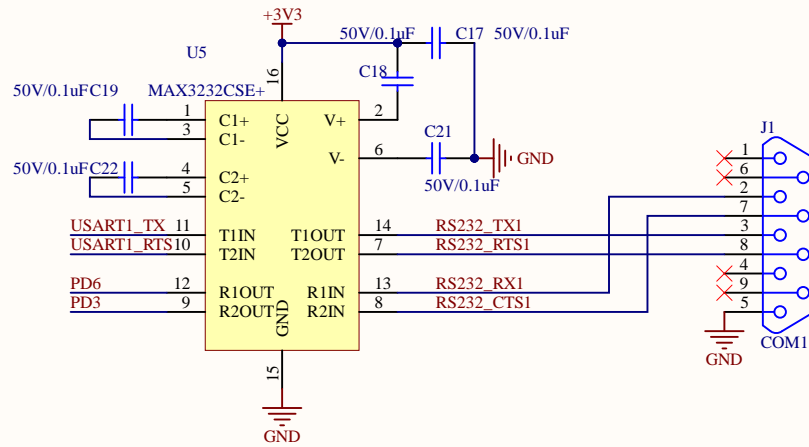


### SPI Flash

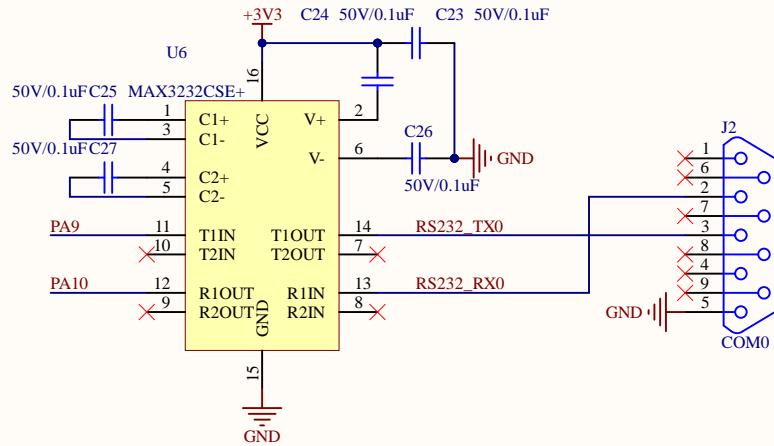


Company Name: GigaDevice		
File Name: SPI		
Revision: 1.0	Data: 2015-01	Author: XuFei

## USART1



## USART0



Company Name: GigaDevice

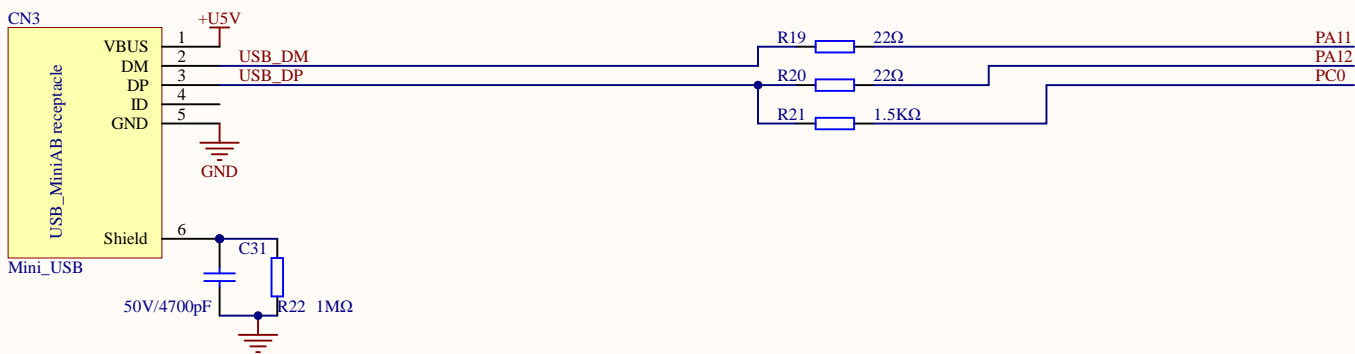
File Name: USART

Revision: 1.0

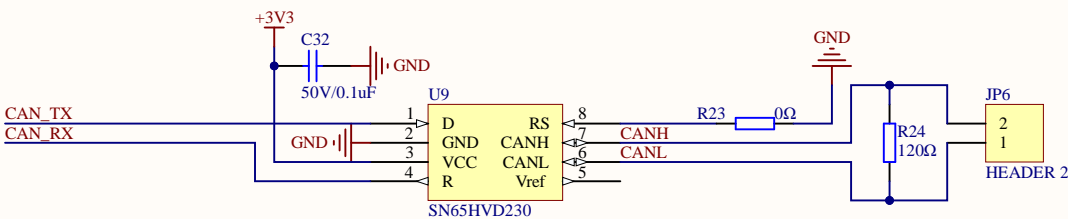
Data: 2015-01

Author: XuFei

USB



CAN



Company Name: GigaDevice

File Name: USB\_CAN

Revision: 1.0

Data: 2015-01

Author: XuFei

