**GigaDevice Semiconductor Inc.** 

Arm<sup>®</sup> Cortex<sup>®</sup>-M3/4/23/33 32-bit MCU

Application Note AN015



# **Table of Contents**

Table (	of Contents	. 2
List of	Figures	. 3
List of	Table	.4
1. Int	roduction	. 5
2. Lit	tleFS transplantation	. 6
2.1.	LittleFS transplanted platform	. 6
2.2.	Add the LittleFS source file	. 6
2.3.	IDE configuration	. 6
2.4.	LittleFS parameter configuration	. 8
3. Lit	tleFS functional test	11
3.1.	LittleFS power failure protection function test	11
3.2.	LittleFS update file data test	13
4. Re	vision historyŕ	16



# **List of Figures**

Figure 2-1. LittleFS version information	6
Figure 2-2. KEIL4 configures C99 standard	7
Figure 2-3. Compile LittleFS source code for the first time	7
Figure 2-4. Macro definition of assert function in LittleFS	7
Figure 2-5. Add LFS_NO_ASSERT macro definition in KEIL4	8
Figure 2-6. Modify the assert macro definition in the lfs_util.h file	8
Figure 3-1. LittleFS power failure protection function test	13
Figure 3-2. Update file data test	15



# List of Table

Table 2-1. LittleFS configuration parameters	8
Table 2-2. LittleFS is based on the interface function definition of GD25Q16 SPI-flash	9
Table 3-1. LittleFS mount code	11
Table 3-2. LittleFS power failure protection function test code	11
Table 3-3. LittleFS update file data code	13
Table 4-1. Revision history	16



# 1. Introduction

LittleFS is an open source small file system launched by Arm<sup>®</sup> for embedded devices. It has the characteristics of anti-power failure, dynamic wear leveling, and less RAM/ROM occupation. It is suitable for managing SPI-flash in IOT embedded devices. The specific introduction can be found at <u>https://github.com/ARMmbed/littlefs.</u>

This article introduces the method of porting LittleFS to GD32 project, and tests the read and write functions of the file system.



## 2. LittleFS transplantation

## 2.1. LittleFS transplanted platform

The LittleFS transplantation platform introduced in this article is the GD32F450Z-EVAL board. An SPI-flash is attached to the GD32F450Z-EVAL board. The SPI-flash model is GD25Q16. Porting of LittleFS uses KEIL4, and the code is ported on the SPI\_QSPI\_Flash project of GD32F450Z, and the project version is V2.0.0.

The file of LittleFS is very simple, only four files of "lfs.c", "lfs.h", "lfs\_util.c" and "lfs\_util.h". The version of LittleFS transplanted in this article is LFS\_VERSION 0x00020002. The version information of LittleFS can be obtained from the "lfs.h" file, and the version information is shown in *Figure 2-1. LittleFS version information*.

Figure 2-1. LittleFS version information

19	/// Version info ///
20	
21	// Software library version
22	// Major (top-nibble), incremented on backwards incompatible changes
23	// Minor (bottom-nibble), incremented on feature additions
24	#define LFS_VERSION 0x00020002
25	#define LFS_VERSION_MAJOR (0xffff & (LFS_VERSION >> 16))
26	#define LFS_VERSION_MINOR (0xffff & (LFS_VERSION >> 0))
27	
28	// Version of On-disk data structures
29	// Major (top-nibble), incremented on backwards incompatible changes
30	// Minor (bottom-nibble), incremented on feature additions
31	#define LFS_DISK_VERSION 0x00020000
32	<pre>#define LFS_DISK_VERSION_MAJOR (0xffff &amp; (LFS_DISK_VERSION &gt;&gt; 16))</pre>
33	#define LFS DISK VERSION MINOR (0xffff & (LFS DISK VERSION >> 0))

## 2.2. Add the LittleFS source file

The transplantation method introduced in this article is based on the SPI\_QSPI\_Flash project of GD32F450Z. First, copy the LittleFS file to the follow folder:

GD32F4xx\_Demo\_Suites\_V2.1.0\GD32450Z\_EVAL\_Demo\_Suites\Projects\SPI\_QSPI\_Flas h\Soft\_Drive.

Then open the project and add two files lfs.c and lfs\_util.c to the project.

## 2.3. IDE configuration

LittleFS must be configured to support the C99 standard when using KEIL4 to compiler. The option configuration is shown in *Figure 2-2. KEIL4 configures C99 standard*.



# AN015 Transplantation of LFS file system based on GD32F450Z

#### Figure 2-2. KEIL4 configures C99 standard

😨 Options for Target 'GD32450Z_EVAL'		×
Device   Target   Output   Listing   Vser	C/C++ Asm Linker Debug	Vtilities
Preprocessor Symbols		
Define: USE_STDPERIPH_DRIVER,GD	32F450	
Undefine:		
Language / Code Generation		
	Strict ANSI C	Wamings:
Optimization: Level 0 (-00)	Enum Container always int	All Warnings 💌
Optimize for Time	Plain Char is Signed	Thumb Mode
Split Load and Store Multiple	Read-Only Position Independent	No Auto Includes
✓ One ELF Section per Function	Read-Write Position Independent	C99 Mode
Include Paths Misc Controls	brary\GD32F4xx_standard_peripheral\lnc	lude;\\\GD32F4xx_
Compiler control string	MICROLIB -g -O0apcs=interworksplit_ D32F4xx_standard_peripheral\\Include -l	ections -I\\
OK	Cancel Defaults	Help

After configuring the C99 standard, compile the project and test whether the LittleFS source code can be compiled successfuly. The compilation result is shown in <u>Figure 2-3. Compile</u> <u>LittleFS source code for the first time</u>, showing that the \_\_aeabi\_assert function is undefined. Because GD32 projects all choose to use micro-library and do not include the assert function, when KEIL4 opens the optimization level for compilation, compilation errors are reported.

#### Figure 2-3. Compile LittleFS source code for the first time

```
Build target 'GD32450Z_EVAL'
linking...
.\output\GD32450Z_EVAL.axf: Error: L6218E: Undefined symbol __aeabi_assert (referred from lfs.o).
Not enough information to list image symbols.
Finished: 1 information, 0 warning and 1 error messages.
".\output\GD32450Z_EVAL.axf" - 1 Error(s), 0 Warning(s).
Target not created
```

There is a macro definition about the assert function in the "lfs\_util.h" file, as shown in *Figure 2-4. Macro definition of assert function in LittleFS*.

#### Figure 2-4. Macro definition of assert function in LittleFS

```
84 // Runtime assertions
85 #ifndef LFS_NO_ASSERT
86 #define LFS_ASSERT(test) assert(test)
87 #else
88 #define LFS_ASSERT(test)
89 #endif
```

Since the assert function is not necessary, the above problems can be solved in two ways.

 Add macro definition in KEIL4/KEIL5. The adding method is shown in <u>Figure 2-5. Add</u> LFS NO ASSERT macro definition in KEIL4.



#### Figure 2-5. Add LFS\_NO\_ASSERT macro definition in KEIL4

vice   Target   Output   Listing   Use	r C/C++ Asm Linker Debug	Vtilities
Preprocessor Symbols	GD32F450	
Language / Code Generation Optimization: Level 0 (-00) Optimize for Time Split Load and Store Multiple One ELF Section per Function Include	Strict ANSI C  Fun Container always int  Plain Char is Signed  Read-Only Position Independent  Read-Wite Position Independent  Library/GD32F4xx_standard_peripheral\Inc	Wamings: All Wamings Thumb Mode No Auto Includes C99 Mode stude:
Paths Misc Controls Compiler control string	MICROLIB -g -00apcs=interwork -split_ \GD32F4xx_standard_peripheral\Include -l	sections -1\\
OK	Cancel Defaults	Help

Modify the macro definition in the lfs\_util.h file and change the macro definition to a no-op. The modification results are as *Figure 2-6. Modify the assert macro definition in the lfs\_util.h file*.

Figure 2-6. Modify the assert macro definition in the lfs\_util.h file

```
84 // ·Runtime ·assertions
85 = #ifndef ·LFS_NO_ASSERT
86 //#define ·LFS_ASSERT(test) ·assert(test)
87 #define ·LFS_ASSERT(test)
88 #else
89 #define ·LFS_ASSERT(test)
90 #endif
```

After completing the above related operations, and then click compile, the compile successfully

### 2.4. LittleFS parameter configuration

The configuration parameter structure "struct lfs\_config" of LittleFS is defined in "lfs.h". When LittleFS manages SPI-flash, it is necessary to configure the parameters according to the actual SPI-flash. The transplantation example of this article uses GD25Q16 SPI-flash. The related parameter configuration is shown in <u>Table 2-1. LittleFS configuration parameters</u>.

Table 2-1. LittleFS configuration parameters

```
/*!

\brief config the block device interface

\param[in] none

\param[out] none

\retval none

*/
```



void	d lfs_config(void)	
{		
	/* block device operations */	
	g_lfs_cfg.read = block_device_read;	//link the block_device_read function
	g_lfs_cfg.prog = block_device_prog;	//link the block_device_prog function
	g_lfs_cfg.erase = block_device_erase;	//link the block_device_sync function
	g_lfs_cfg.sync = block_device_sync;	//link the block_device_sync function
	/* block device configuration */	
	g_lfs_cfg.read_size = 256;	//config read data size for each block(256 byte)
	g_lfs_cfg.prog_size = 256;	//config write data size for each block(256 byte)
	g_lfs_cfg.block_size = 4096;	//config the block size(4096 byte
	a lfs. cfa.cache size = 256 <sup>.</sup>	//Must be a multiple of the read and program sizes
	$g_{10}_{01}$	//the total of block
	g_lis_cig.block_coult = 1024,	
	g_lfs_cfg.lookahead_size = 128;	//Predictive depth for block allocation:1024/8=128
	g_lfs_cfg.block_cycles = 500;	//Set to -1 to disable block-level wear-leveling
}		

In the structure lfs\_config, four function pointers are defined: int (\*read), int (\*prog), int (\*erase) and int (\*sync) and the interface function to be called needs to be completed by the user. This article is based on the interface functions of GD25Q16 SPI-flash as shown in <u>Table 2-2</u>. <u>LittleFS is based on the interface function definition of GD25Q16 SPI-flash</u>.

#### Table 2-2. LittleFS is based on the interface function definition of GD25Q16 SPI-flash

/*!		
	\brief	read the data from spi flash block
	\param[in]	*c : the lfs_config struct pointer
	\param[in]	block: the number of block
	\param[in]	off: the offset in block
	\param[in]	buffer: the read data buffer
	\param[in]	size: the size of read data
	\param[out	] none
	\retval	none
*/		
int3	32_t block_d	evice_read(const struct lfs_config *c, lfs_block_t block,
	lfs_off_t off	, void *buffer, lfs_size_t size)
{		
	/* read the	data from spi flash */
	spi_flash_b	ouffer_read((uint8_t*) buffer,(block * (c->block_size) + off),size);
	return 0;	
}		
/*!		



```
\brief
                 write the data from spi flash block
    \param[in] *c : the lfs_config struct pointer
    \param[in] block: the number of block
    \param[in] off: the offset in block
    \param[in] buffer: the write data buffer
    \param[in] size: the size of write data
    \param[out] none
    \retval
                 none
*/
int32_t block_device_prog(const struct lfs_config *c, lfs_block_t block,
    lfs_off_t off, const void *buffer, lfs_size_t size)
{
    /* write the data to spi flash */
    spi_flash_buffer_write((uint8_t*)buffer, ((block) * (c->block_size) + off), size);
    return 0;
}
/*!
    \brief
                 erase the spi flash block
    \param[in] *c : the lfs_config struct pointer
    \param[in] block: the number of block
    \param[out] none
    \retval
                 none
*/
int32_t block_device_erase(const struct lfs_config *c, lfs_block_t block)
{
    /* erase the sector of spi flash */
    spi_flash_sector_erase(block * (c->block_size));
    return 0;
}
/*!
    \brief
                 Sync the state of the underlying block device.
    \param[in] none
    \param[out] none
    \retval
                 none
*/
int32_t block_device_sync(const struct lfs_config *c)
{
    /* no operation */
    return 0;
```



# 3. LittleFS functional test

This chapter introduces the test of read and write function after porting LittleFS, and gives a test demo.Before testing the LittleFS function, it is necessary to mount the file system. The code of mounting LittleFS is as <u>Table 3-1. LittleFS mount code</u>.

### Table 3-1. LittleFS mount code

```
/*1
    \brief
                mount the filesystem
    \param[in] none
    \param[out] none
    \retval
                 none
*/
void sys_lfs_mount(void)
{
    lfs_config();
    /* mount the filesystem */
    int err = lfs_mount(&g_lfs, &g_lfs_cfg);
    /* reformat if we can't mount the filesystem£¬this should only happen on the first boot */
    if (err) {
         /* format a block device with the littlefs */
         lfs_format(&g_lfs, &g_lfs_cfg);
         /* mount the filesystem */
         lfs_mount(&g_lfs, &g_lfs_cfg);
    }
```

## 3.1. LittleFS power failure protection function test

The power-down protection function is an advantage of LittleFS. The code for the powerdown protection function test refers to <u>Table 3-2. LittleFS power failure protection function</u> <u>test code</u>.

```
Table 3-2. LittleFS power failure protection function test code
```

```
/*!

\brief lilltleFS power-off protection test

\param[in] none

\param[out] none

\retval none

*/

void lfs_power_off_protection_test(void)

{

uint32_t boot_count = 0;
```



/\* open the file \*/
Ifs\_file\_open(&g\_Ifs, &g\_Ifs\_file, "boot\_count", LFS\_O\_RDWR | LFS\_O\_CREAT);
/\* read the data \*/
Ifs\_file\_read(&g\_Ifs, &g\_Ifs\_file, &boot\_count, sizeof(boot\_count));
/\* update boot count \*/
boot\_count += 1;
/\* write to the beginning of the file \*/
Ifs\_file\_rewind(&g\_Ifs, &g\_Ifs\_file);
Ifs\_file\_write(&g\_Ifs, &g\_Ifs\_file, &boot\_count, sizeof(boot\_count));
/\* remember the storage is not updated until the file is closed successfully \*/
Ifs\_file\_close(&g\_Ifs, &g\_Ifs\_file);
/\* release any resources \*/
Ifs\_unmount(&g\_Ifs);
/\* print the boot count \*/
printf("boot\_count:%d\n", boot\_count);

The file named "boot\_count" is updated every time the main function runs. The program can be interrupted at any time, without losing the record of the number of starts, and without damaging the file system. How many power-down tests are performed, and the test results are shown in *Figure 3-1. LittleFS power failure protection function test*.



#### Figure 3-1. LittleFS power failure protection function test

· /	CommUart Assistant	- 🗆 ×
COM Settings	Data receive	SAVAGE V4.2.3
PortNum COM6		
BaudR 115200 💌	The Flash ID:0xC84015	
DPaity NONE 💌		
DataB 8 🖃	\Soft_Drive\Lfs.c:997:error: Corrupted dir pair at {0x0, } hoot count:1	0x1}
StopB 1		
	The Flash ID:0xC84015	
Ulose		
Recv Options	boot_count:2	
Receive to file		
🗖 Auto linefeed	The Flash_ID:0xC84015	
🔲 Show timestamp	boot_count:3	
Receive as hex		
Pause receive	The Flash ID: 0xC84015	
<u>Save</u> <u>Clear</u>		
Send Options	boot_count:4	
🗖 Data from file		
🗌 Auto checksum		
🗌 Auto clear input		
Send as hex	1.DCD • 2.RXD • 3.TXD • <u>4.DTR</u> • 5.GND • 6.DSR • <u>7.RTS</u>	● 8.CTS ● 9.RI ●
☐ Period 1000 ms		
Load <u>Clear</u>		Send
💣 Ready!	• TX:0 RX:14	31 Reset //

As shown in the figure above, when the file system is mounted for the first time, the file system can not be mounted. At this time, it needs to be reformatted and then mounted.

## 3.2. LittleFS update file data test

The test of updating file data is mainly to write data to the same file multiple times and print the file content through the serial port. Then use LittleFS's function of cropping file data to delete unnecessary data. The test demo is as <u>Table 3-3</u>. LittleFS update file data code.

Table 3-3. LittleFS update file data code

/*!		
	\brief	read the data from spi flash block
	\param[in]	*c : the lfs_config struct pointer
	\param[in]	block: the number of block
	\param[in]	off: the offset in block
	\param[in]	buffer: the read data buffer
	\param[in]	size: the size of read data
	\param[out	] none
	\retval	none
*/		
int3	2_t block_d	evice_read(const struct lfs_config *c, lfs_block_t block,
	lfs_off_t off	, void *buffer, lfs_size_t size)
{		



# AN015 Transplantation of LFS file system based on GD32F450Z

```
/* read the data from spi flash */
    spi_flash_buffer_read((uint8_t*) buffer,(block * (c->block_size) + off),size);
     return 0;
}
/*!
    \brief
                 write the data from spi flash block
    \param[in] *c : the lfs_config struct pointer
    \param[in] block: the number of block
    \param[in] off: the offset in block
    \param[in] buffer: the write data buffer
    \param[in] size: the size of write data
    \param[out] none
    \retval
                 none
*/
int32_t block_device_prog(const struct lfs_config *c, lfs_block_t block,
    lfs_off_t off, const void *buffer, lfs_size_t size)
{
    /* write the data to spi flash */
    spi_flash_buffer_write((uint8_t*)buffer, ((block) * (c->block_size) + off), size);
    return 0;
}
/*1
    \brief
                 erase the spi flash block
    \param[in] *c : the lfs_config struct pointer
    \param[in] block: the number of block
    \param[out] none
    \retval
                 none
*/
int32_t block_device_erase(const struct lfs_config *c, lfs_block_t block)
{
    /* erase the sector of spi flash */
    spi_flash_sector_erase(block * (c->block_size));
    return 0;
}
/*!
    \brief
                 Sync the state of the underlying block device.
    \param[in] none
    \param[out] none
    \retval
                 none
```



*/
int32_t block_device_sync(const struct lfs_config *c)
{
/* no operation */
return 0;
}

The test result is shown in *Figure 3-2. Update file data test*. As shown in the figure, the "E:\\my\_test\_file" file is created, and the content of the file is updated. The last print result is the data retained by the file after cutting part of the data.

Figure 3-2. Update file data test

· · /	CommUart Assistant	×
COM Settings	Data receive	SAVAGE V4.2.3
PortNum COM6		
BaudR 115200 🖃	The Flash ID:0xC84015	
DPaity NONE -		
DataB 8 🔻	E:\my_test_file Ciantumian MCM for LittleWS Toot anotherfully !	
Steep 1	E:\my_test_file	
зторв ј –	GigaDevice MCU for LittleFS Test successfully !GigaDevice MCU	for LittleFS
💓 Close	Repeat Write Test successfully !! F:\mu test file	
	GigaDevice MCU for LittleFS Test successfully !GigaDevice MCU	
Recv Options		
Receive to file		
🗌 Auto linefeed		
🥅 Show timestamp		
🥅 Receive as hex		
🔲 Pause receive		
<u>Save</u> <u>Clear</u>		
Send Options		
🔲 Data from file		
🔲 Auto checksum		
🦳 Auto clear input		
🔲 Send as hex	1.DCD • 2.RXD • 3.TXD • <u>4.DTR</u> • 5.GND • 6.DSR • <u>7.RTS</u> •	8.CTS • 9.RI •
Period 1000 ms		
Load Clear		Send
🎯 Ready!	TX:0 RX:814	Reset



# 4. Revision history

### Table 4-1. Revision history

Revision No.	Description	Date
1.0	Initial Release	Dec.13 2021



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