

A

A

B

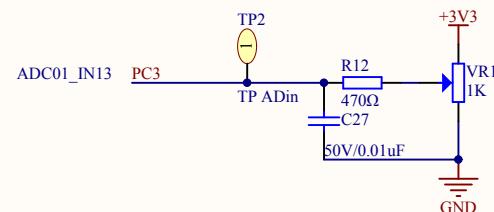
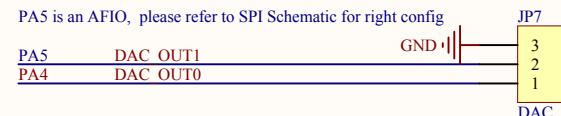
B

C

C

D

D

ADC**DAC****Company Name: GigaDevice****File Name: AD_DA**

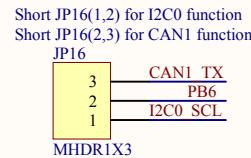
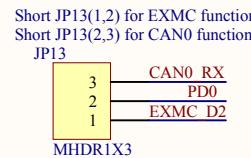
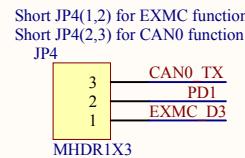
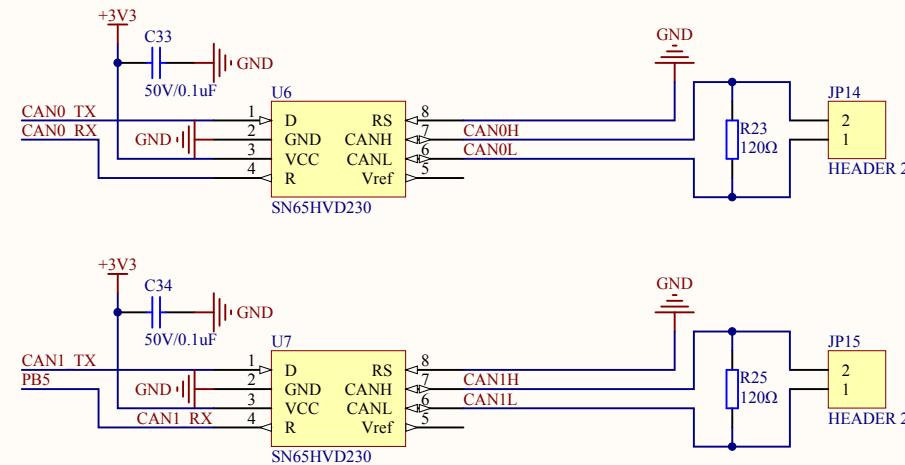
Revision: 1.0

Data: 2019-6

Author: XuFei

A

A

**CAN****Company Name: GigaDevice****File Name: CAN**

Revision: 1.0

Data: 2013-7-24

Author: XuFei

A

A

B

B

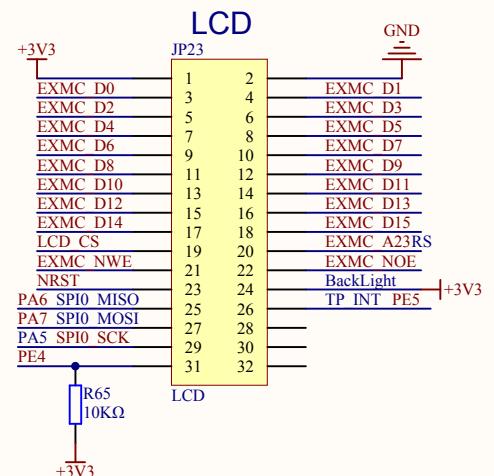
C

C

D

D

PD0,PD1 are AFIOs, please refer to CAN schematic for right config	
PD14	EXMC D0
PD15	EXMC D1
PD0	EXMC D2
PD1	EXMC D3
PE7	EXMC D4
PE8	EXMC D5
PE9	EXMC D6
PE10	EXMC D7
PE11	EXMC D8
PE12	EXMC D9
PE13	EXMC D10
PE14	EXMC D11
PE15	EXMC D12
PD8	EXMC D13
PD9	EXMC D14
PD10	EXMC D15
PE2 EXMC A23	
PD4	EXMC NOE
PD5	EXMC NWE
PD7	EXMC NE0 LCD CS



Company Name: GigaDevice

File Name: EXMC

Revision: 1.0

Data: 2019-6

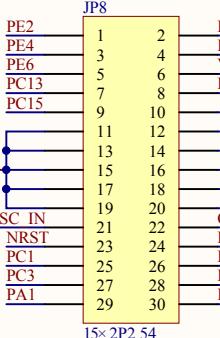
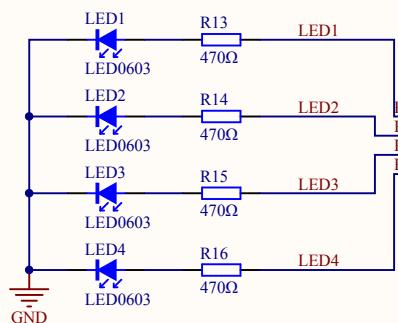
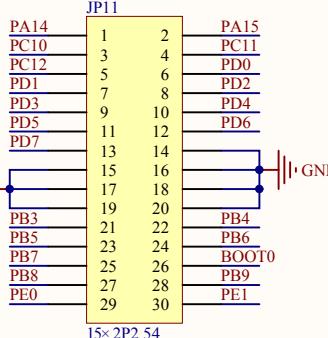
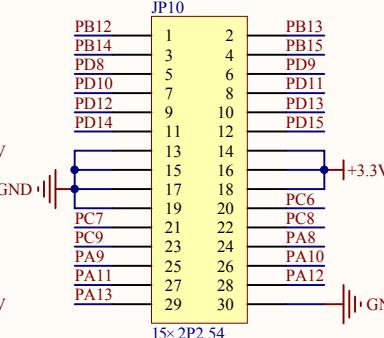
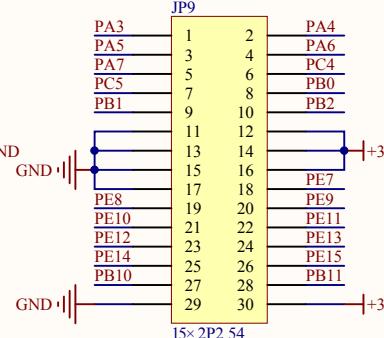
Author: XuFei

1

2

3

4

LED**Extension Pin**

A

A

B

B

C

C

D

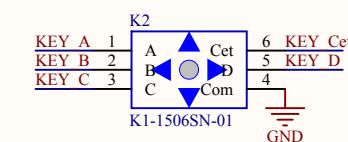
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1

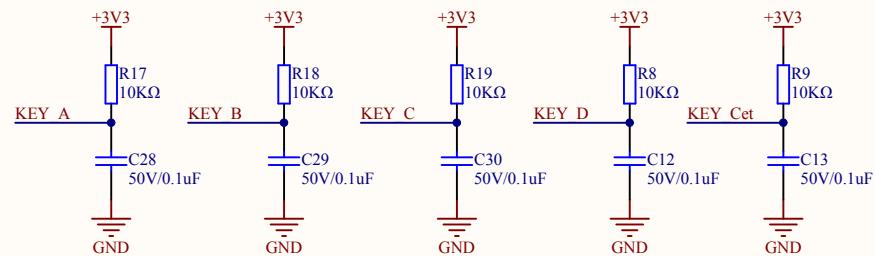
2

3

4



PA0	KEY_A
PC13	KEY_B
PB14	KEY_C
PC5	KEY_D
PC4	KEY_Cet

KEY**Company Name: GigaDevice****File Name: Extension**

Revision: 1.0

Data: 2013-8-20

Author: XuFei

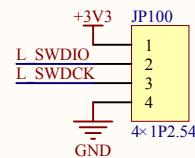
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2

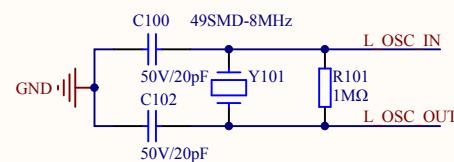
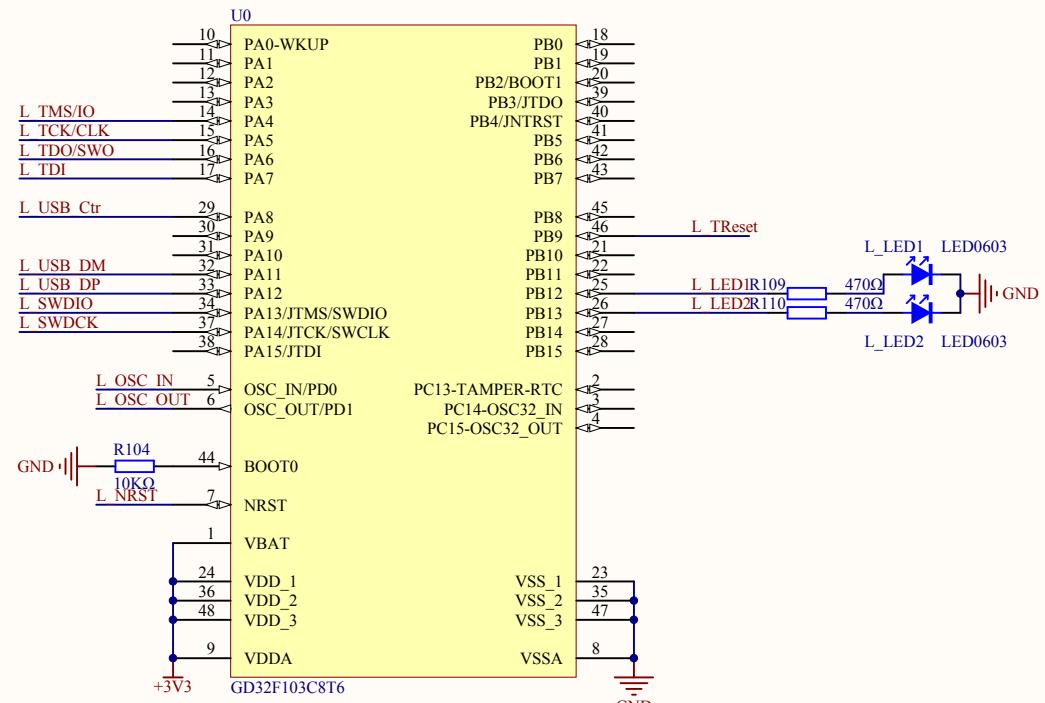
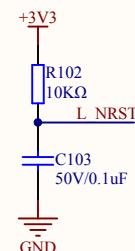
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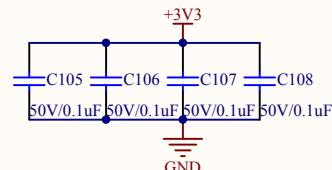
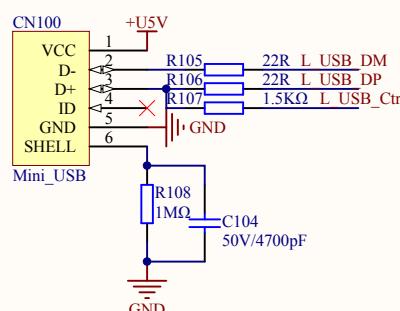
A

MCU SWD

B

**Reset**

C

**Company Name: GigaDevice****File Name: GDLink**

Revision: 1.0

Data: 2019-6

Author: XuFei

1

2

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4

A

B

C

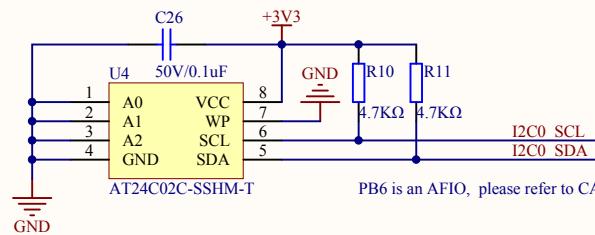
D

A

B

C

D

I2C

Company Name: GigaDevice

File Name: I2C

Revision: 1.0	Data: 2019-6	Author: XuFei
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A

A

B

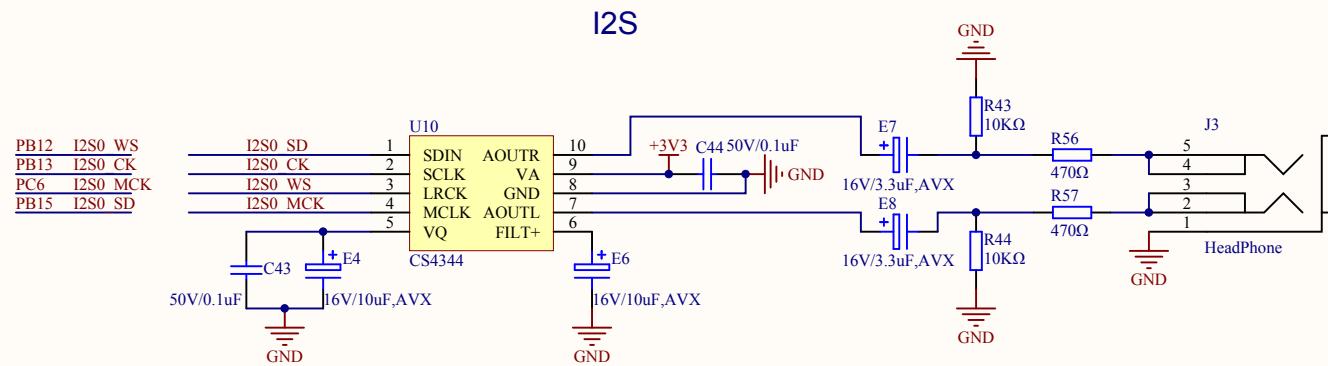
B

C

C

D

D



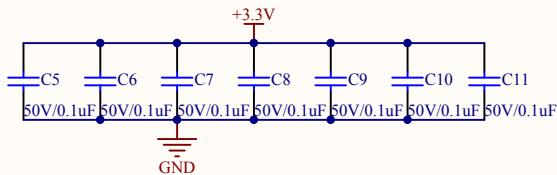
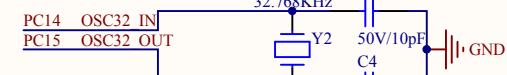
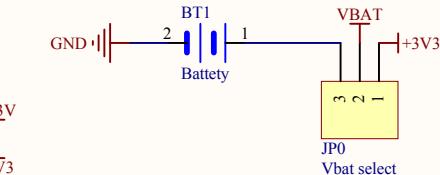
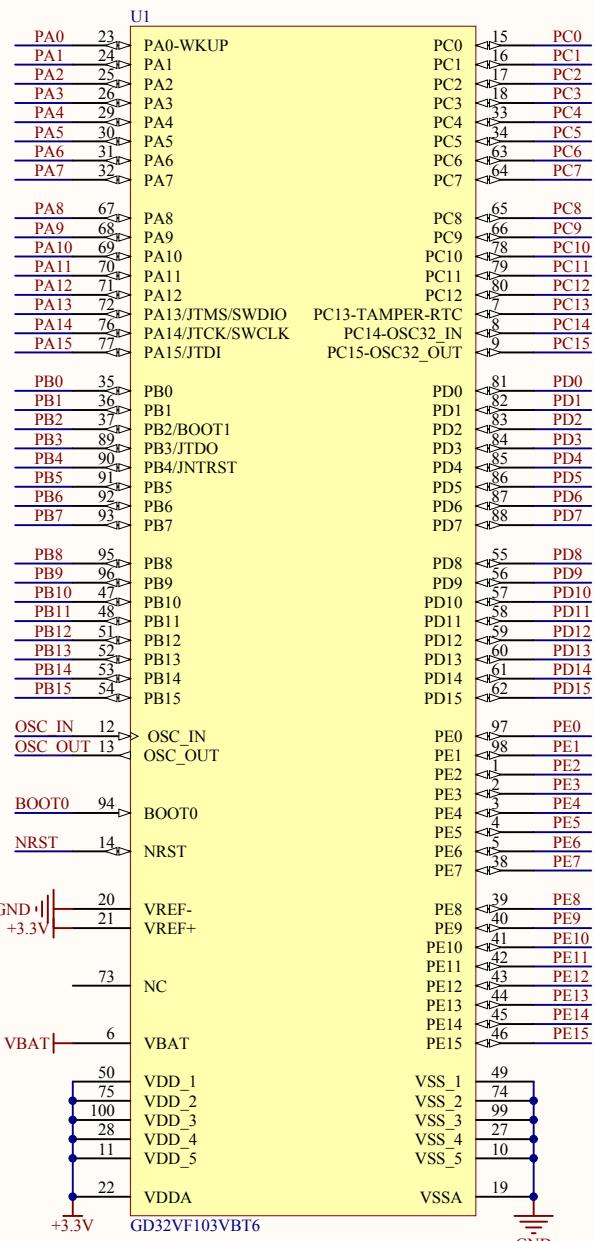
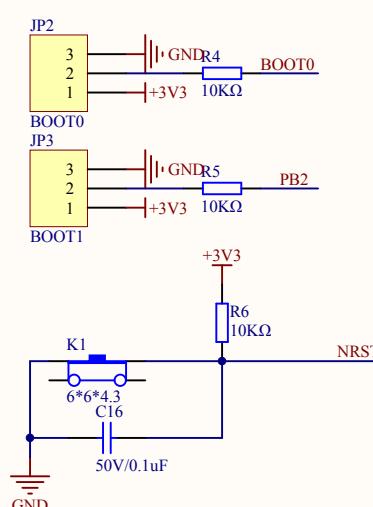
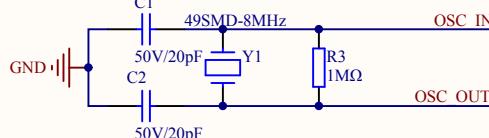
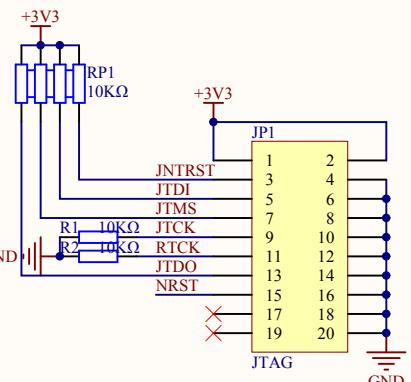
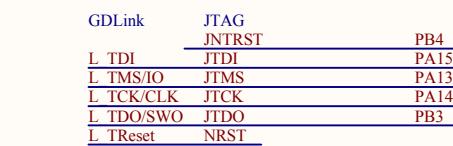
Company Name: GigaDevice

File Name: I2S

Revision: 1.0

Data: 2019-6

Author: Lixin



Company Name: GigaDevice

File Name: MCU

Revision: 1.0

Data: 2019-6

Author: XuFei

A

A

B

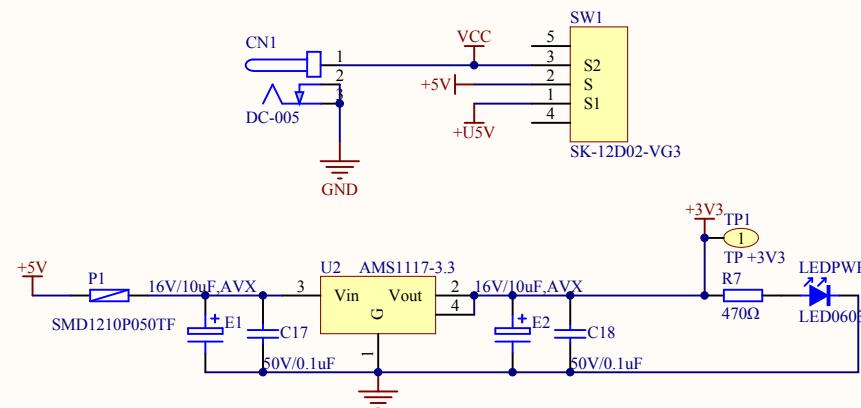
B

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D



Company Name: GigaDevice

File Name: Power

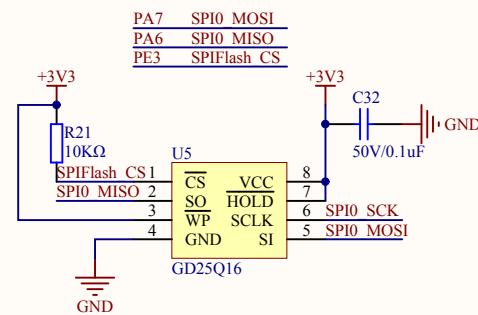
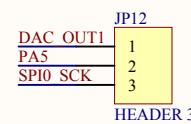
Revision: 1.0

Data: 2019-6

Author: XuFei

SPI Flash

Short JP12(1,2) for DAC function
Short JP12(2,3) for SPI0 function



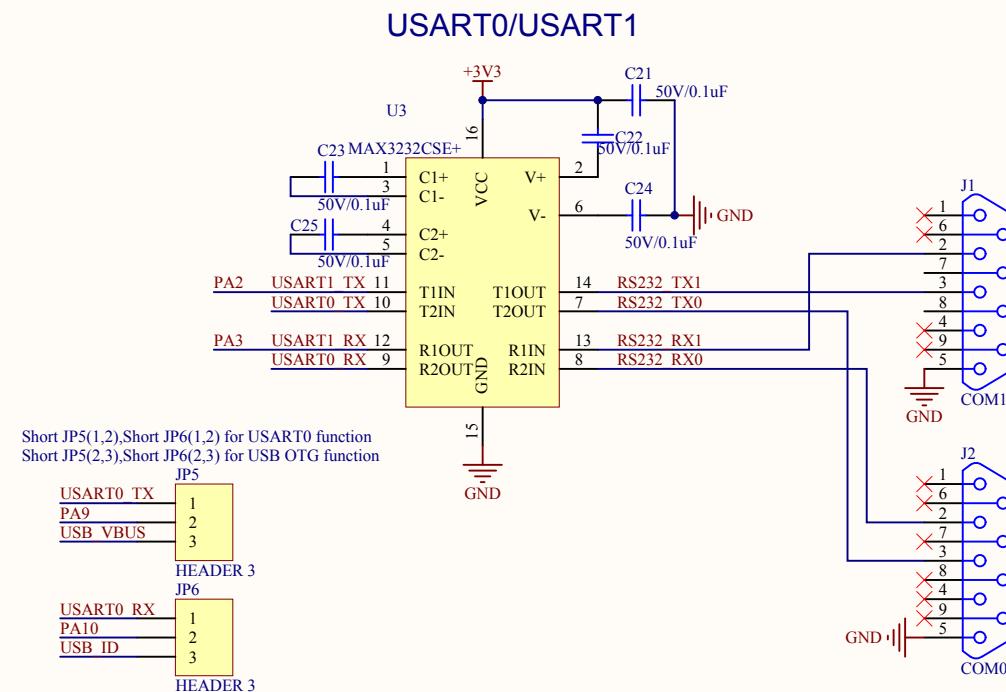
Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2019-6

Author: XuFei



Company Name: GigaDevice

File Name: USART

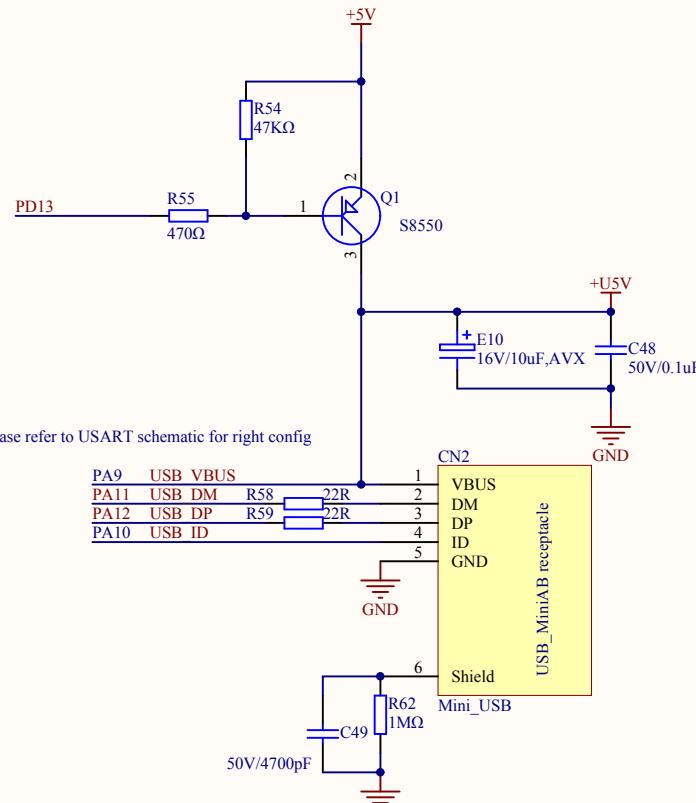
Revision: 1.0

Data: 2013-8-20

Author: XuFei

A

A



Company Name: GigaDevice

File Name: USB_OTG

Revision: 1.0	Data: 2019-6	Author: Xufei
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