

A

A

B

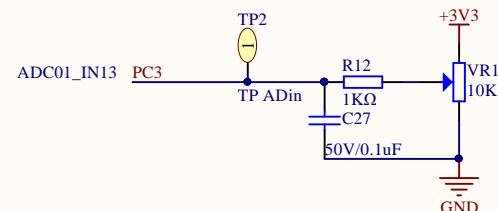
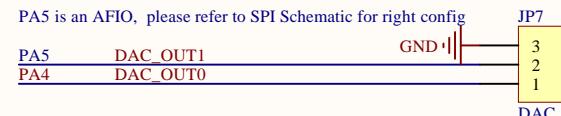
B

C

C

D

D

ADC**DAC**

Company Name: GigaDevice

File Name: AD_DA

Revision: 1.0

Data: 2022-7

Author: XuFei

A

A

B

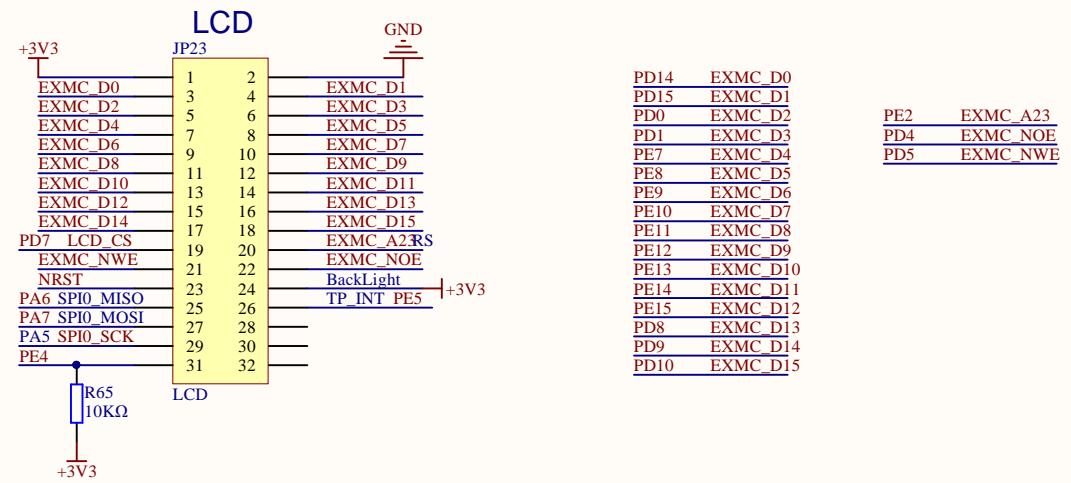
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C

C

D

D



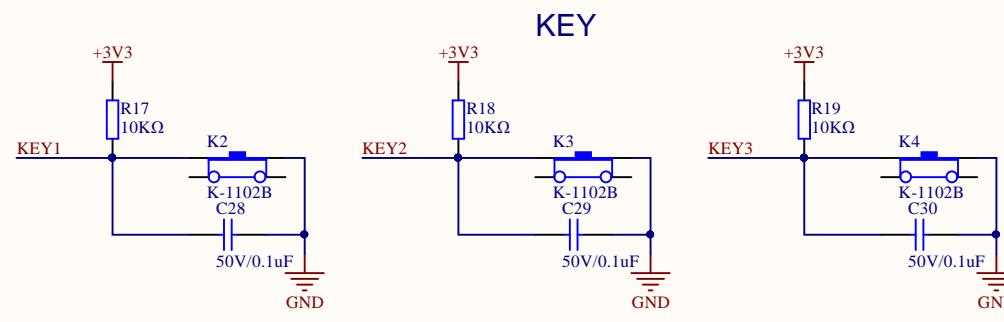
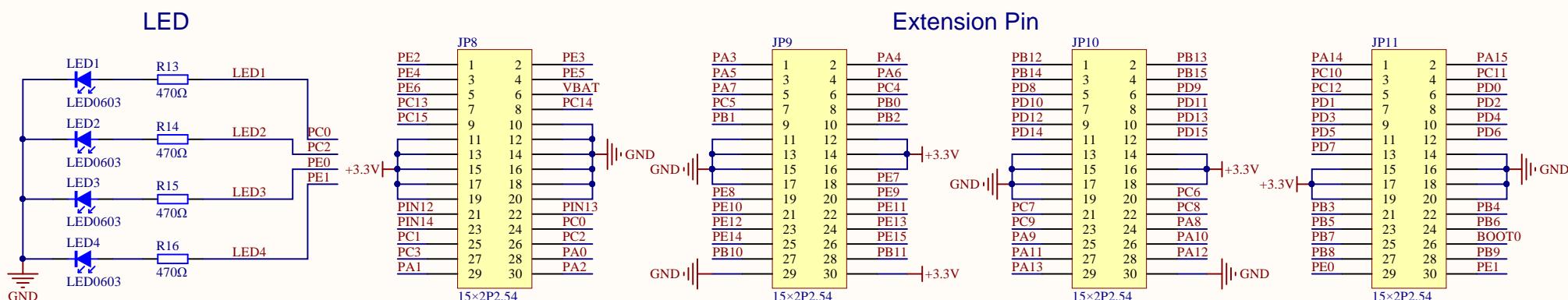
Company Name: GigaDevice

File Name: EXMC

Revision: 1.0

Data: 2022-7

Author: XuFei



PA0 KEY1
PC13 KEY2
PB14 KEY3

Company Name: GigaDevice

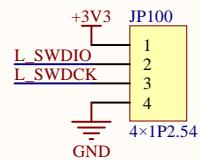
File Name: Extension

Revision: 1.0

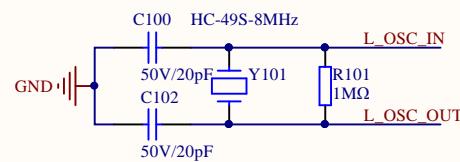
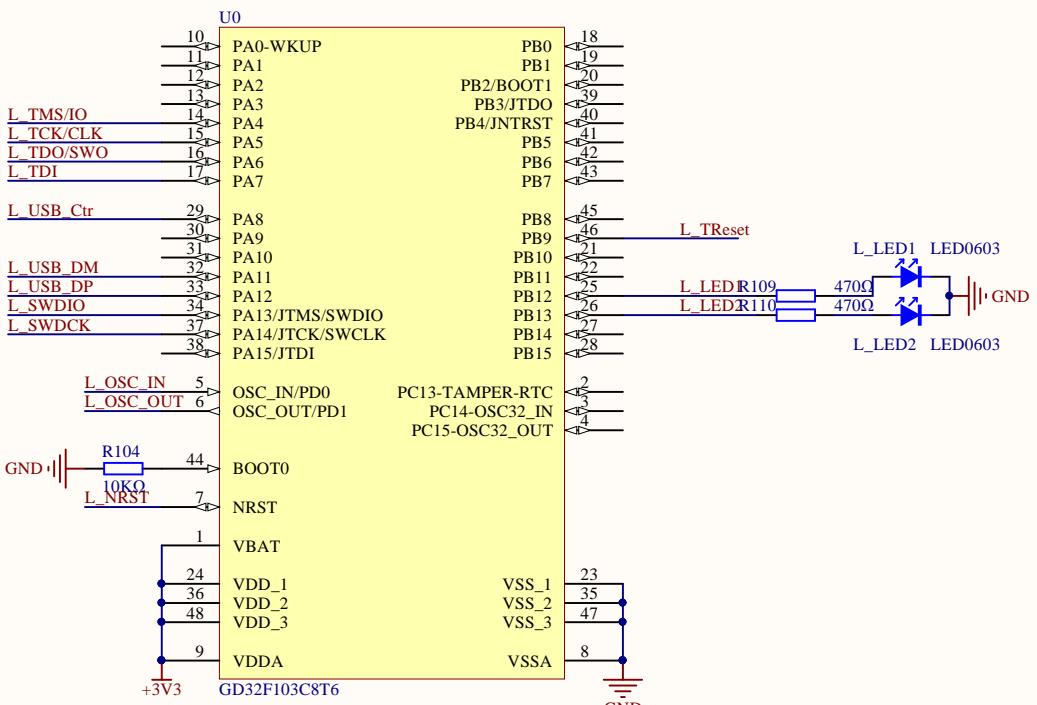
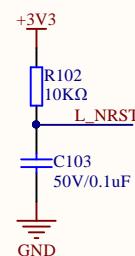
Data: 2022-7

Author: XuFei

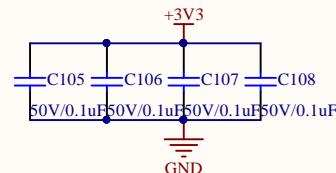
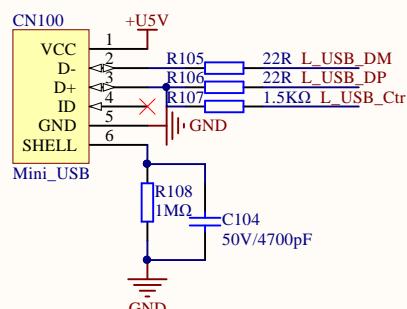
A

MCU SWD

B

**Reset**

C



Company Name: GigaDevice

File Name: GDLink

Revision: 1.0

Data: 2022-7

Author: XuFei

A

A

B

B

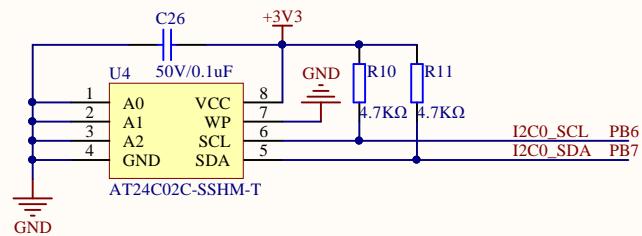
C

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D

D

I2C



Company Name: GigaDevice

File Name: I2C

Revision: 1.0

Data: 2022-7

Author: XuFei

A

A

B

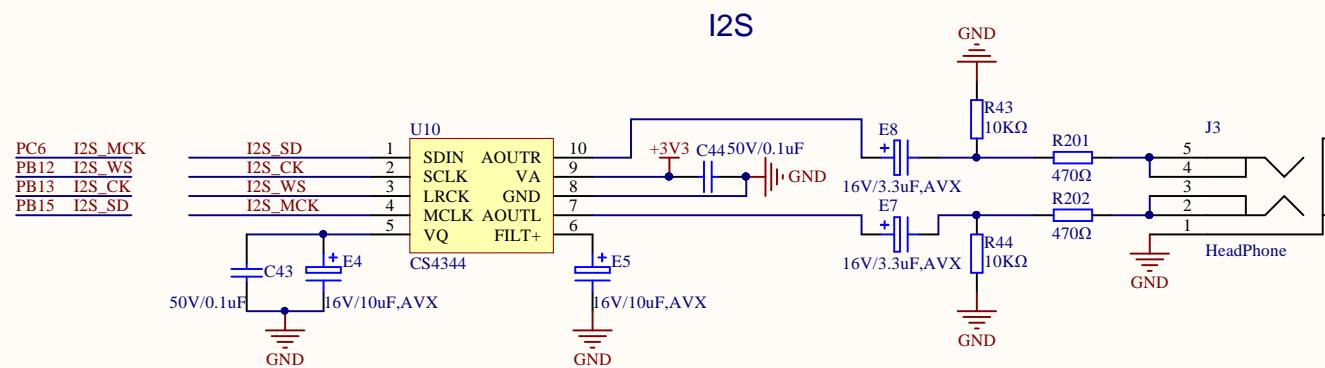
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D



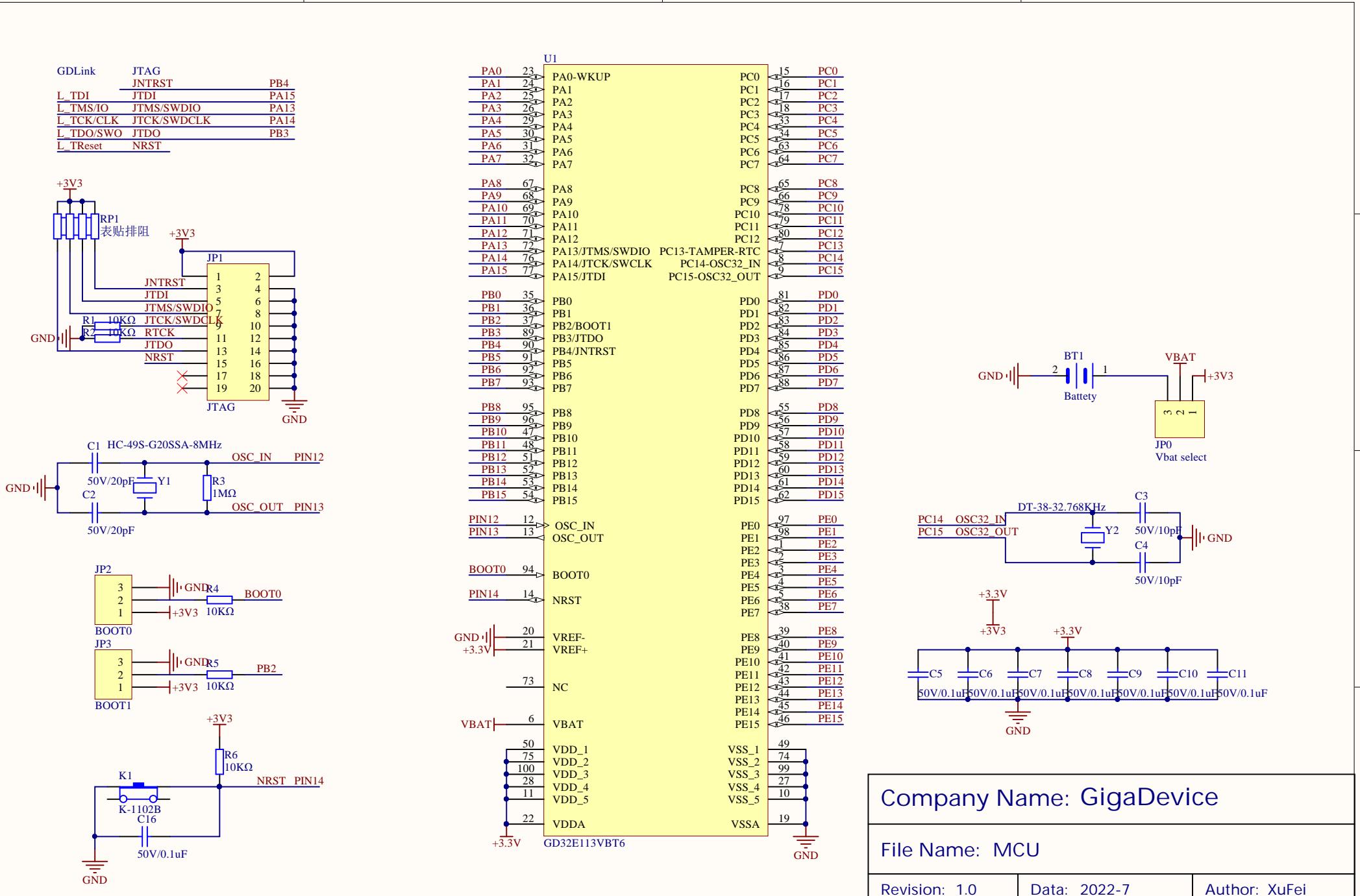
Company Name: GigaDevice

File Name: I2S

Revision: 1.0

Data: 2022-7

Author: Lixin



A

A

B

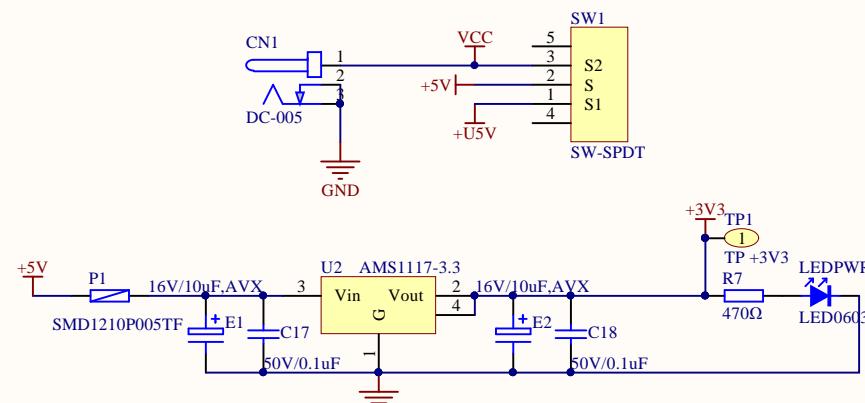
B

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D



Company Name: GigaDevice

File Name: Power

Revision: 1.0	Data: 2022-7	Author: XuFei
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A

A

B

B

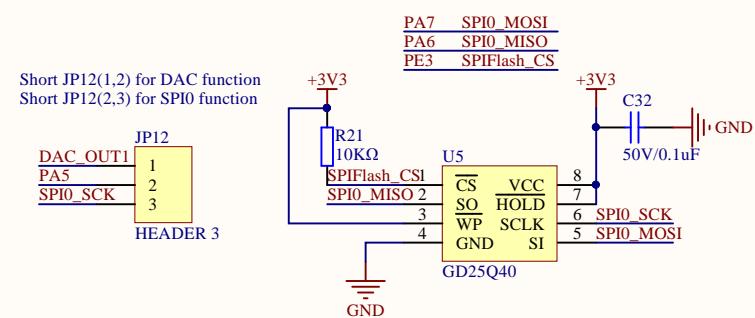
C

C

D

D

SPI Flash



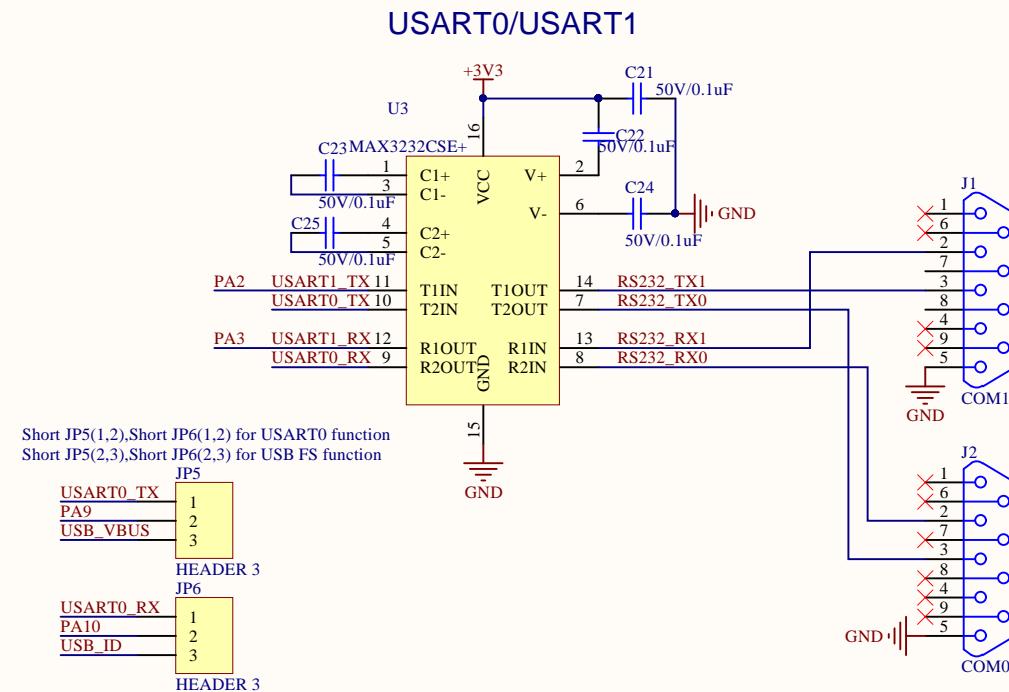
Company Name: GigaDevice

File Name: SPI

Revision: 1.0

Data: 2022-7

Author: XuFei



Company Name: GigaDevice

File Name: USART

Revision: 1.0

Data: 2022-7

Author: XuFei

A

A

B

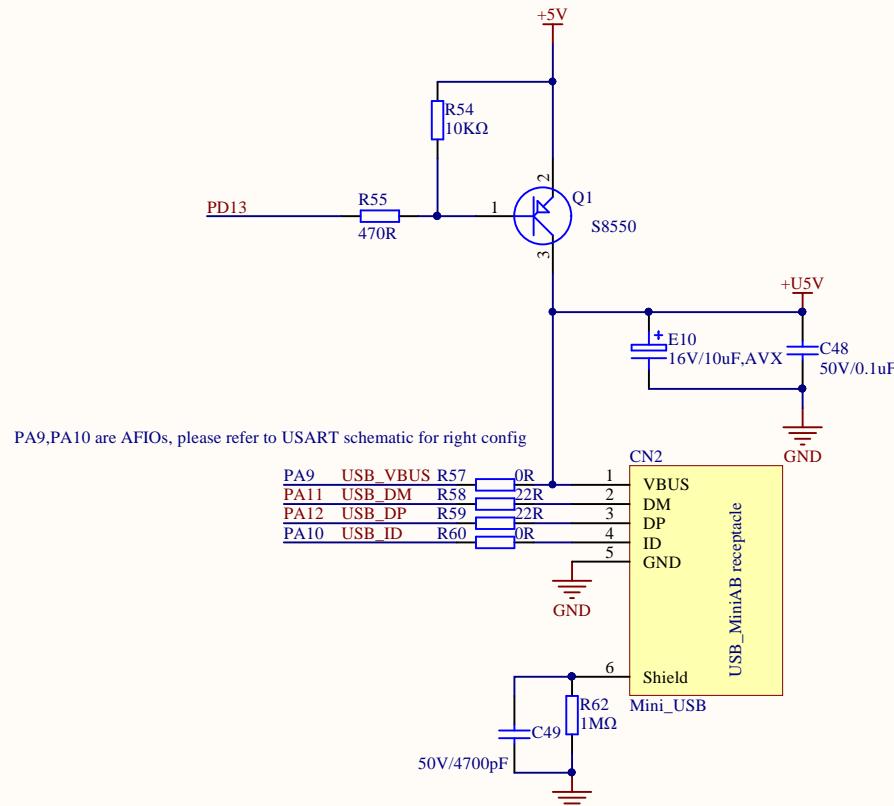
B

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C

D

D



Company Name: GigaDevice

File Name: USBFS

Revision: 1.1

Data: 2022-7

Author: Xufei

