

1

2

3

4

A

A

B

B

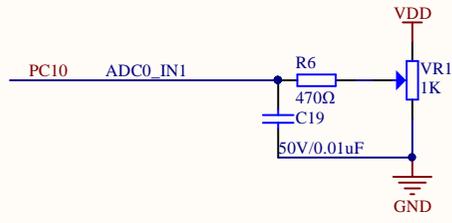
C

C

D

D

ADC



Company Name: GigaDevice

File Name: ADC

Revision: 1.0

Data: 2022-1

Author: Xufei

1

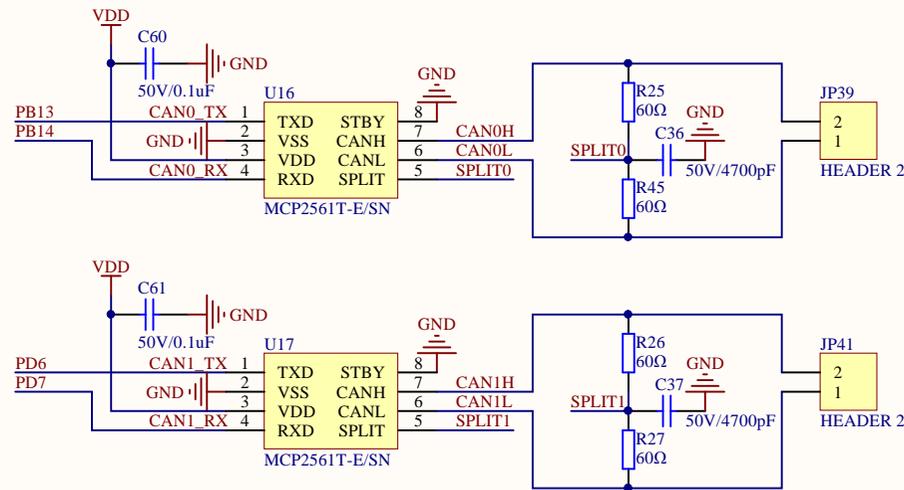
2

3

4

CAN

Note: CAN Bus be used when VDD=5V



Company Name: GigaDevice		
File Name: CAN		
Revision: 1.0	Data: 2022-1	Author: XuFei

1

2

3

4

A

A

B

B

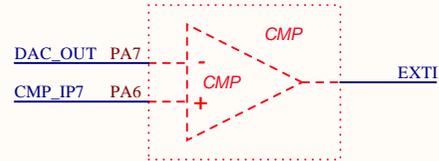
C

C

D

D

CMP



Company Name: GigaDevice

File Name: CMP

Revision: 1.0

Data: 2022-1

Author: Xufei

1

2

3

4

1

2

3

4

A

A

B

B

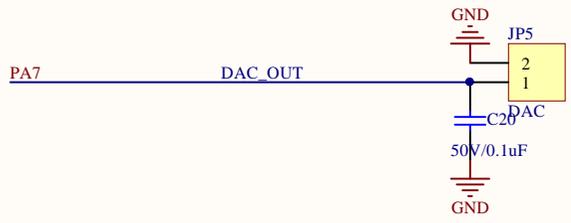
C

C

D

D

DAC



Company Name: GigaDevice

File Name: DAC

Revision: 1.0

Data: 2022-1

Author: Xufei

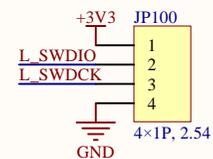
1

2

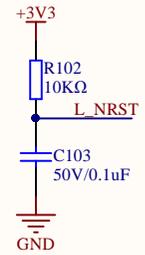
3

4

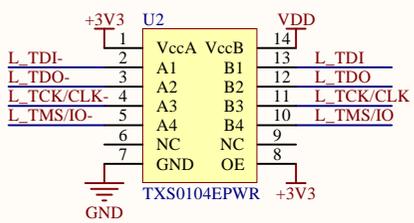
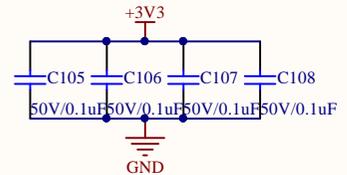
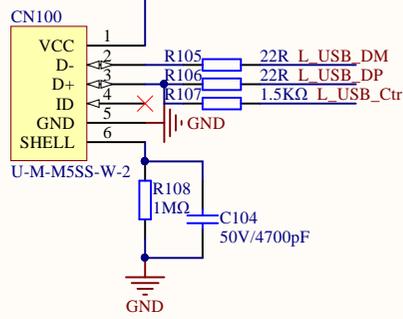
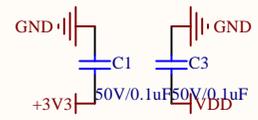
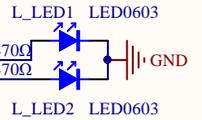
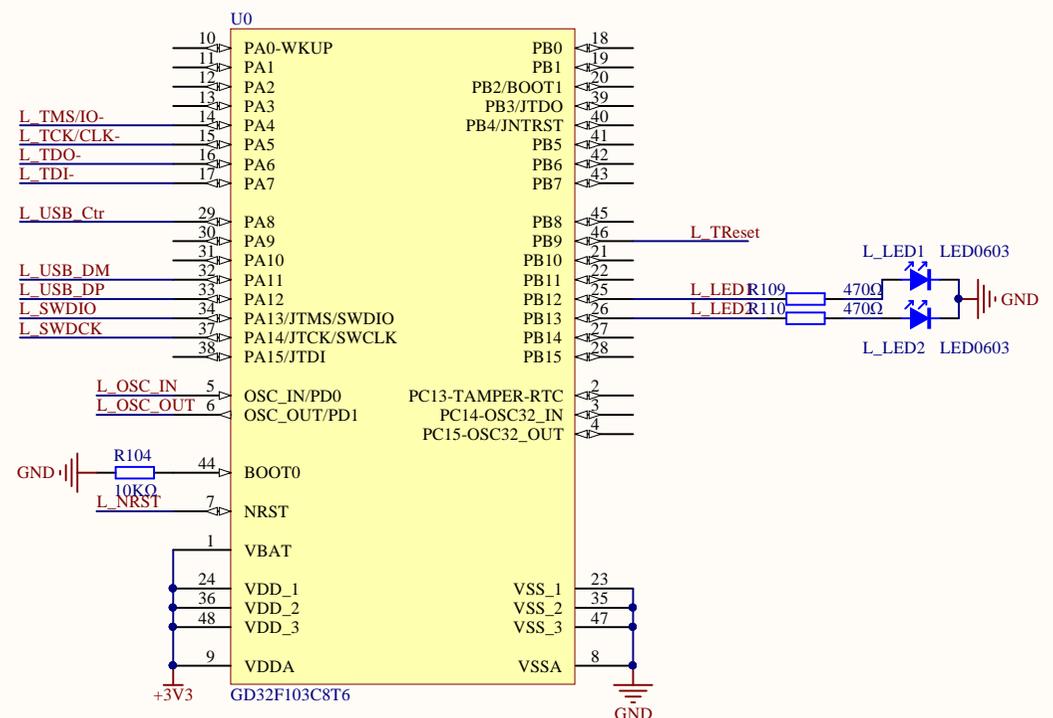
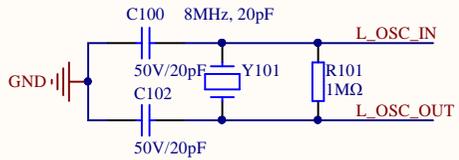
MCU SWD



Reset



HXTAL



Company Name: GigaDevice

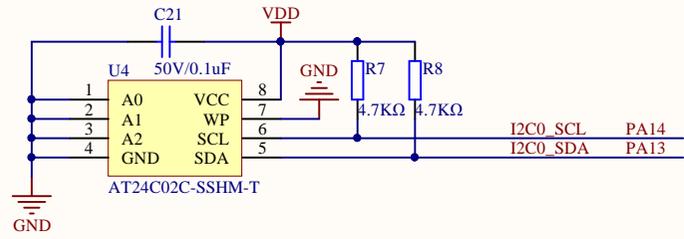
File Name: GDLink

Revision: 1.0

Data: 2022-1

Author: XuFei

I2C



Company Name: GigaDevice

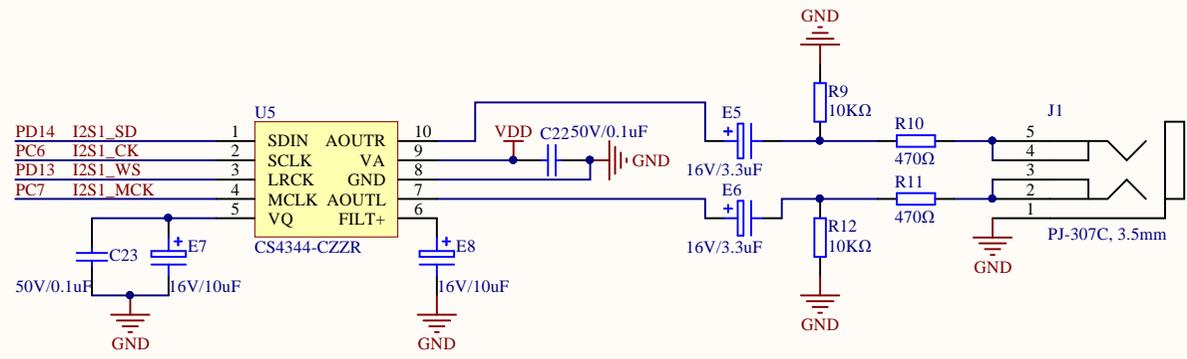
File Name: I2C

Revision: 1.0

Data: 2022-1

Author: Xufei

I2S



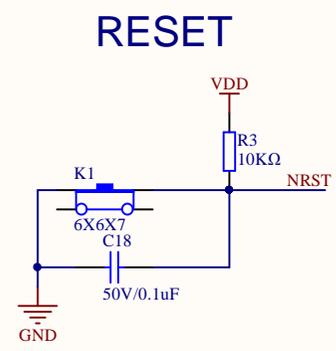
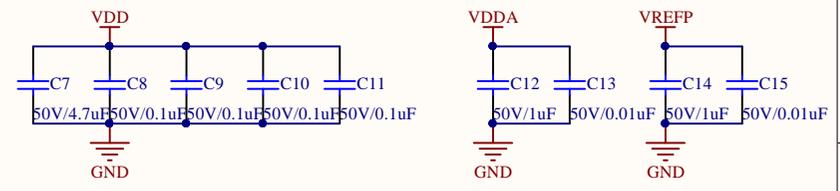
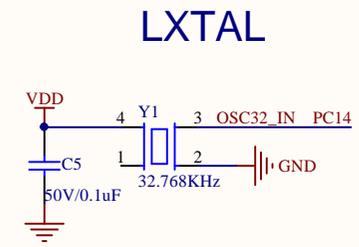
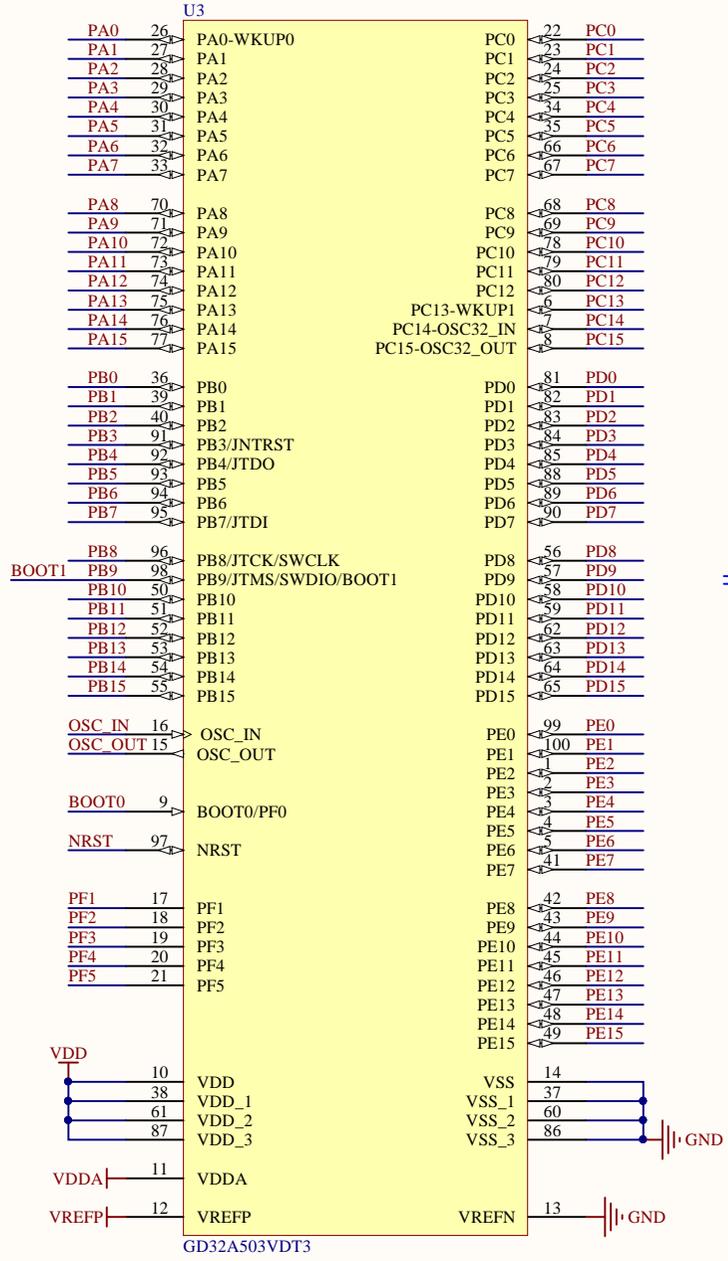
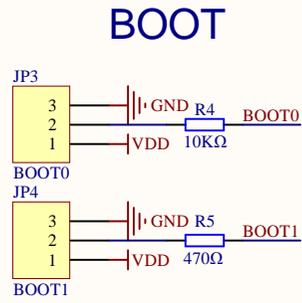
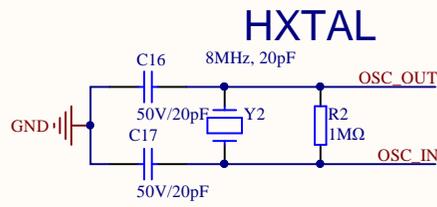
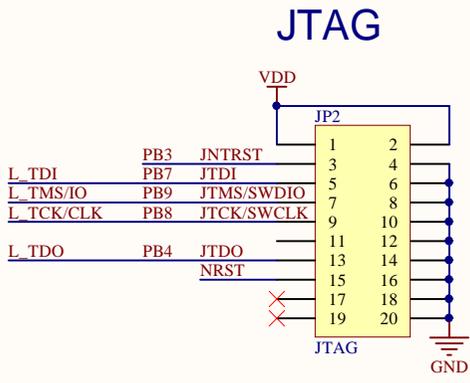
Company Name: GigaDevice

File Name: I2S

Revision: 1.0

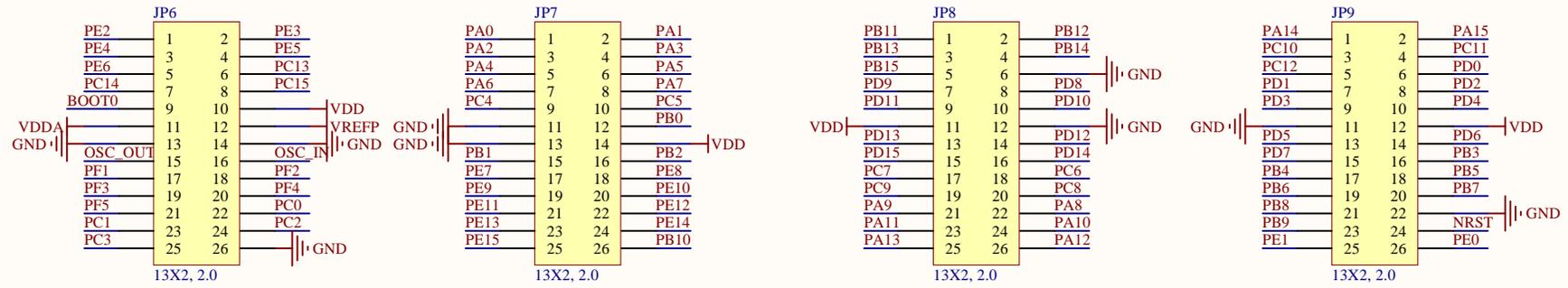
Data: 2022-1

Author: Xufei

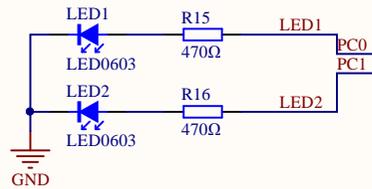


Company Name: GigaDeice		
File Name: MCU		
Revision: 1.0	Data: 2022-3	Author: Xufei

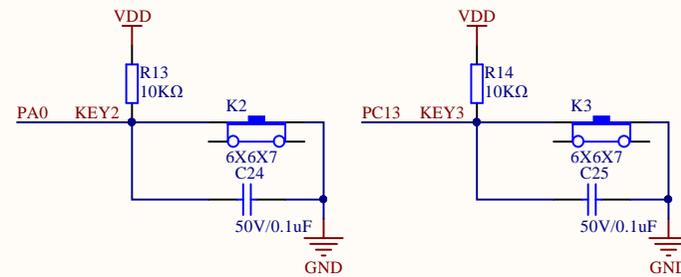
Extension Pin



LED



KEY



Company Name: GigaDevice

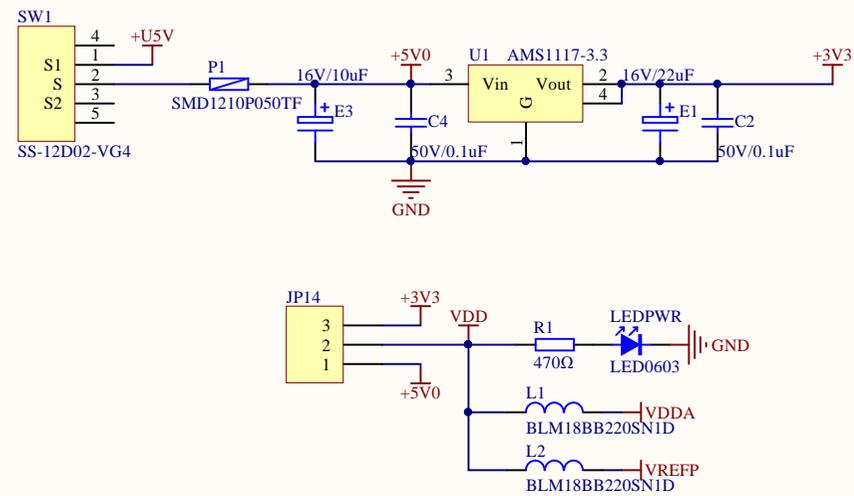
File Name: Peripherals

Revision: 1.0

Data: 2022-1

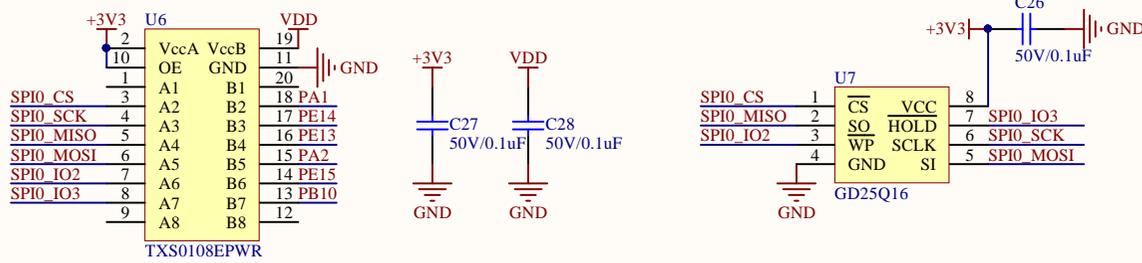
Author: Xufei

POWER



Company Name: GigaDevice		
File Name: POWER		
Revision: 1.0	Data: 2022-1	Author: Xufei

QSPI



Company Name: GigaDevice

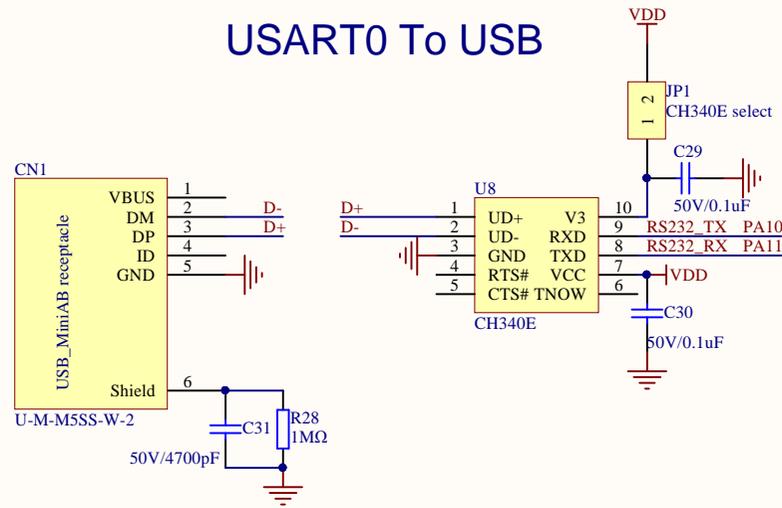
File Name: SPI

Revision: 1.0

Data: 2022-1

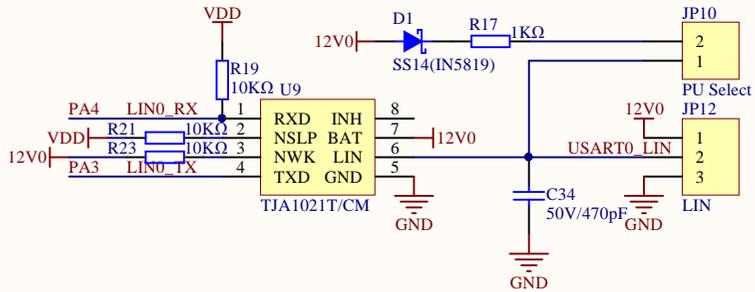
Author: Xufei

USART0 To USB

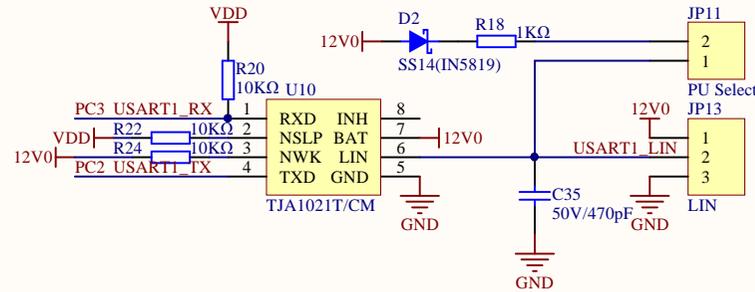


Note: JP1 CONNECT WHEN VDD=3.3V
 Note: JP1 DISCONNECT WHEN VMCU=5V

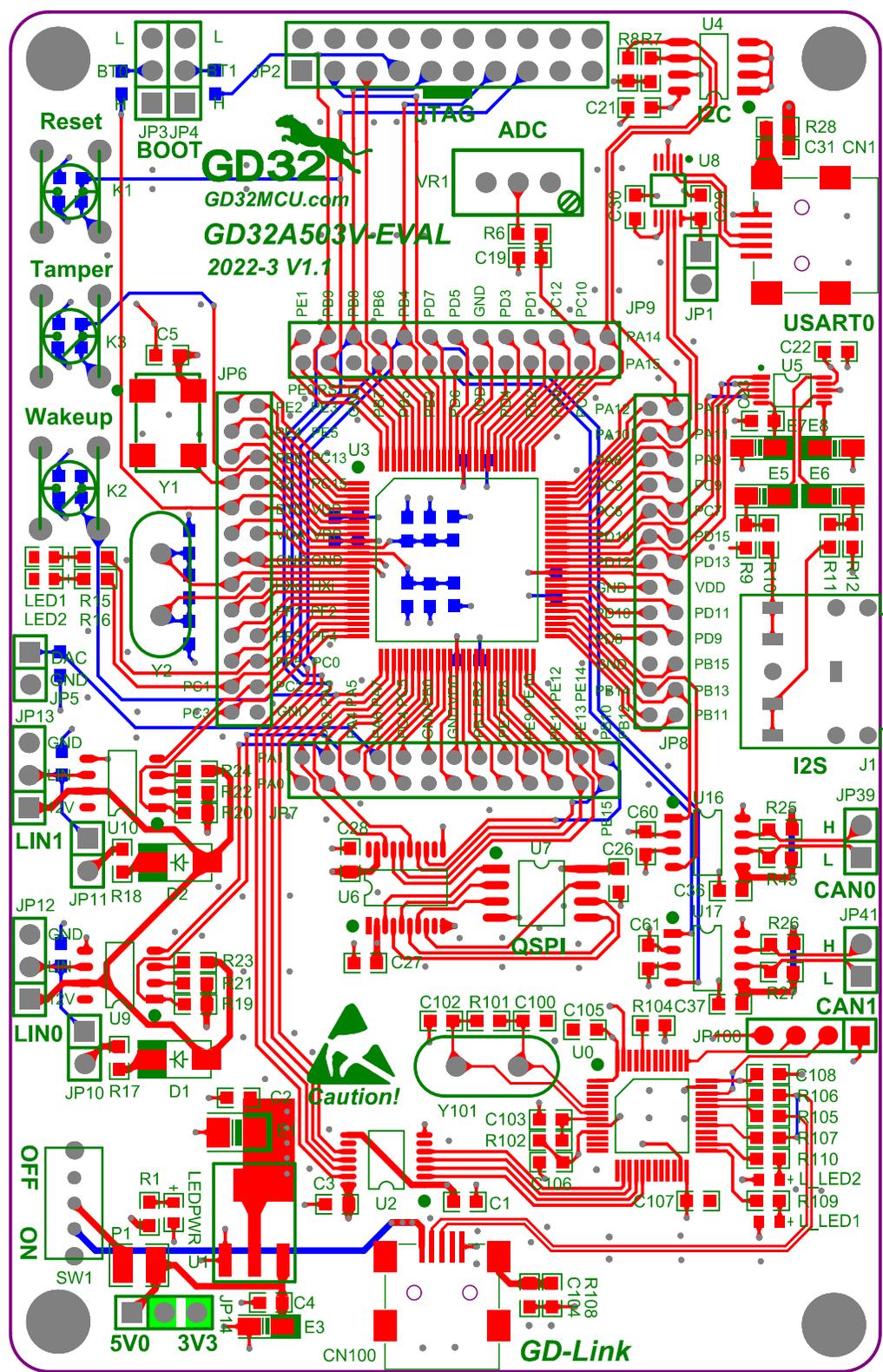
USART0-LIN



USART1-LIN



Company Name: GigaDevice		
File Name: USART		
Revision: 1.0	Data: 2022-1	Author: Xufei



GD32MCU.com

GD32A503V-EVAL

2022-3 V1.1

Caution!

GD-Link

Reset

Tamper

Wakeup

LIN1

LIN0

OFF
ON

JTAG

ADC

USART0

I2S

CAN0

CAN1

5V0 3V3

CN100

