

GigaDevice Semiconductor Inc.

Device limitations of GD32L233

Errata Sheet

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1. Introduction

This document applies to GD32L233 product series, as shown in [Table 1-1. Applicable products](#). It provides the technical details that need to be paid attention to in the process of using GD32 MCU, as well as solutions to related problems.

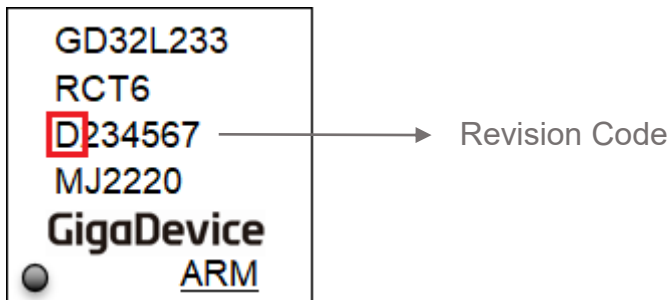
Table 1-1. Applicable products

Type	Part Numbers
MCU	GD32L233xx series

1.1. Revision identification

The device revision can be determined by the mark on the top of the package. The 1st code on the line 3 of the mark represents product revision code. As the picture shown in [Figure 1-1. Device revision code of GD32L233](#).

Figure 1-1. Device revision code of GD32L233



1.2. Summary of device limitations

The device limitations of GD32L233 are shown in [Table 1-2. Device limitations](#), please refer to section 2 for more details.

Table 1-2. Device limitations

Module	Limitations	Workaround		
		Rev. Code B	Rev. Code C	Rev. Code D
PMU	<i>FWDGTRSTF flag can not be set in Deep-sleep mode</i>	N	N	N
SLCD	<i>Do not support the use of internal voltage source</i>	Y	Y	--

Note:

Y = Limitation present, workaround available

N = Limitation present, no workaround available

'--' = Limitation fixed

2. Descriptions of device limitations

2.1. PMU

2.1.1. FWDGTRSTF flag can not be set in Deep-sleep mode

Description & impact

FWDGTRSTF bit can not be set by hardware when mcu is in Deep-sleep / Deep-sleep 1 / Deep-sleep 2 mode and FWDGT reset is occurred.

Workarounds

Not available.

2.2. SLCD

2.2.1. Do not support the use of internal voltage source

Description & impact

SLCD only supports the use of external voltage source but internal voltage source.

Workarounds

Use the external voltage source.

3. Revision history

Table 3-1. Revision history

Revision No.	Description	Date
1.0	Initial Release	Nov.11 2022
1.1	Add limitations of Rev. Code D	Mar.28 2023
1.2	Update note of chapter 1.2	Apr.4 2023

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