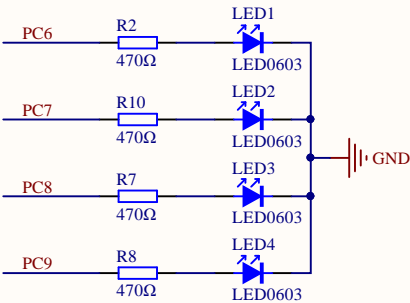
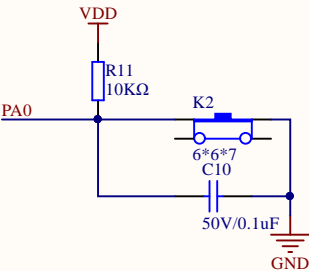


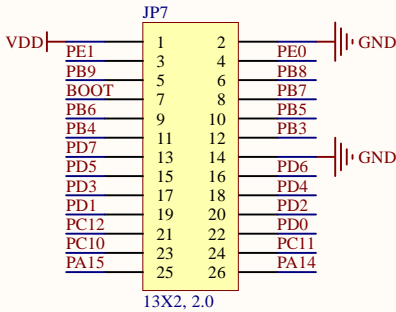
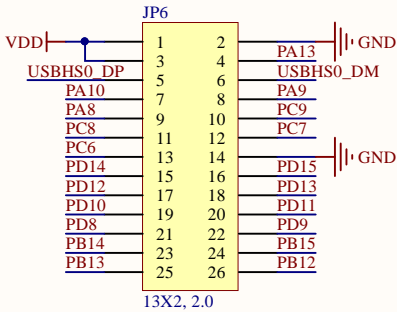
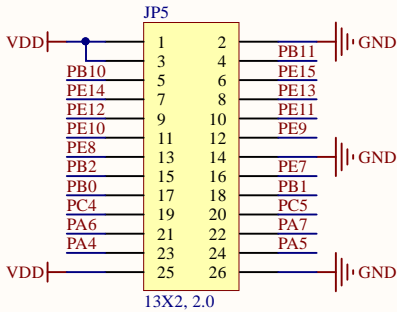
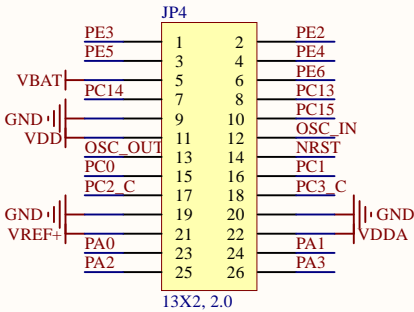
LED



KEY



Extension Pin



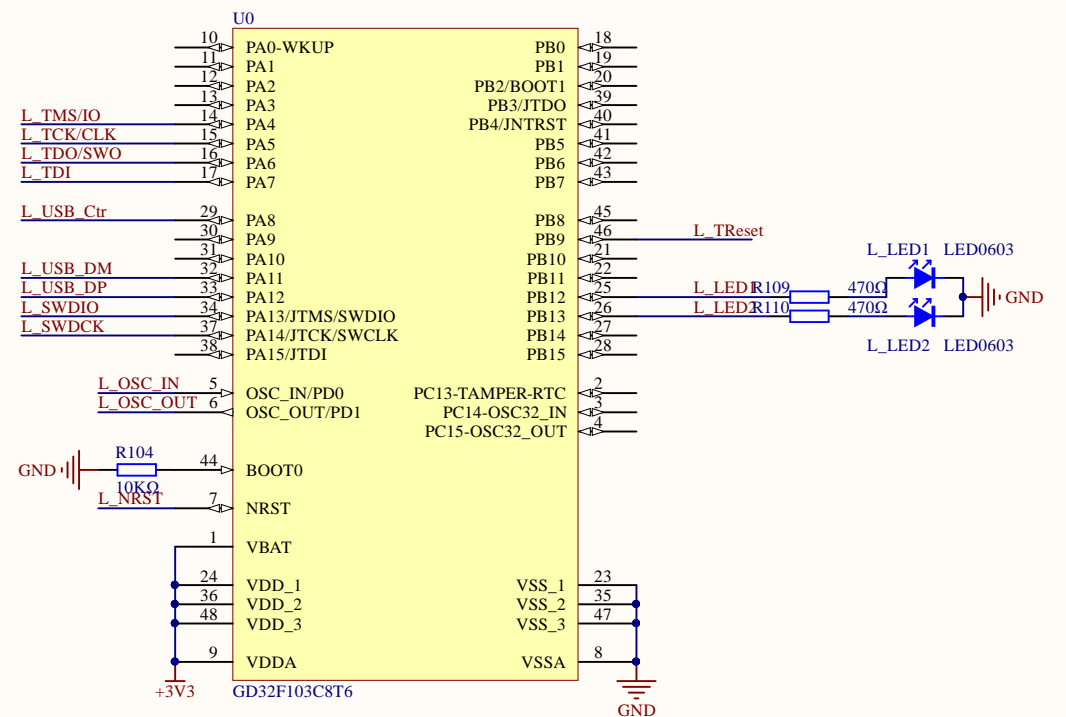
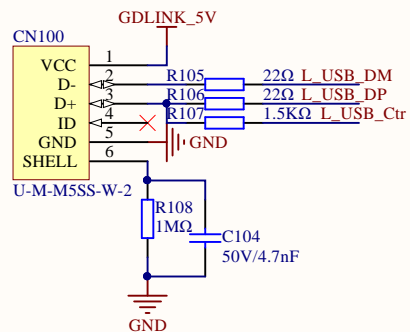
Company Name: GigaDevice

File Name: Extension

Revision: 1.5

Date: 2024-12

Author: BOYA.JIANG



Company Name: GigaDevice

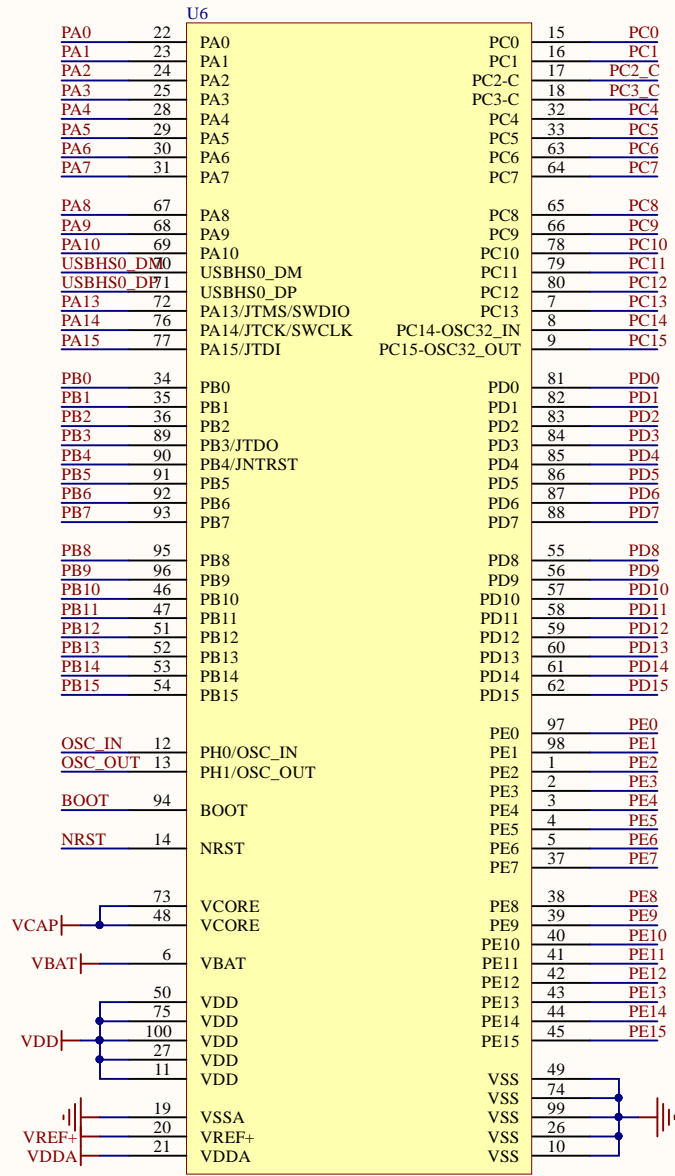
File Name: GDLink

Revision: 1.5

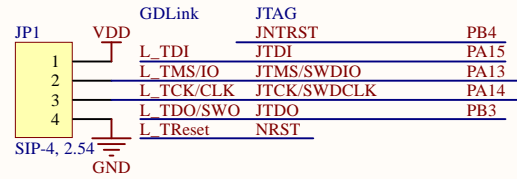
Date: 2024-12

Author: boya.jiang

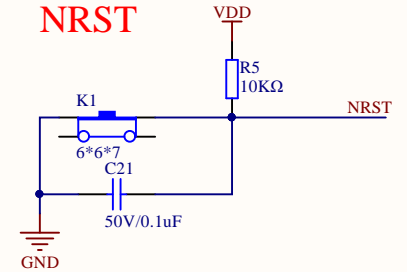
MCU



JTAG/SWD

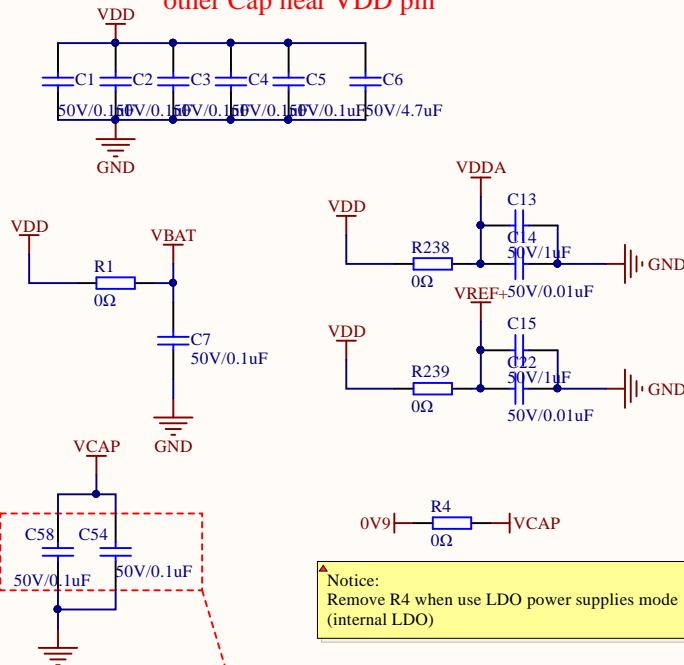


NRST

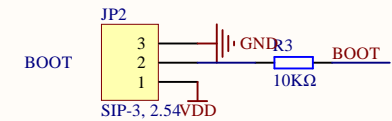


Filter

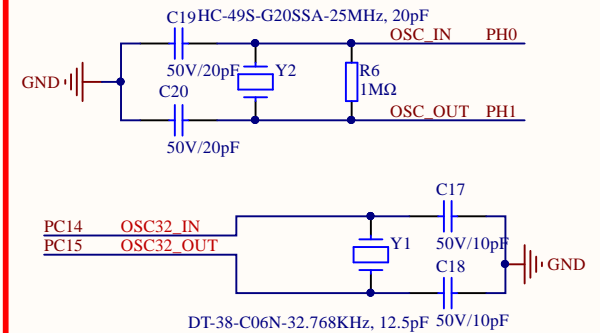
C6 first across VDD NET
other Cap near VDD pin



BOOT



HXTAL&LXTAL



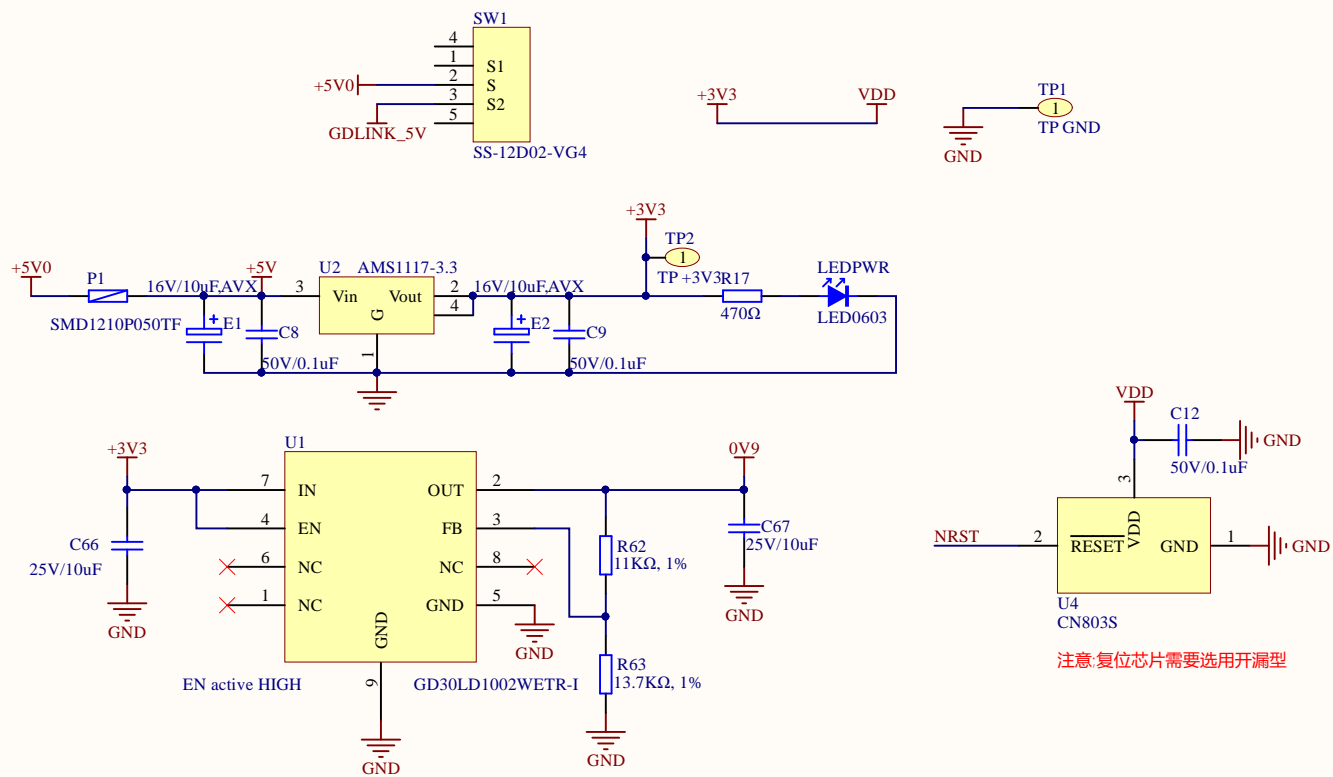
Company Name: GigaDevice

File Name: MCU

Revision: 1.5

Date: 2024-12

Author: BOYA.JIANG



反馈电阻R62、R63应至少为1%精度

Company Name: GigaDevice

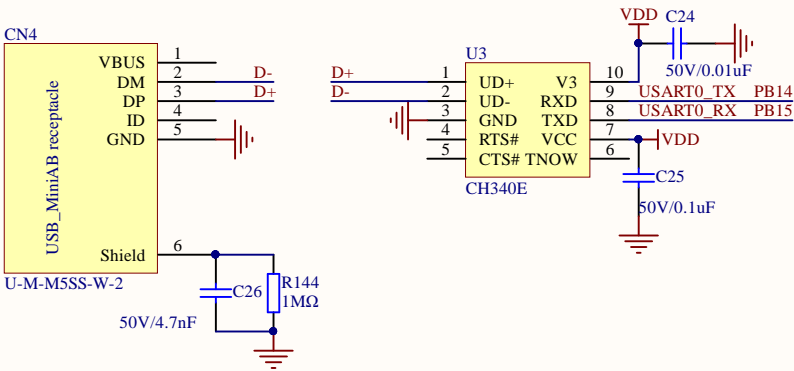
File Name: Power

Revision: 1.5

Date: 2024-12

Author: BOYA.JIANG

USART0 To USB



Company Name: GigaDevice		
File Name: USART		
Revision: 1.5	Date: 2024-12	Author: BOYA.JIANG

