

板材信息（参考）

板材 FR4		Typical (mil)	Unit
Solder Mask		0.7	mil
Copper Plated	0.5 oz	0.7	mil
Copper foil	0.5 oz	0.7	mil
PP	6.6mil	6.6	mil
Copper foil	1 oz	1.4	mil
Core (7628*5)	12 mil	12	mil
Copper foil	1 oz	1.4	mil
PP	6.6mil	6.6	mil
Copper foil	0.5 oz	0.7	mil
Copper Plated	0.5 oz	0.7	mil
Solder Mask		0.7	mil
Total Board Thickness		32.200	mil
Total Board Thickness		0.818	mm

阻抗线宽（参考）

单端走线			
线宽(mil)	所在/参考层	ohm 值	仿真值
11(gap14mil)	L1/L2	50	50.02

The screenshot displays the Polar SI9000 PCB Transmission Line Field Solver interface. The main window shows a 3D model of a Coated Coplanar Waveguide With Ground 1B. The model parameters are listed on the right, including Substrate 1 Height (H1), Substrate 1 Dielectric (Er1), Lower Trace Width (W1), Upper Trace Width (W2), Ground Strip Separation (D1), Trace Thickness (T1), Coating Above Substrate (C1), Coating Above Trace (C2), and Coating Dielectric (CEr). The Impedance (Zo) is calculated as 50.02. The software also includes a Notes section and a bottom bar for Lossless and Frequency Dependent Calculations.

Parameter	Value	Tolerance	Minimum	Maximum	Calculate
Substrate 1 Height (H1)	6.6000	+/- 0.0000	6.6000	6.6000	Calculate
Substrate 1 Dielectric (Er1)	4.2000	+/- 0.0000	4.2000	4.2000	Calculate
Lower Trace Width (W1)	11.0000	+/- 0.0000	11.0000	11.0000	Calculate
Upper Trace Width (W2)	10.5000	+/- 0.0000	10.5000	10.5000	Calculate
Ground Strip Separation (D1)	14.0000	+/- 0.0000	14.0000	14.0000	Calculate
Trace Thickness (T1)	1.4000	+/- 0.0000	1.4000	1.4000	Calculate
Coating Above Substrate (C1)	0.7000	+/- 0.0000	0.7000	0.7000	Calculate
Coating Above Trace (C2)	0.7000	+/- 0.0000	0.7000	0.7000	Calculate
Coating Dielectric (CEr)	3.8000	+/- 0.0000	3.8000	3.8000	Calculate
Impedance (Zo)	50.02		50.02	50.02	[Calculate]