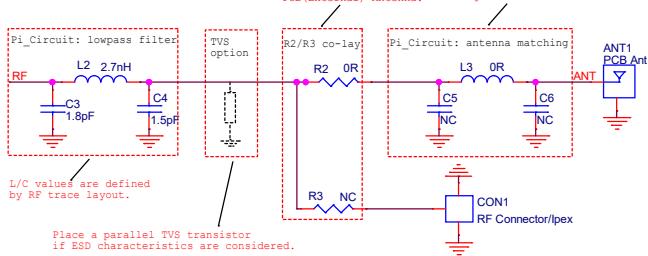


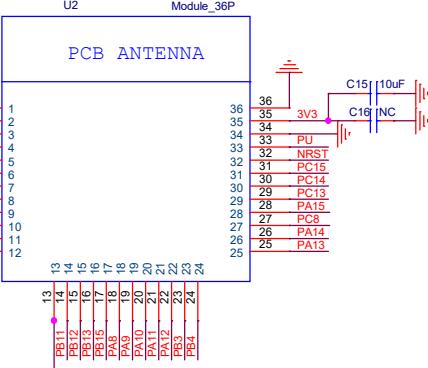
RF TRACE



## Module Pins

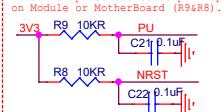
## GPIO PINMUX:

LogUart: PA6/PA7  
UART2 TX/RX  
JTAG: PA13/PA14/PA15/PB3/PB4  
JTMS/JTCK/JTDI/JTDO/JNTRST  
BOOT: PC8/PB1  
BOOT0/BOOT1



Latch-up pins

1 PU&NRST pins need to be pull-up



2 BOOT0 pin need to be pull-down  
on Module or MotherBoard  
for the chip to boot from sip flash

40MHz Crystal

<b>GIGA DEVICE</b>		
<b>Title</b>	GD32VW553_MD1	
<b>Size</b>	Document Number	Rev
Custom	MODULE	1.0
<b>Date:</b>	Friday, December 15, 2023	Sheet
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