GigaDevice Semiconductor Inc.

Migration from GD32E230 series to GD32E235 series

Application Note AN169

Revision 1.0

(Aug.2024)



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1. Introduction

The GD32E235xx device is a new 32-bit general-purpose microcontroller based on the ARM® Cortex®-M23 core. It has very high resource similarity with the GD32E230xx series, and this application note aims to help you quickly port applications from the GD32E230xx series microcontroller to the GD32E235xx series microcontroller.

In order to make better use of the information in this application note, you need to download it from the website www.GD32MCU.com, such as datasheet, user manual, official code and various development tools.



2. Introduction to hardware differences

The definition of pins for GD32E235xx series chips and GD32E230xx series devices in the same packaging is completely the same.



3.

Comparison of resource and peripheral

The internal resources of GD32E235xx and GD32E230xx devices with the same suffix are consistent. In addition, GD32E235 has added a chip model with a FLASH capacity of 128KB and a RAM capacity of 16KB: GD32E235xB. As shown in <u>Table 3-1. GD32E235xB devices</u> <u>features and peripheral list</u>.

	Part Number	GD32E235xx						
		KBU6	KBT6	CBT6				
FLASH(KB)		128(1)	128(1)	128(1)				
-	SRAM(KB)	16 ⁽¹⁾	16 ⁽¹⁾	16 ⁽¹⁾				
	General	5	5	5				
	timer(16bit)	(2, 13-16)	(2, 13-16)	(2, 13-16)				
	Advanced	1	1	1				
γ	timer(16bit)	(0)	(0)	(0)				
mer	SysTick	1	1	1				
F	Basic	1	1	1				
	timer(16bit)	(5)	(5)	(5)				
	Watchdog	2	2	2				
	RTC	1	1	1				
	USART	2	2	2				
Ϊţ		(0-1)	(0-1)	(0-1)				
sctiv	I2C	2	2	2				
onne		(0-1)	(0-1)	(0-1)				
ŏ	SPI/I2S	2/1	2/1	2/1				
		(0-1)/(0)	(0-1)/(0)	(0-1)/(0)				
	GPIO	27	25	39				
	CMP	1	1	1				
	EXTI	16	16	16				
	Units	1	1	1				
	Channels	10	10	10				
ADC	(External)							
	Channels	2	2	2				
	(Internal)							
	Package	QFN32	LQFP32	LQFP48				

 Table 3-1. GD32E235xB devices features and peripheral list

Note: (1) Compared with GD32E230x8 devices with the same packaging, these three new devices have the same resources except for the difference in FLASH capacity and SRAM capacity.



4. Program Migration

4.1 Differences in FMC

From the previous section, it can be seen that the FLASH capacity of GD32E235xB is larger, and there is a difference in the number of bits in the write protect register (FMC_WP) between GD32E235xx and GD32E230xx devices. As shown in *Figure 4-1. Comparison of Write Protect Registers (FMC WP) Differences between GD32E230xx and GD32E235xx devices*.

Figure 4- 1. Comparison of Write Protect Registers (FMC_WP) Differences between GD32E230xx and GD32E235xx devices



The new version firmware library GD32E23x_Firmware_Library(Version 2.0.0 and above) has made modifications to the "gd32e23x_fmc.h" and "gd32e23x_fmc.c" documents in order to be compatible with GD32E235xB. As shown in *Figure 4-2. The difference of "gd32e23x_fmc. h" document*. The detailed functions can be found in the "gd32e23x_fmc. c" document.

Figure 4- 2. The difference of "gd32e23x_fmc. h" document

/* FHC S# */			/* THC MP */		
#define FHC_WP_WP	BITS(8,15)	C	#define FMC_WP_WP	8ITS(0,51)	1
/* enable option byte wr fmc_state_enum ob_write_r	<pre>ite protection (OB_WP) */ protection_enable(uintis_t ob_wp);</pre>		/* enable option byte wr fmc_state_enum ob_write_	its protection (08_WP) */ protection_enable(uint12_t of	_wp);
/* get the FMC option byt wint16_t ob_write_protect	<pre>is write protection */ iion_get(void);</pre>		/* get the FMC option by uintiz_t ob_write_protect	ts writs protection */ tion_get(void);	
New version	firmware library		Old version	n firmware libr	ary



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When porting from GD32E230xx to GD32E235xx or from GD32E235xx to GD32E230xx, it is necessary to pay attention to whether the flash size is compatible and modify the two documents "gd32e23x_fmc. h" and "gd32e23x_fmc. c". When replacing chips with flash capacities below 128KB, if the files "gd32e23x_fmc. h" and "gd32e23x_fmc. c" are not modified, it will not affect the functionality of the chip.

4.2 Differences in Comparator

The hysteresis levels of the GD32E235xx series and GD32E230xx series comparators can be configured, but the GD32E235xx series cannot be configured as high hysteresis mode. If the GD32E235xx comparator operates in high hysteresis mode, it will result in functional errors with incorrect comparator results, When porting from GD32E230xx to GD32E235xx, this issue needs to be noted.

Figure 4- 3. The GD32E233xx series comparator can be configured in high hysteresis mode, while the GD32E235xx series comparator cannot be configured in high hysteresis mode

11.4.	1.	Control/status register (CMP_CS)											
		Address of Reset valu	offset: 0x00 alue: 0x0000 0000										
						u (02 b	,						
31	30	29 28	27	26 25	24 	23	22	21	20	19	18	17	16
15	14	13 12	11	10 9	8	7	8	5	4	3	2	1	0
CMPLK	CMPO	CMPHST[1:0]	CMPPL	CMPOSE	L[2:0]	Reserved	c	MPMSEL[2:		CMPI	- M[1:0]	CMPSW	CMPEN
nvo	r	rw/r	rw/r	rwi				rw/r		rv	vír	rw/r	rw/r
Bits 31:16 15		Fields Reserved CMPLK		Descriptions Must be kept at reset value CMP lock									
				This bit could set all control bits of CMP as read-only. This bit is write-once. It of only be cleared by a system reset once It is set by software. 0: CMP_CS[15:0] bits are read-write 1: CMP_CS[15:0] bits are read-only					lt can				
14	CMPO CMP output This is a copy of CMP output state, which is read only. 0: Non-inverting input below inverting input and the output is low												
13:12		CMPHST[1:) [1: Non-inverting input above inverting input and the output is high CMP hysteresis These bits are used to control the hysteresis level. 00: No hysteresis 01: Low hysteresis 10: Medium hysteresis 11: High hysteresis									



5. Revision history

Table 5-1. Revision history

Revision No.	Description	Date		
1.0	Initial Release	Aug.8 2023		
1.1	Explanation of adding differences in the comparator	Aug.5 2024		



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