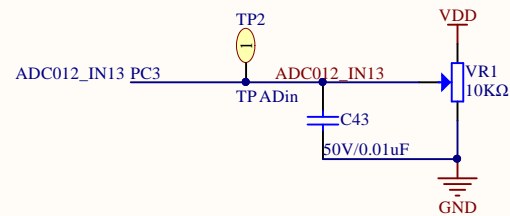


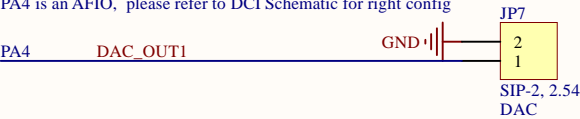
ADC



PC3 is an AFIO, please refer to USB_HS Schematic for right config

DAC

PA4 is an AFIO, please refer to DCI Schematic for right config



Company Name: GigaDevice

File Name: AD_DA

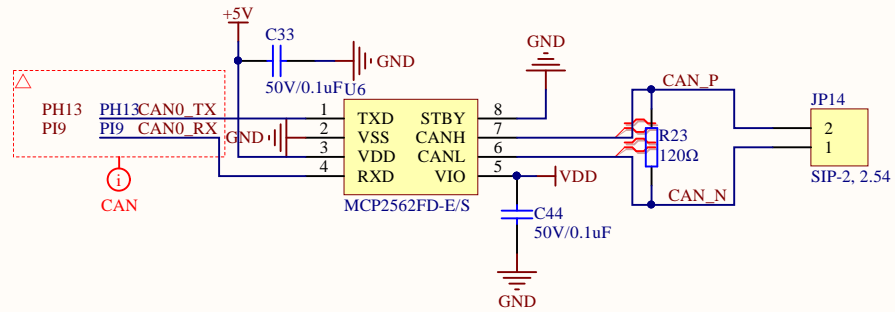
Revision: 1.0

Data: 2023-12

Author: boyajiang

CAN

PH13, PI9 are AFIOs, please refer to TLI schematic for right config



Company Name: GigaDevice

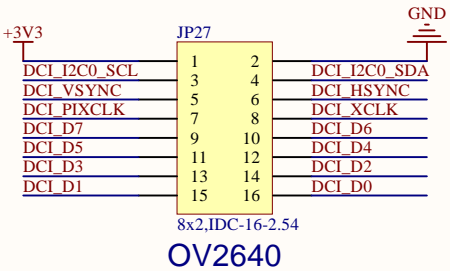
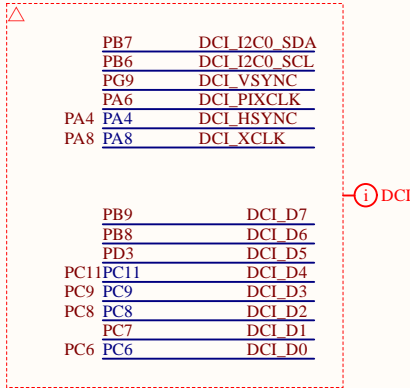
File Name: CAN

Revision: 1.0

Data: 2023-12

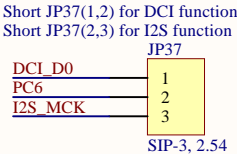
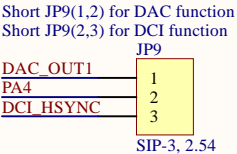
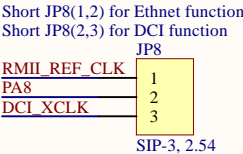
Author: boyajiang

DCI 8bit



DCI_8bit, TLI and SDRAM can be used at the same time

PC8、PC9、PC11 are AFIOs, please refer to SDIO Schematic for right config



Company Name: GigaDevice

File Name: DCI

Revision: 1.0

Data: 2023-12

Author: boyajiang

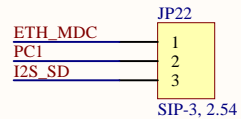
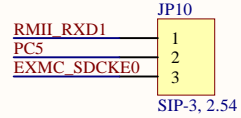
Ethernet

PG11 is an AFIO, please refer to TLI Schematic for right config
PG13 is an AFIO, please refer to SPI Schematic for right config
PG14 is an AFIO, please refer to SPI Schematic for right config

PG11	PG11	RMII_TX_EN
PG13	PG13	RMII_TXD0
PG14	PG14	RMII_TXD1
PC4	PC4	RMII_RXD0
PC5	PC5	RMII_RXD1
PA7	PA7	RMII_CRSDV
PA2	PA2	ETH_MDIO
PC1	PC1	ETH_MDC
PA1	PA1	RMII_REF_CLK

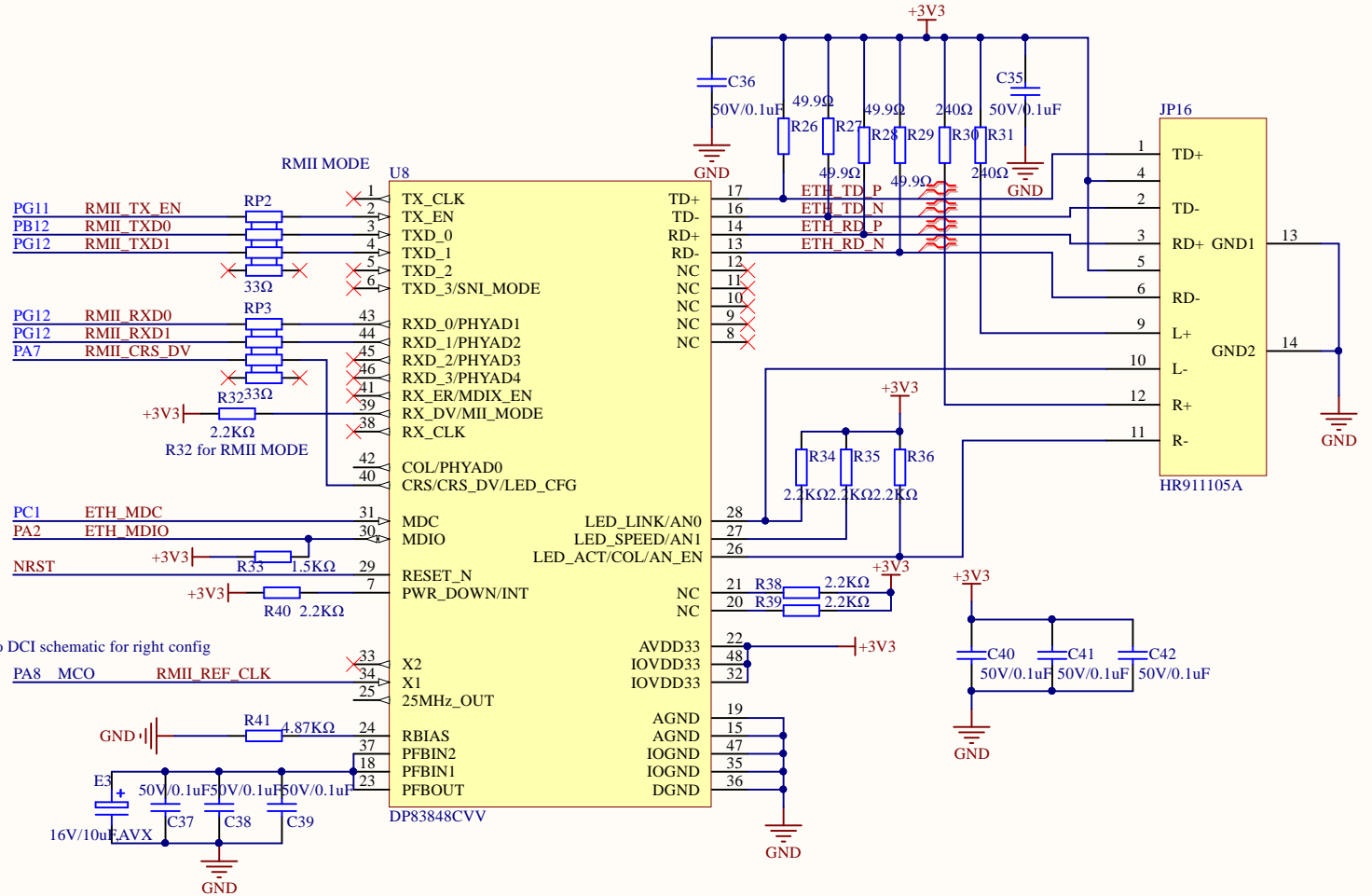
ETHNET

Short JP10(1,2) for Ethernet function
Short JP10(2,3) for SDRAM function



Short JP22(1,2) for Ethernet function
Short JP22(2,3) for I2S function

PA8 is an AFIO, refer to DCI schematic for right config



Company Name: GigaDevice

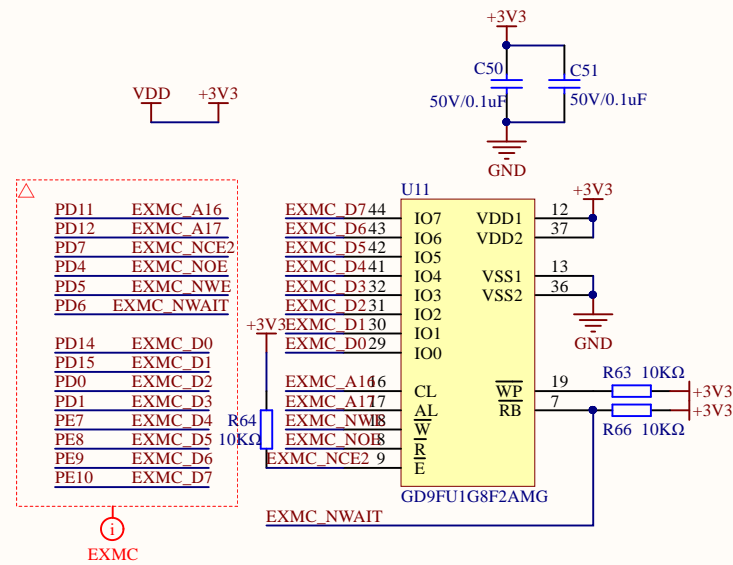
File Name: Ethernet

Revision: 1.0

Date: 2023-12

Author: boyajiang

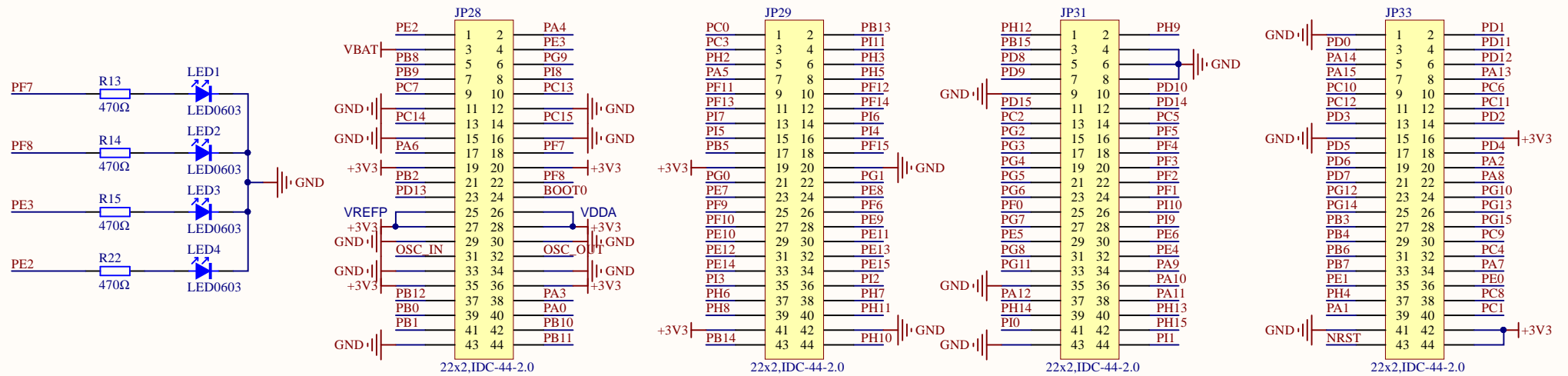
Nand Flash



Company Name: GigaDevice		
File Name: EXMC		
Revision: 1.0	Data: 2023-12	Author: boyajiang

	<div>LED</div> <div></div>		<div>Extension Pin</div> <div></div>	
	<div>KEY</div> <div></div>		<div>Company Name: GigaDevice</div> <div>File Name: Extension</div> <div>Revision: 1.0 Data: 2023-12 Author: boya.jiang</div>	

Extension Pin



LED

Extension Pin

KEY

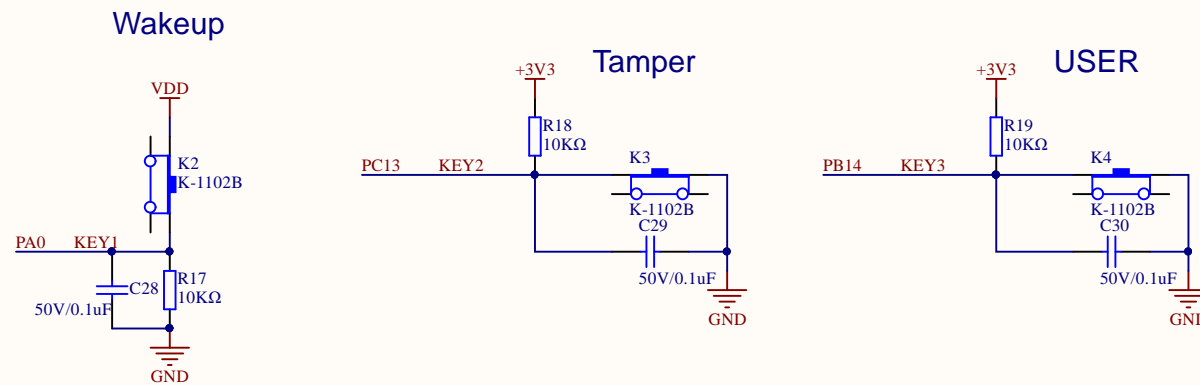
Company Name: GigaDevice

File Name: Extension

Revision: 1.0

Data: 2023-12

Author: boya.jiang



Company Name: GigaDevice

File Name: Extension

Revision: 1.0

Data: 2023-12

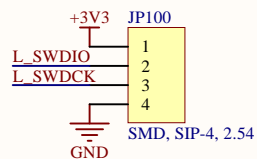
Author: boya.jiang

1

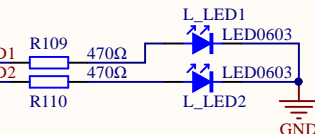
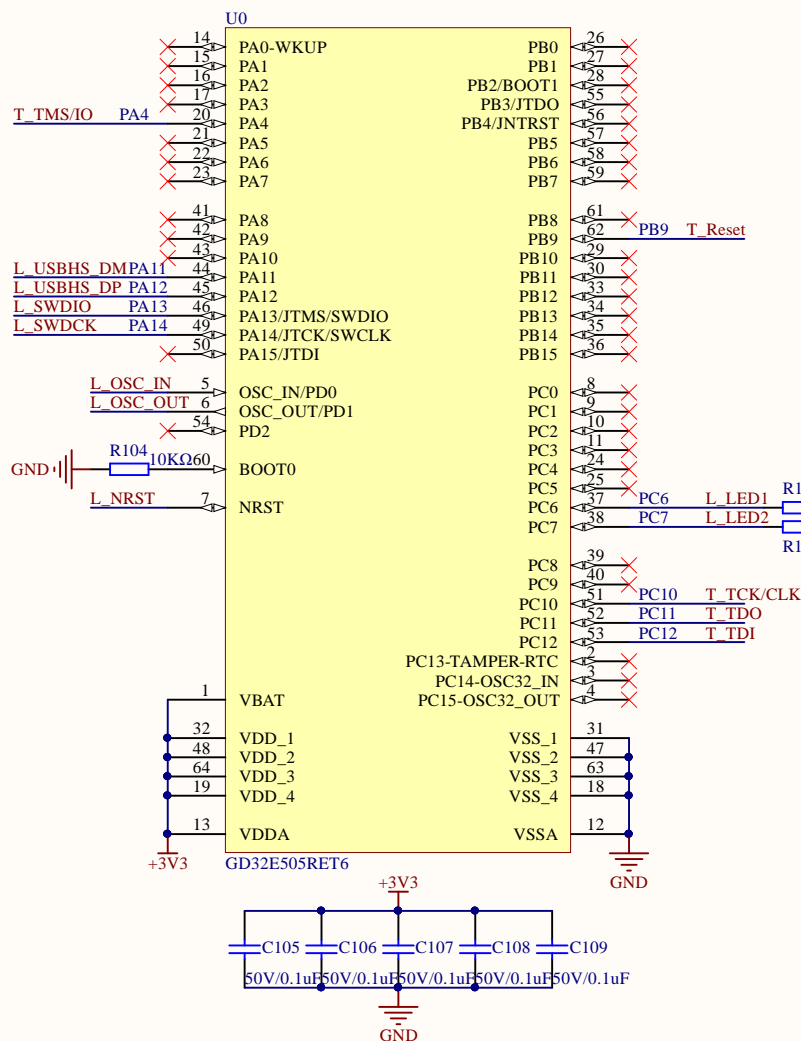
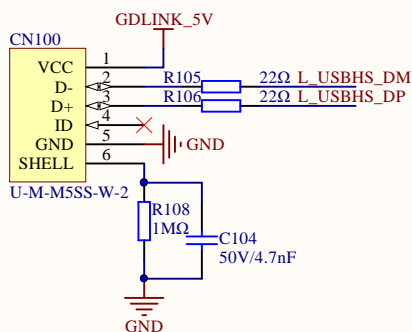
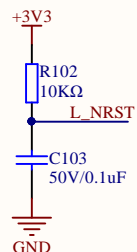
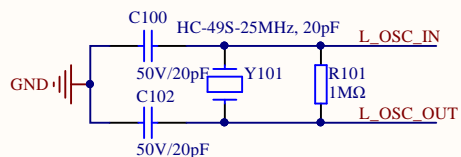
2

3

4



T_TDI	PA15
T_TMS/IO	PA13
T_TCK/CLK	PA14
T_TDO	PB3
T_Reset	NRST



Company Name: GigaDevice

File Name: GD-Link-Onboard

Revision: 4.1

Data: 2023-12

Author: boyajiang

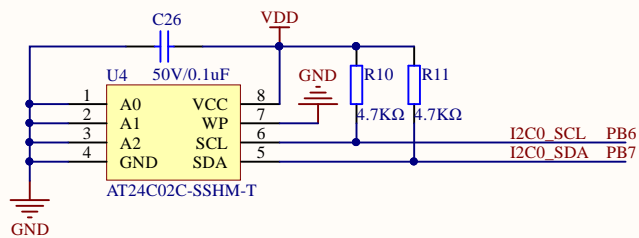
1

2

3

4

I2C



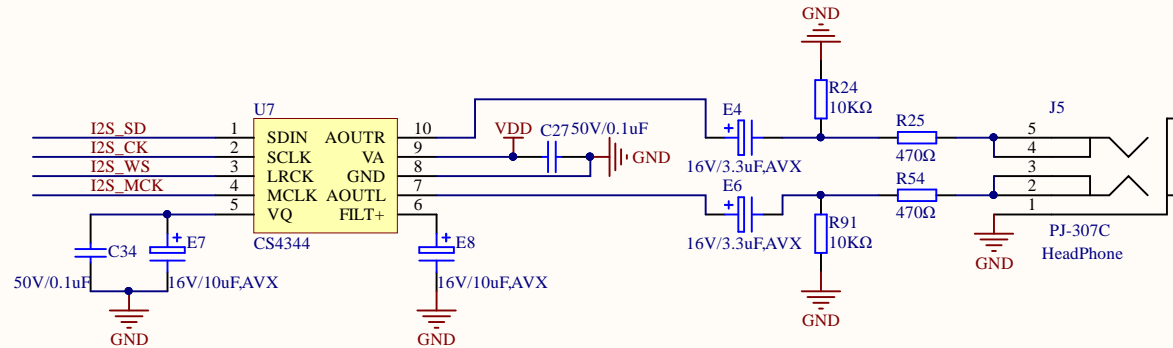
Company Name: GigaDevice		
File Name: I2C		
Revision: 1.0	Data: 2023-12	Author: boya.jiang

I2S

PC1	PC1	I2S_SD
PI1	PI1	I2S_CK
PI0	PI0	I2S_WS
PC6	PC6	I2S_MCK

i
I2S

PC1 is an AFIO, please refer to ETHNET Schematic for right config
PI1 is an AFIO, please refer to TLI Schematic for right config
PI0 is an AFIO, please refer to TLI Schematic for right config
PC6 is an AFIO, please refer to DC1 Schematic for right config



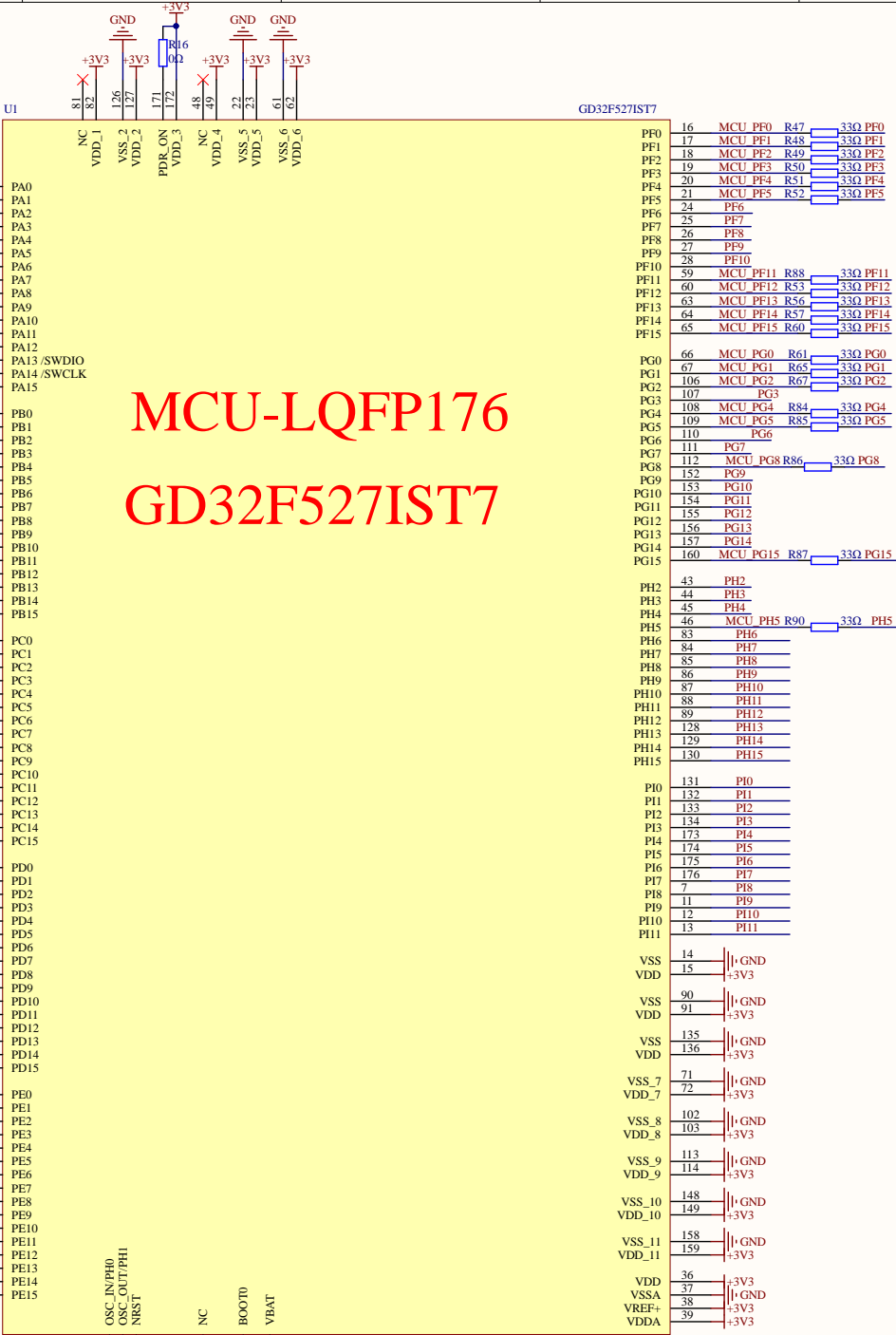
Company Name: GigaDevice

File Name: I2S

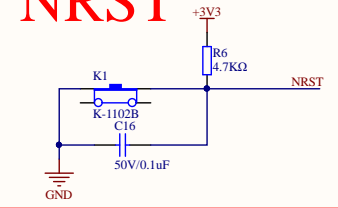
Revision: 1.0

Data: 2023-12

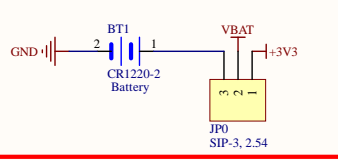
Author: boyajiang



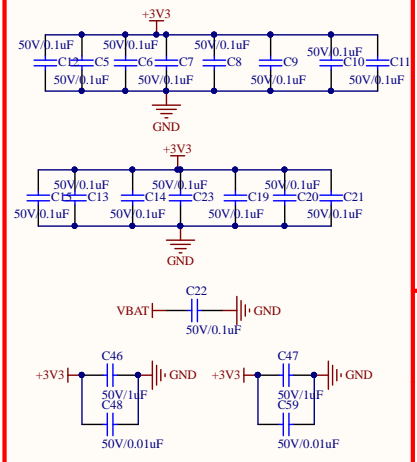
NRST



Vbat select

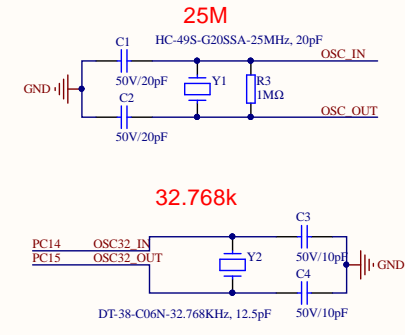


Filter

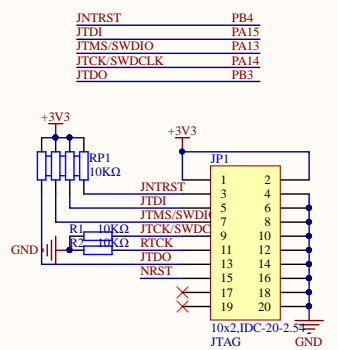


MCU_PC2	MCU_PFO
MCU_PD0	MCU_PFI
MCU_PD1	MCU_PFI2
MCU_PD8	MCU_PFI3
MCU_PD9	MCU_PFI4
MCU_PD14	MCU_PFI5
MCU_PD15	MCU_PFI12
MCU_PE7	MCU_PFI13
MCU_PE8	MCU_PFI14
MCU_PE9	MCU_PFI15
MCU_PE10	MCU_PG0
MCU_PE11	MCU_PG1
MCU_PG2	MCU_PG4
MCU_PG4	MCU_PG5
MCU_PG8	MCU_PG15
MCU_PHS	MCU_PHS

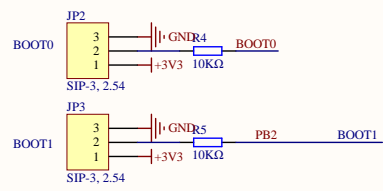
HXTAL&LXTAL



JTAG/SWD



BOOT



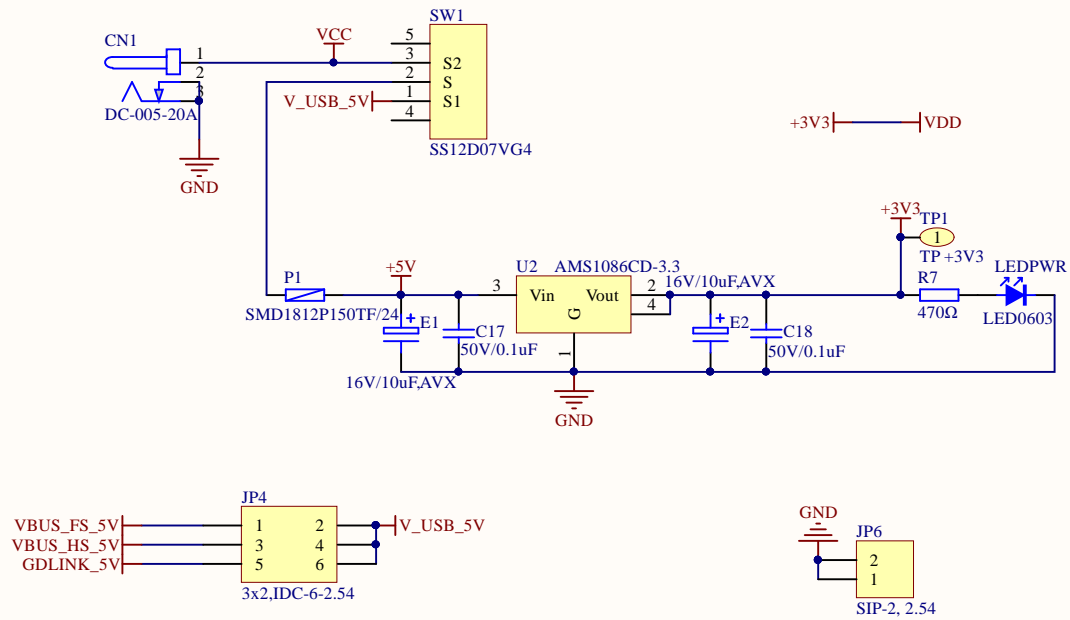
NRST	OSC_IN	OSC_OUT	BOOT0	BOOT1
PC14	PC15	PC15	PC14	PC15

Company Name: GigaDevice

File Name: MCU

Revision: 1.0 Date: 2023-12 Author: boyajiang

POWER



USB Power Supply selector

Company Name: GigaDevice

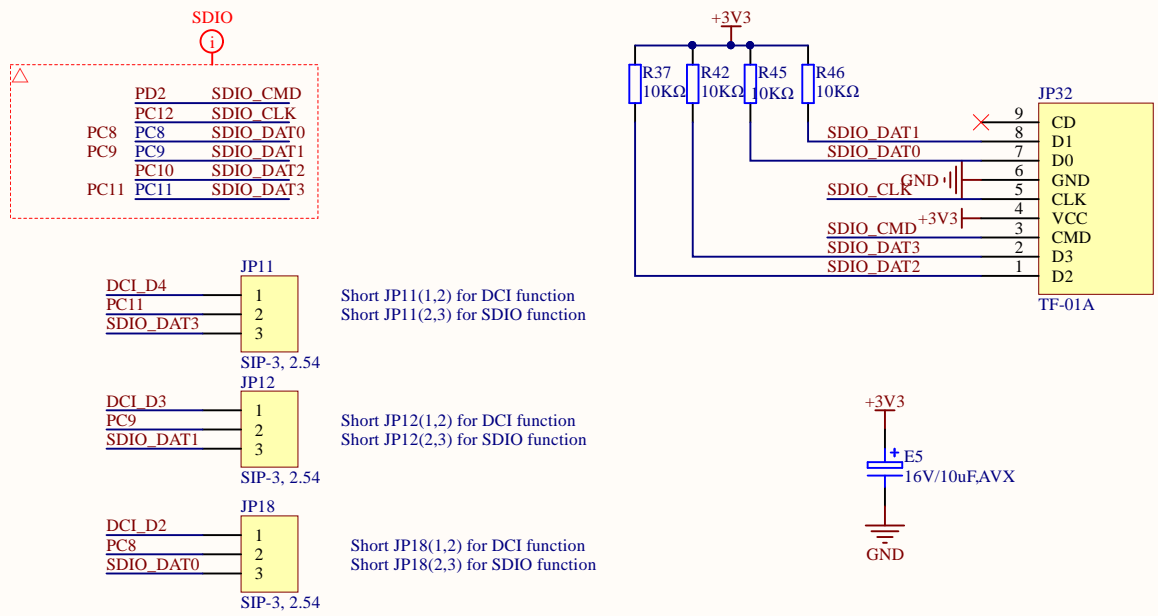
File Name: Power

Revision: 1.0

Data: 2023-12

Author: boyajiang

SDIO



Company Name: GigaDevice

File Name: SDIO

Revision: 1.0

Data: 2023-12

Author: boyajiang

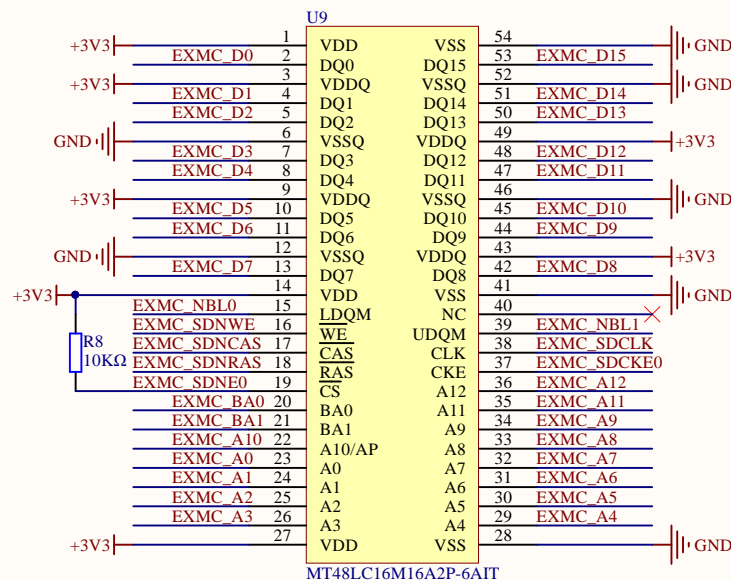
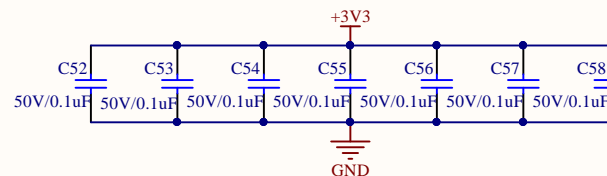
PF0 EXMC_A0
PF1 EXMC_A1
PF2 EXMC_A2
PF3 EXMC_A3
PF4 EXMC_A4
PF5 EXMC_A5
PF12 EXMC_A6
PF13 EXMC_A7
PF14 EXMC_A8
PF15 EXMC_A9
PG0 EXMC_A10
PG1 EXMC_A11
PG2 EXMC_A12

PD14 EXMC_D0
PD15 EXMC_D1
PD0 EXMC_D2
PD1 EXMC_D3
PE7 EXMC_D4
PE8 EXMC_D5
PE9 EXMC_D6
PE10 EXMC_D7
PE11 EXMC_D8
PE12 EXMC_D9
PE13 EXMC_D10
PE14 EXMC_D11
PE15 EXMC_D12
PD8 EXMC_D13
PD9 EXMC_D14
PD10 EXMC_D15

PC5 EXMC_NBL0
PE0 EXMC_NBL1
PE1 EXMC_NBL1
PC5 EXMC_SDCKE0
PG4 EXMC_BA0
PG5 EXMC_BA1
PG8 EXMC_SDCLK
PG15 EXMC_SDNCAS
PF11 EXMC_SDNRAS
PC2 EXMC_SDNE0
PH5 EXMC_SDNWE

PC5 is AFIO, please refer to ETHNET schematic for right config

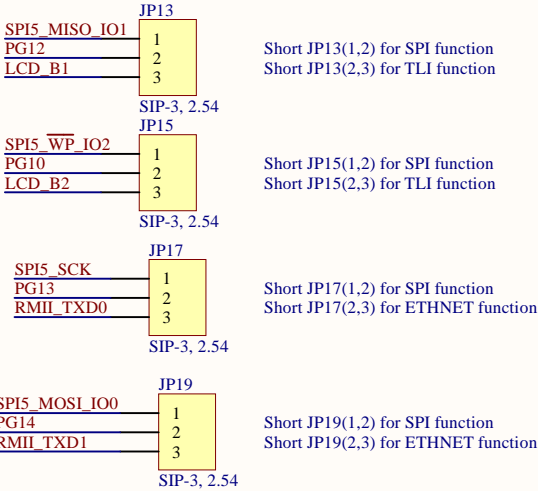
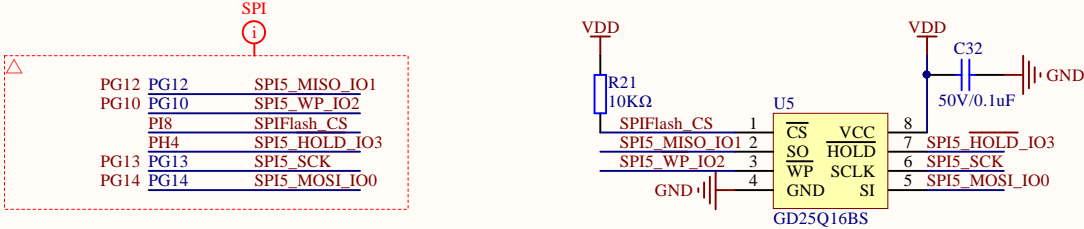
SDRAM



DCI_8bit, TLI and SDRAM can be used at the same time

Company Name: GigaDevice		
File Name: SDRAM		
Revision: 1.0	Data: 2023-12	Author: boyajiang

Standard & Quad SPI Flash



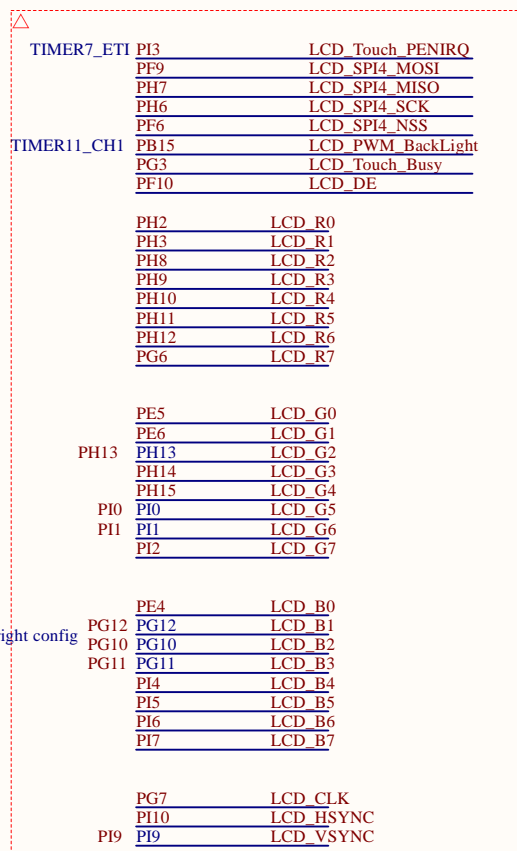
Company Name: GigaDevice

File Name: SPI

Revision: 1.0

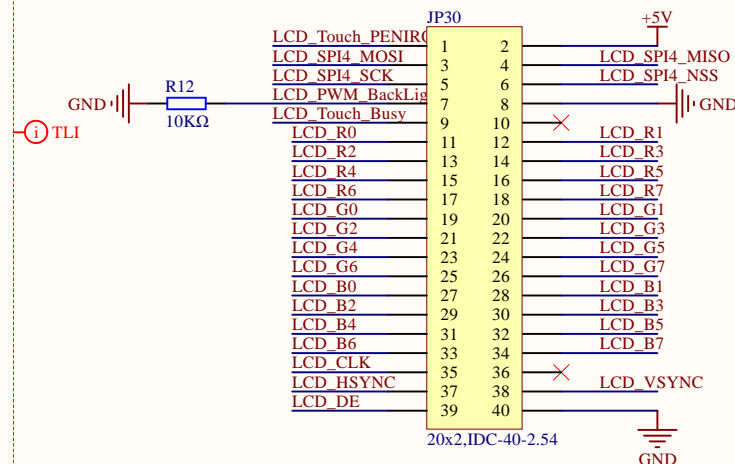
Data: 2023-12

Author: boyajiang



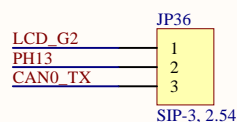
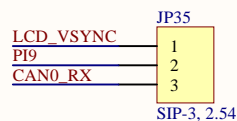
PG10、PG12 are AFIOs, please refer to SPI Schematic for right config

TLI

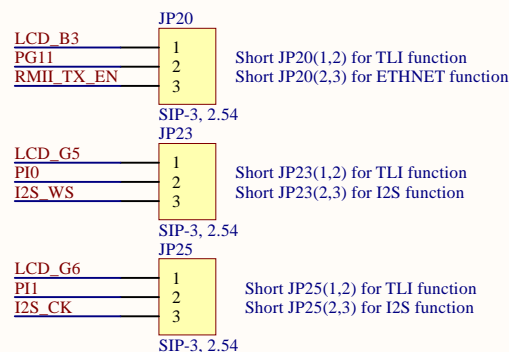


DCI_8bit, TLI and SDRAM can be used at the same time

Short JP35(1,2) for TLI function
Short JP35(2,3) for CAN function



Short JP36(1,2) for TLI function
Short JP36(2,3) for CAN function



Short JP20(1,2) for TLI function
Short JP20(2,3) for ETHNET function

Short JP23(1,2) for TLI function
Short JP23(2,3) for I2S function

Short JP25(1,2) for TLI function
Short JP25(2,3) for I2S function

Company Name: GigaDevice

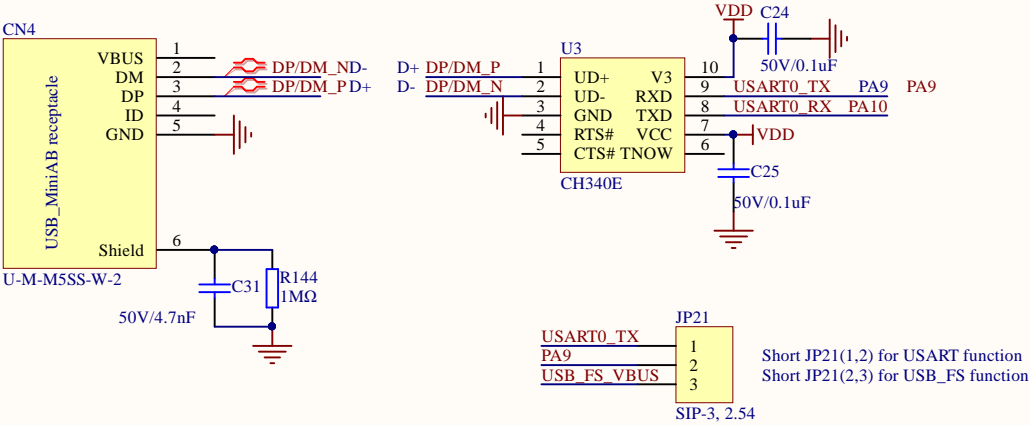
File Name: TLI

Revision: 1.0

Data: 2023-12

Author: boyajiang

USART0 To USB



Company Name: GigaDevice

File Name: USART

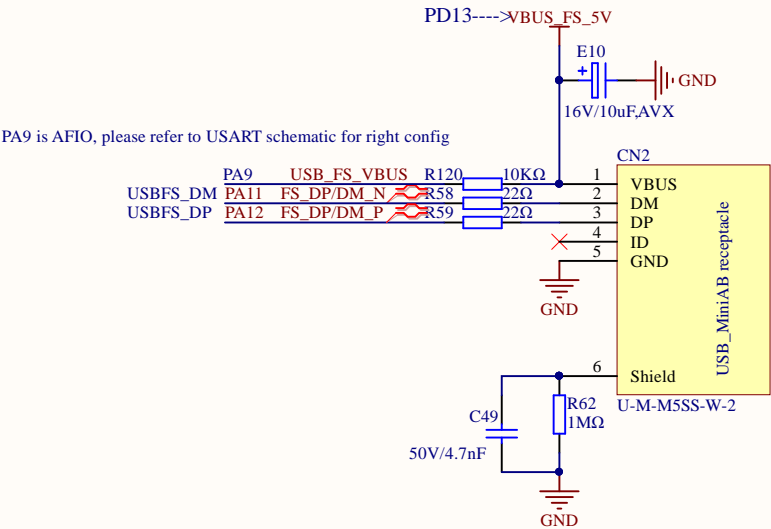
Revision: 1.0

Data: 2023-12

Author: boya.jiang

USB_FS

"VBUS_FS_5V control (active HIGH) " see USB_HS schematic



Company Name: GigaDevice

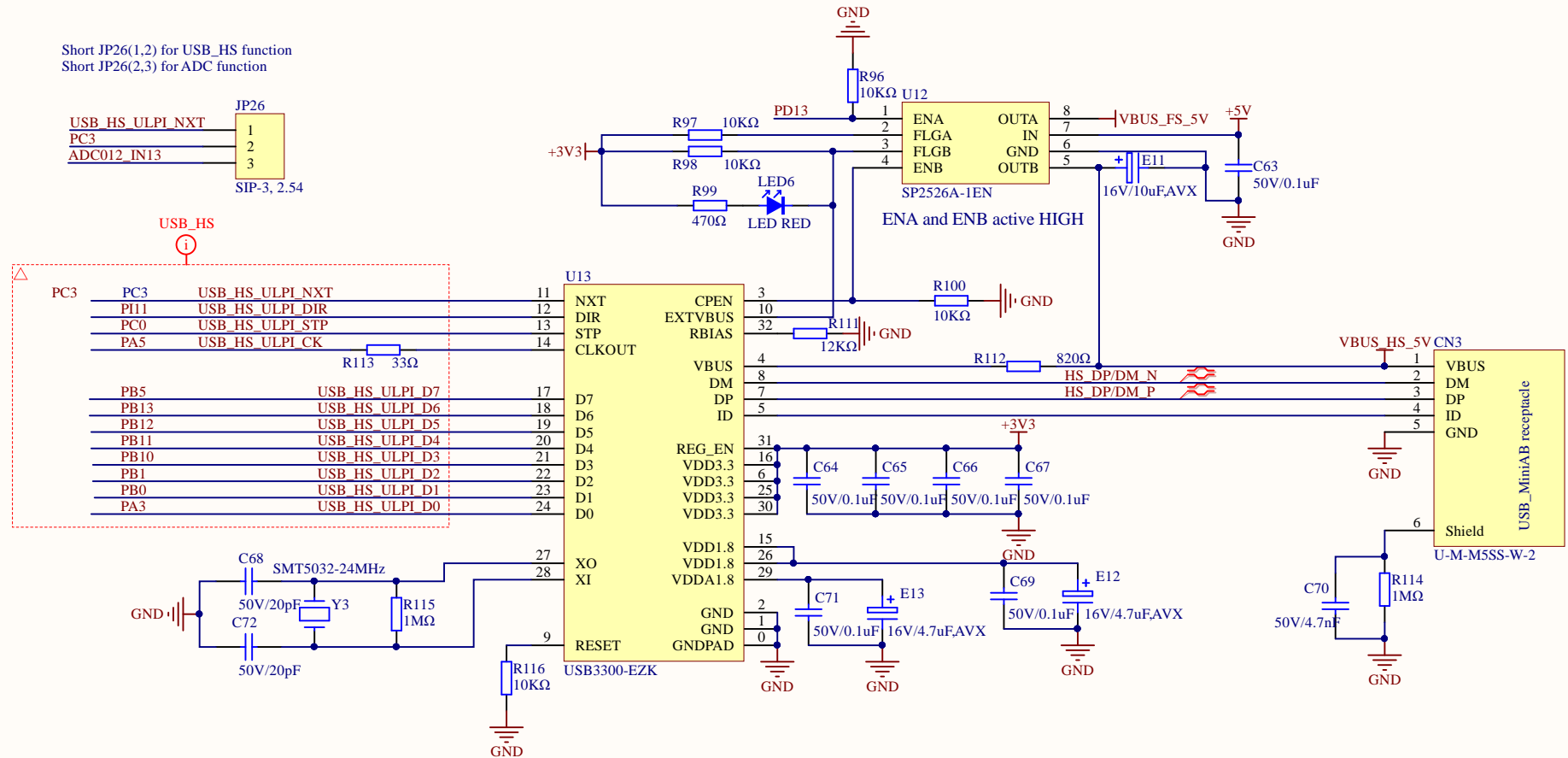
File Name: USB_FS

Revision: 1.0

Data: 2023-12

Author: boyajiang

USB_HS_ULPI



Company Name: GigaDevice

File Name: USB_HS

Revision: 1.0

Data: 2023-12

Author: boya.jiang

