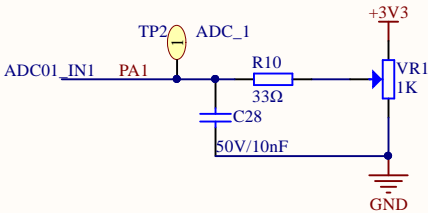
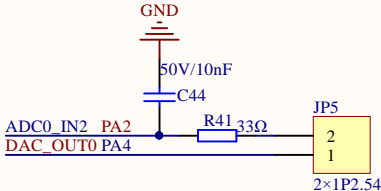


ADC



DAC



PA4 is AFIO, please refer to EXMC schematic for right config

Company Name: GigaDevice

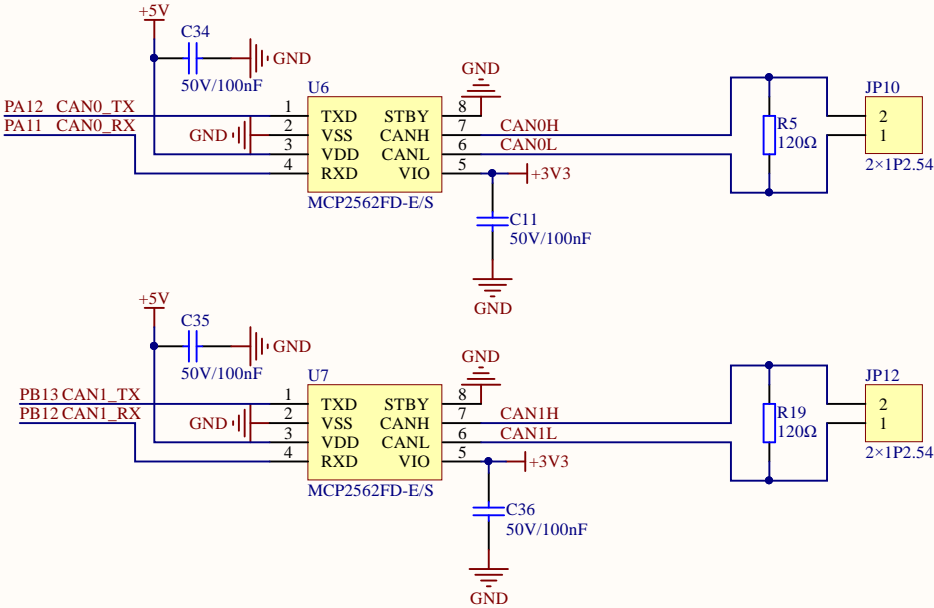
File Name: AD_DA

Revision: 1.0

Data: 2024-1

Author: sijia.wang

CAN



Company Name: GigaDevice

File Name: CAN

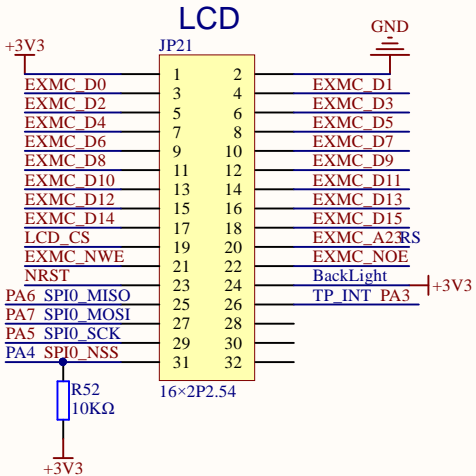
Revision: 1.0

Data: 2024-1

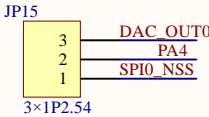
Author: sijia.wang

PD14 EXMC_D0
PD15 EXMC_D1
PD0 EXMC_D2
PD1 EXMC_D3
PE7 EXMC_D4
PE8 EXMC_D5
PE9 EXMC_D6
PE10 EXMC_D7
PE11 EXMC_D8
PE12 EXMC_D9
PE13 EXMC_D10
PE14 EXMC_D11
PE15 EXMC_D12
PD8 EXMC_D13
PD9 EXMC_D14
PD10 EXMC_D15

PE2 EXMC_A23
PD4 EXMC_NOE
PC2 EXMC_NWE
PC4 EXMC_NE0 LCD_CS



Short JP15(1,2) for SPI0 function
Short JP15(2,3) for DAC function



Company Name: GigaDevice

File Name: EXMC

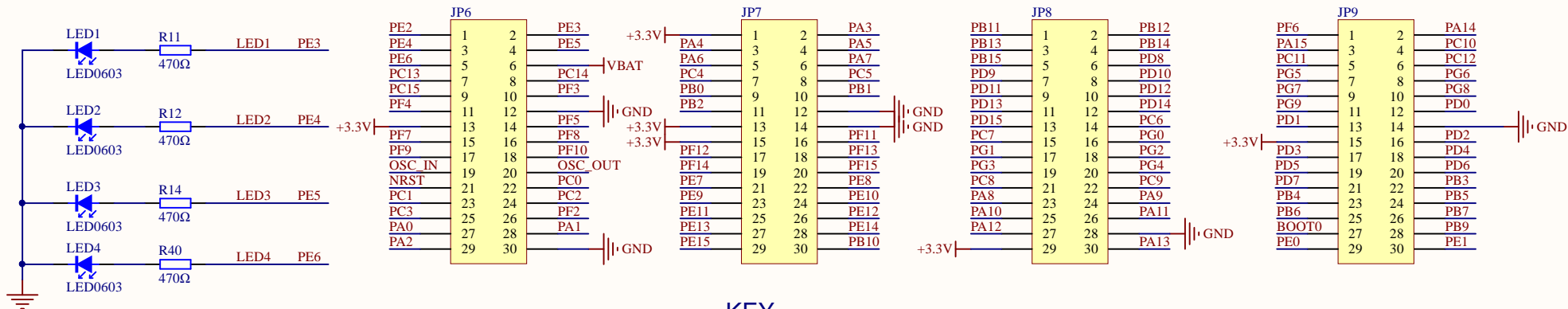
Revision: 1.0

Data: 2024-1

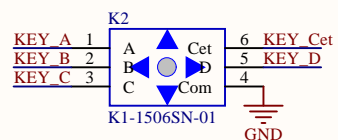
Author: [sijia.wang](#)

LED

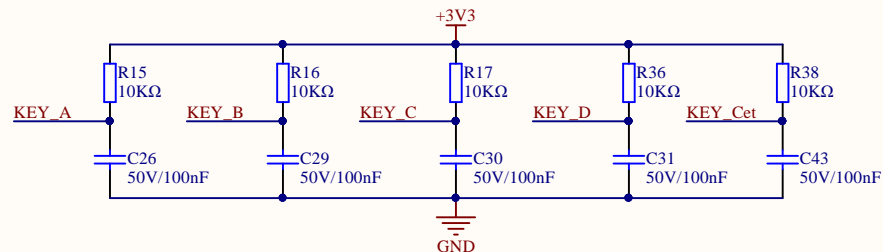
Extension Pin



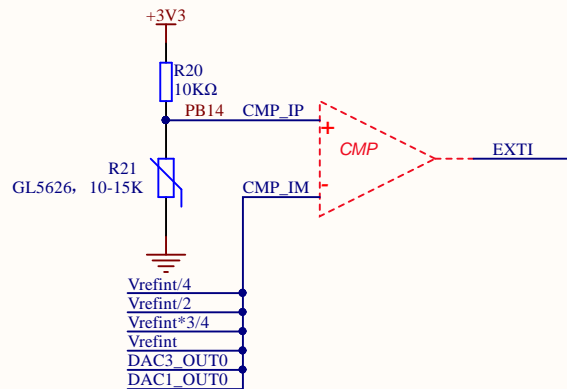
KEY



PA0 KEY_A
PC13 KEY_B
PF10 KEY_C
PF9 KEY_D
PF7 KEY_Cet



CMP



Company Name: GigaDevice

File Name: Extension

Revision: 1.0

Data: 2024-1

Author: sijia.wang

Company Name: GigaDevice

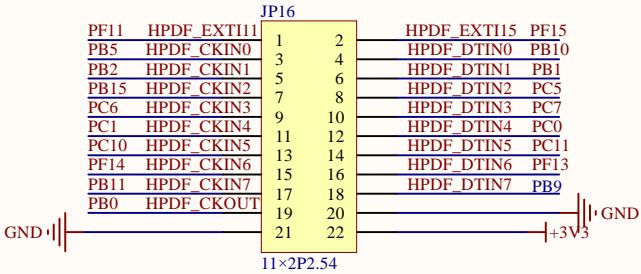
File Name: GD-Link-Onboard

Revision: 4.1

Data: 2024-1

Author: sijia.wang

HPDF



PB9 is AFIO, please refer to IFRP schematic for right config

Company Name: GigaDevice

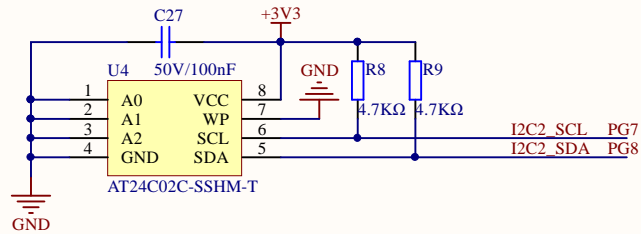
File Name: HPDF

Revision: 1.0

Data: 2024-1

Author: [sijia.wang](#)

I2C



Company Name: GigaDevice		
File Name: I2C		
Revision: 1.0	Data: 2024-1	Author: sijia.wang

1

2

3

4

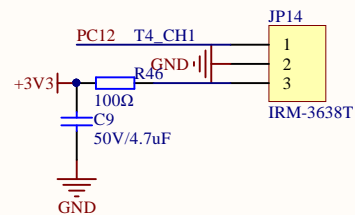
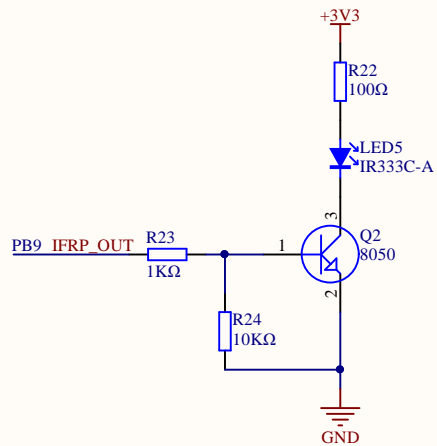
A

B

C

D

Short JP17(1,2) for IFRP function
Short JP17(2,3) for HPDF function
JP17



Company Name: GigaDevice

File Name: IFRP

Revision: 1.0

Data: 2024-1

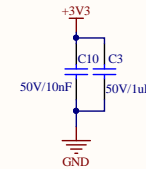
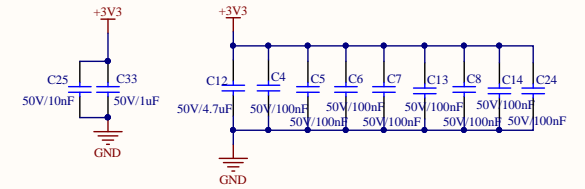
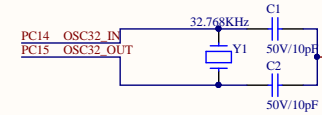
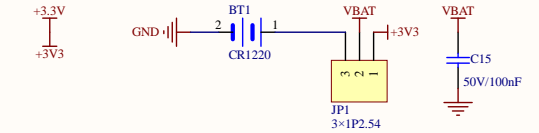
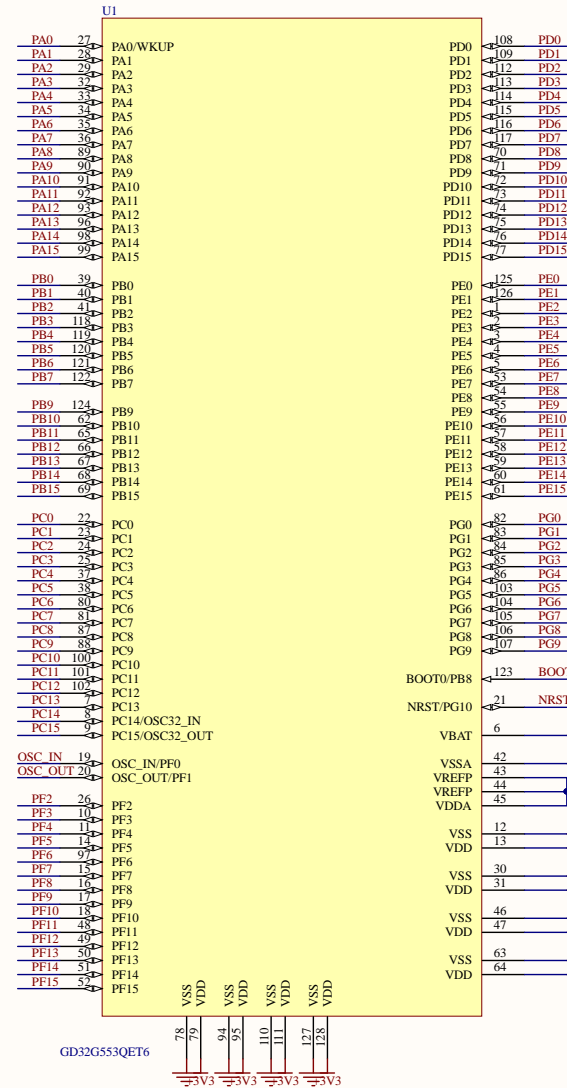
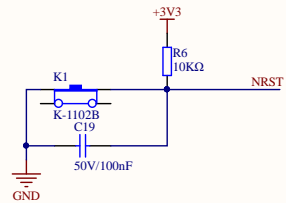
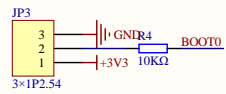
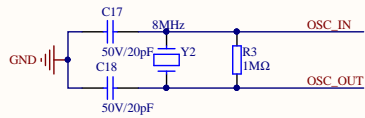
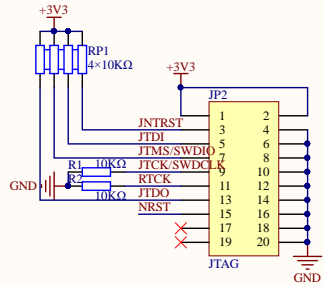
Author: sijia.wang

1

2

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4



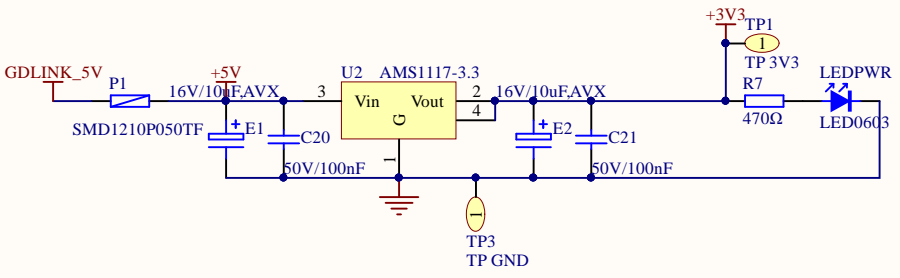
Company Name: GigaDevice

File Name: MCU

Revision: 1.0

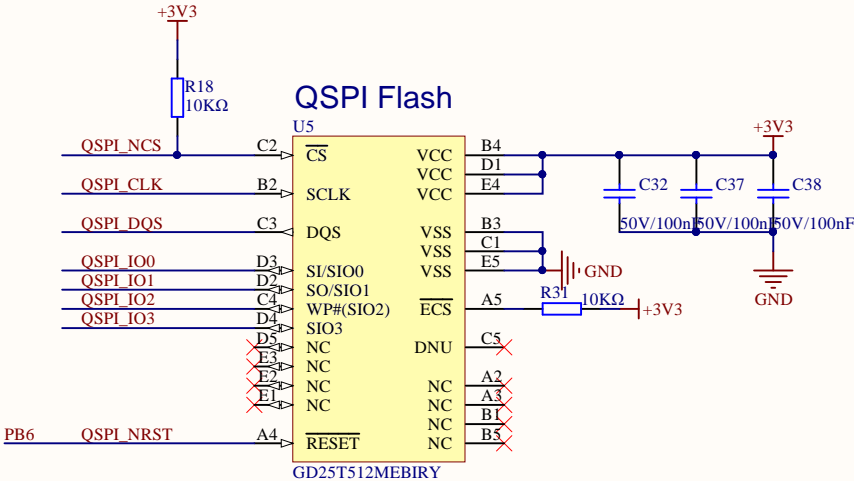
Data: 2024-1

Author: sijia.wang



Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2024-1	Author: sijia.wang

QSPI Flash



QSPI_NCS			PD3
QSPI_CLK	R26	33Ω	PD2
QSPI_IO0	R27	33Ω	PF8
QSPI_IO1	R28	33Ω	PD5
QSPI_IO2	R29	33Ω	PD6
QSPI_IO3	R30	33Ω	PD7
QSPI_DQS	R32	33Ω	PB7

Company Name: GigaDevice

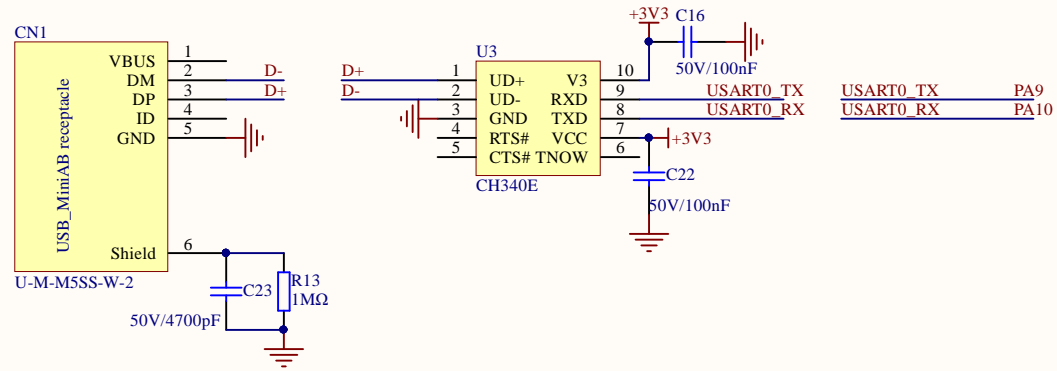
File Name: QSPI

Revision: 1.0

Data: 2024-1

Author: sijia.wang

USART To USB



Company Name: GigaDevice

File Name: USART

Revision: 1.0

Data: 2024-1

Author: sijia.wang

